

図番 AZ21482

記録

A

B

C

D

E

F

A

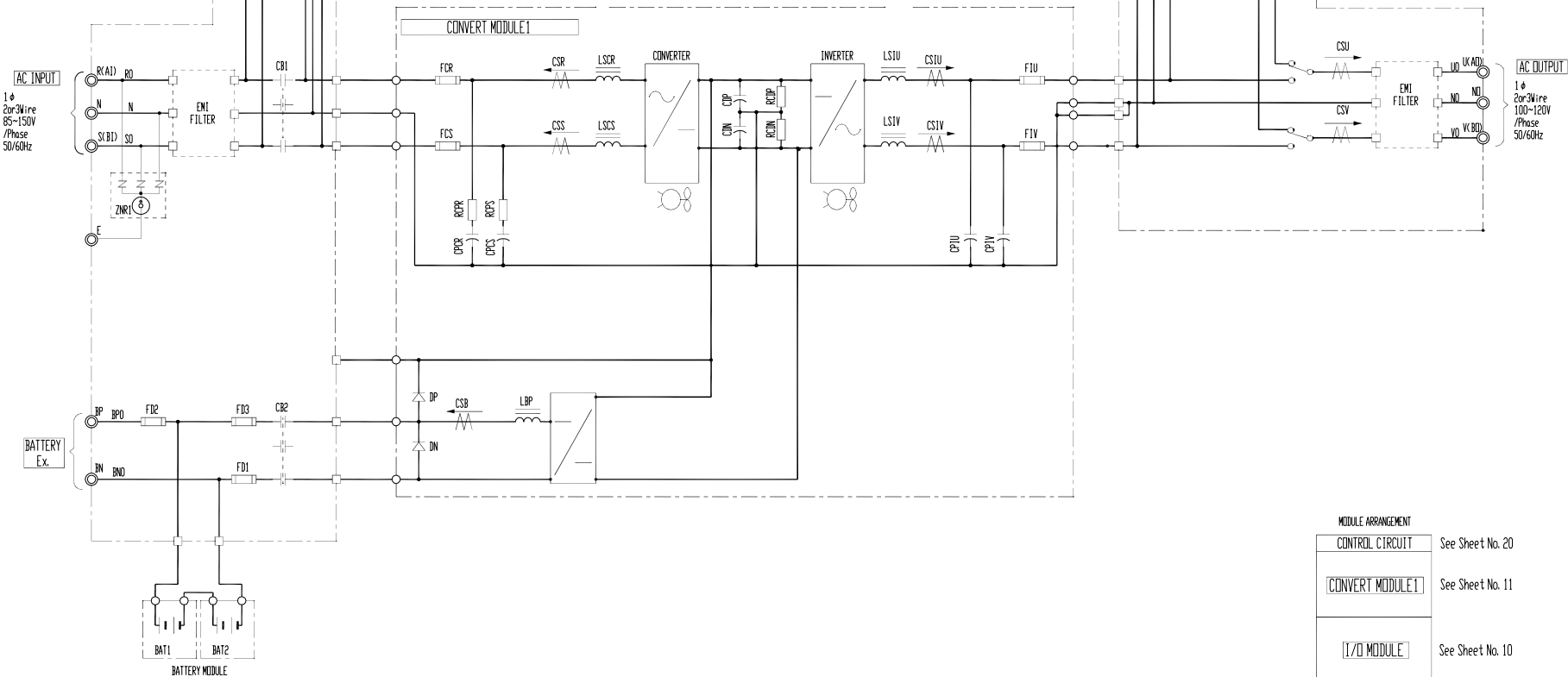
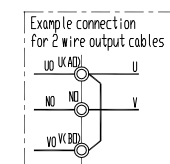
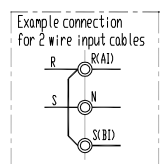
B

C

D

E

F



MODULE ARRANGEMENT	
CONTROL CIRCUIT	See Sheet No. 20
CONVERT MODULE1	See Sheet No. 11
I/O MODULE	See Sheet No. 10
BAT1 BAT2	See Sheet No. 10

Win版

CAD

出図先

UP

UP8校

調達経由

三友通商

1

控

計



MITSUBISHI
ELECTRIC

日付

FEB. 28. '01

作成・照査

K. Miyano

図面

設計・検認

校核

SYSTEM
CONFIGURATION
AZ21482

SHEET NO.

E01

6kVA

PAGE NO.

02