



GE Industrial Systems

N60 Network Stability and Synchrophasor Measurement System UR Series Instruction Manual

N60 Revision: 4.9x

Manual P/N: 1601-0125-**M2** (GEK-113212A)

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ADDENDUM

This Addendum contains information that relates to the N60 Network Stability and Synchrophasor Measurement System relay, version 4.9x. This addendum lists a number of information items that appear in the instruction manual GEK-113212A (revision **M2**) but are not included in the current N60 operations.

The following functions/items are not yet available with the current version of the N60 relay:

- N/A

Version 4.0x and higher releases of the N60 relay includes new hardware (CPU and CT/VT modules).

- The new CPU modules are specified with the following order codes: 9E, 9G, 9H, 9J, 9K, 9L, 9M, 9N, 9P, and 9R.
- The new CT/VT modules are specified with the following order codes: 8F, 8G, 8H, 8J.

The following table maps the relationship between the old CPU and CT/VT modules to the newer versions:

MODULE	OLD	NEW	DESCRIPTION
CPU	9A	9E	RS485 and RS485 (Modbus RTU, DNP)
	9C	9G	RS485 and 10Base-F (Ethernet, Modbus TCP/IP, DNP)
	9D	9H	RS485 and redundant 10Base-F (Ethernet, Modbus TCP/IP, DNP)
	--	9J	RS485 and multi-mode ST 100Base-FX
	--	9K	RS485 and multi-mode ST redundant 100Base-FX
	--	9L	RS485 and single mode SC 100Base-FX
	--	9M	RS485 and single mode SC redundant 100Base-FX
	--	9N	RS485 and 10/100Base-T
	--	9P	RS485 and single mode ST 100Base-FX
	--	9R	RS485 and single mode ST redundant 100Base-FX
CT/VT	8A	8F	Standard 4CT/4VT
	8B	8G	Sensitive Ground 4CT/4VT
	8C	8H	Standard 8CT
	8D	8J	Sensitive Ground 8CT/8VT

The new CT/VT modules can only be used with the new CPUs (9E, 9G, 9H, 9J, 9K, 9L, 9M, 9N, 9P, 9R), and the old CT/VT modules can only be used with the old CPU modules (9A, 9C, 9D). To prevent any hardware mismatches, the new CPU and CT/VT modules have blue labels and a warning sticker stating “**Attn.: Ensure CPU and DSP module label colors are the same!**”. In the event that there is a mismatch between the CPU and CT/VT module, the relay will not function and a **DSP ERROR** or **HARDWARE MISMATCH** error will be displayed.

All other input/output modules are compatible with the new hardware.

With respect to the firmware, firmware versions 4.0x and higher are only compatible with the new CPU and CT/VT modules. Previous versions of the firmware (3.4x and earlier) are only compatible with the older CPU and CT/VT modules.

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Please read this chapter to help guide you through the initial setup of your new relay.

1.1.1 CAUTIONS AND WARNINGS



WARNING



CAUTION

Before attempting to install or use the relay, it is imperative that all **WARNINGS** and **CAUTIONS** in this manual are reviewed to help prevent personal injury, equipment damage, and/or downtime.

1.1.2 INSPECTION CHECKLIST

- Open the relay packaging and inspect the unit for physical damage.
- View the rear nameplate and verify that the correct model has been ordered.

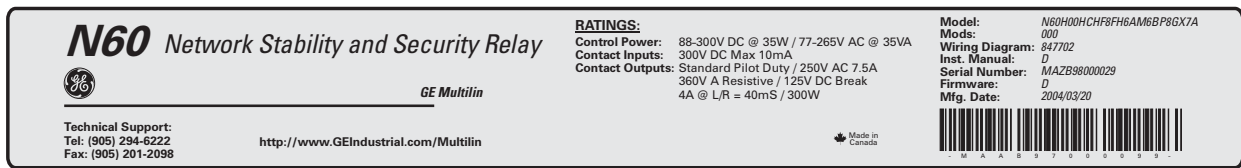


Figure 1-1: REAR NAMEPLATE (EXAMPLE)

- Ensure that the following items are included:
 - Instruction Manual
 - GE enerVista CD (includes the EnerVista UR Setup software and manuals in PDF format)
 - mounting screws
 - registration card (attached as the last page of the manual)
- Fill out the registration form and return to GE Multilin (include the serial number located on the rear nameplate).
- For product information, instruction manual updates, and the latest software updates, please visit the GE Multilin web-site at <http://www.GEindustrial.com/multilin>.



NOTE

If there is any noticeable physical damage, or any of the contents listed are missing, please contact GE Multilin immediately.

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1.2.1 INTRODUCTION TO THE UR

Historically, substation protection, control, and metering functions were performed with electromechanical equipment. This first generation of equipment was gradually replaced by analog electronic equipment, most of which emulated the single-function approach of their electromechanical precursors. Both of these technologies required expensive cabling and auxiliary equipment to produce functioning systems.

Recently, digital electronic equipment has begun to provide protection, control, and metering functions. Initially, this equipment was either single function or had very limited multi-function capability, and did not significantly reduce the cabling and auxiliary equipment required. However, recent digital relays have become quite multi-functional, reducing cabling and auxiliaries significantly. These devices also transfer data to central control facilities and Human Machine Interfaces using electronic communications. The functions performed by these products have become so broad that many users now prefer the term IED (Intelligent Electronic Device).

It is obvious to station designers that the amount of cabling and auxiliary equipment installed in stations can be even further reduced, to 20% to 70% of the levels common in 1990, to achieve large cost reductions. This requires placing even more functions within the IEDs.

Users of power equipment are also interested in reducing cost by improving power quality and personnel productivity, and as always, in increasing system reliability and efficiency. These objectives are realized through software which is used to perform functions at both the station and supervisory levels. The use of these systems is growing rapidly.

High speed communications are required to meet the data transfer rates required by modern automatic control and monitoring systems. In the near future, very high speed communications will be required to perform protection signaling with a performance target response time for a command signal between two IEDs, from transmission to reception, of less than 3 milliseconds. This has been established by the IEC 61850 standard.

IEDs with the capabilities outlined above will also provide significantly more power system data than is presently available, enhance operations and maintenance, and permit the use of adaptive system configuration for protection and control systems. This new generation of equipment must also be easily incorporated into automation systems, at both the station and enterprise levels. The GE Multilin Universal Relay (UR) has been developed to meet these goals.

a) UR BASIC DESIGN

The UR is a digital-based device containing a central processing unit (CPU) that handles multiple types of input and output signals. The UR can communicate over a local area network (LAN) with an operator interface, a programming device, or another UR device.

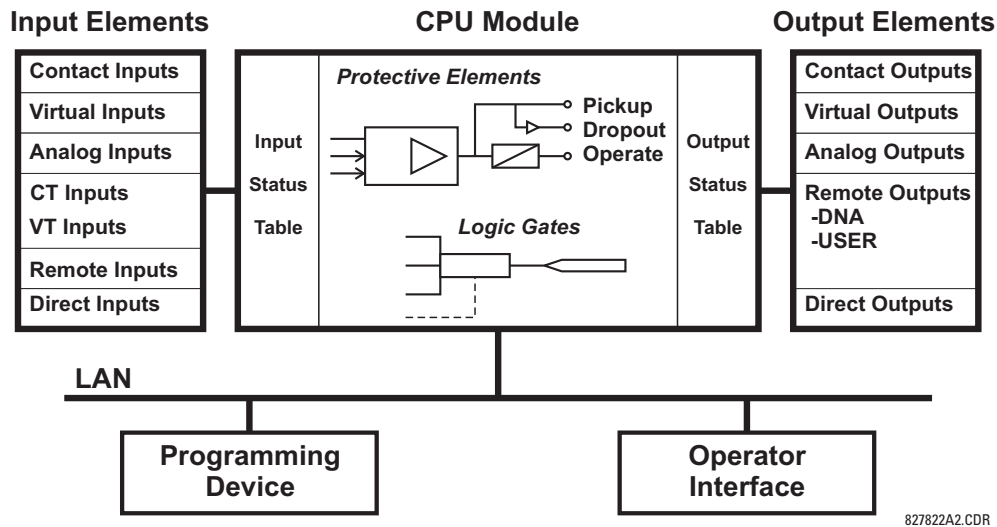


Figure 1-2: UR CONCEPT BLOCK DIAGRAM

The **CPU module** contains firmware that provides protection elements in the form of logic algorithms, as well as programmable logic gates, timers, and latches for control features.

Input elements accept a variety of analog or digital signals from the field. The UR isolates and converts these signals into logic signals used by the relay.

Output elements convert and isolate the logic signals generated by the relay into digital or analog signals that can be used to control field devices.

b) UR SIGNAL TYPES

The **contact inputs and outputs** are digital signals associated with connections to hard-wired contacts. Both 'wet' and 'dry' contacts are supported.

The **virtual inputs and outputs** are digital signals associated with UR-series internal logic signals. Virtual inputs include signals generated by the local user interface. The virtual outputs are outputs of FlexLogic™ equations used to customize the device. Virtual outputs can also serve as virtual inputs to FlexLogic™ equations.

The **analog inputs and outputs** are signals that are associated with transducers, such as Resistance Temperature Detectors (RTDs).

The **CT and VT inputs** refer to analog current transformer and voltage transformer signals used to monitor AC power lines. The UR-series relays support 1 A and 5 A CTs.

The **remote inputs and outputs** provide a means of sharing digital point state information between remote UR-series devices. The remote outputs interface to the remote inputs of other UR-series devices. Remote outputs are FlexLogic™ operands inserted into IEC 61850 GSSE and GOOSE messages.

The **direct inputs and outputs** provide a means of sharing digital point states between a number of UR-series IEDs over a dedicated fiber (single or multimode), RS422, or G.703 interface. No switching equipment is required as the IEDs are connected directly in a ring or redundant (dual) ring configuration. This feature is optimized for speed and intended for pilot-aided schemes, distributed logic applications, or the extension of the input/output capabilities of a single relay chassis.

c) UR SCAN OPERATION

The UR-series devices operate in a cyclic scan fashion. The device reads the inputs into an input status table, solves the logic program (FlexLogic™ equation), and then sets each output to the appropriate state in an output status table. Any resulting task execution is priority interrupt-driven.

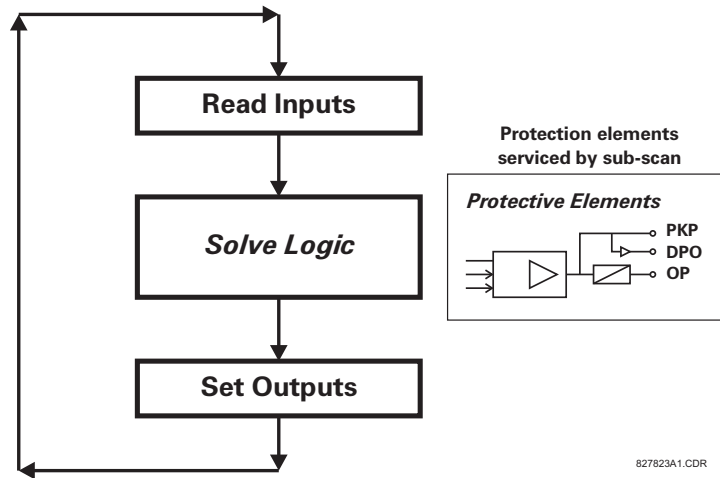


Figure 1–3: UR-SERIES SCAN OPERATION

1.2.3 SOFTWARE ARCHITECTURE

The firmware (software embedded in the relay) is designed in functional modules which can be installed in any relay as required. This is achieved with Object-Oriented Design and Programming (OOD/OOP) techniques.

Object-Oriented techniques involve the use of 'objects' and 'classes'. An 'object' is defined as "a logical entity that contains both data and code that manipulates that data". A 'class' is the generalized form of similar objects. By using this concept, one can create a Protection Class with the Protection Elements as objects of the class such as Time Overcurrent, Instantaneous Overcurrent, Current Differential, Undervoltage, Overvoltage, Underfrequency, and Distance. These objects represent completely self-contained software modules. The same object-class concept can be used for Metering, Input/Output Control, HMI, Communications, or any functional entity in the system.

Employing OOD/OOP in the software architecture of the Universal Relay achieves the same features as the hardware architecture: modularity, scalability, and flexibility. The application software for any Universal Relay (e.g. Feeder Protection, Transformer Protection, Distance Protection) is constructed by combining objects from the various functionality classes. This results in a 'common look and feel' across the entire family of UR-series platform-based applications.

1.2.4 IMPORTANT CONCEPTS

As described above, the architecture of the UR-series relays differ from previous devices. To achieve a general understanding of this device, some sections of Chapter 5 are quite helpful. The most important functions of the relay are contained in "elements". A description of the UR-series elements can be found in the *Introduction to Elements* section in Chapter 5. An example of a simple element, and some of the organization of this manual, can be found in the *Digital Elements* section. A description of how digital signals are used and routed within the relay is contained in the *Introduction to FlexLogic™* section in Chapter 5.

1.3.1 PC REQUIREMENTS

1

The faceplate keypad and display or the EnerVista UR Setup software interface can be used to communicate with the relay. The EnerVista UR Setup software interface is the preferred method to edit settings and view actual values because the PC monitor can display more information in a simple comprehensible format.

The following minimum requirements must be met for the EnerVista UR Setup software to properly operate on a PC.

- Pentium class or higher processor (Pentium II 300 MHz or higher recommended)
- Windows 95, 98, 98SE, ME, NT 4.0 (Service Pack 4 or higher), 2000, XP
- Internet Explorer 4.0 or higher
- 128 MB of RAM (256 MB recommended)
- 200 MB of available space on system drive and 200 MB of available space on installation drive
- Video capable of displaying 800 x 600 or higher in high-color mode (16-bit color)
- RS232 and/or Ethernet port for communications to the relay

The following qualified modems have been tested to be compliant with the N60 and the EnerVista UR Setup software.

- US Robotics external 56K FaxModem 5686
- US Robotics external Sportster 56K X2
- PCTEL 2304WT V.92 MDC internal modem

1.3.2 INSTALLATION

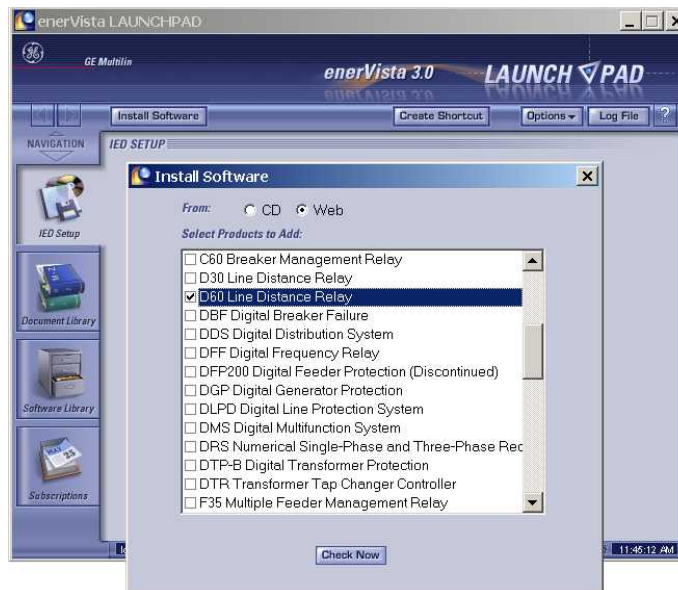
After ensuring the minimum requirements for using EnerVista UR Setup are met (see previous section), use the following procedure to install the EnerVista UR Setup from the enclosed GE enerVista CD.

1. Insert the GE enerVista CD into your CD-ROM drive.
2. Click the **Install Now** button and follow the installation instructions to install the no-charge enerVista software.
3. When installation is complete, start the enerVista Launchpad application.
4. Click the **IED Setup** section of the **Launch Pad** window.

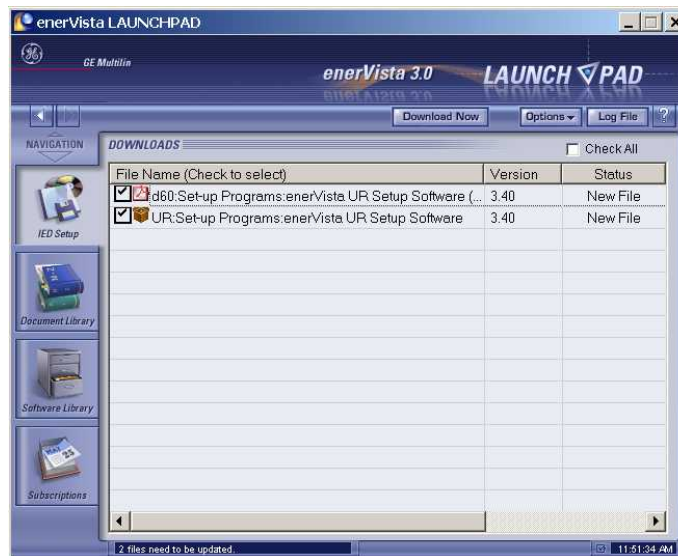


5. In the enerVista Launch Pad window, click the **Install Software** button and select the "N60 Network Stability and Synchrophasor Measurement System" from the Install Software window as shown below. Select the "Web" option to

ensure the most recent software release, or select “CD” if you do not have a web connection, then click the **Check Now** button to list software items for the N60.



6. Select the N60 software program and release notes (if desired) from the list and click the **Download Now** button to obtain the installation program.



7. enerVista Launchpad will obtain the installation program from the Web or CD. Once the download is complete, double-click the installation program to install the EnerVista UR Setup software.
8. Select the complete path, including the new directory name, where the EnerVista UR Setup will be installed.
9. Click on **Next** to begin the installation. The files will be installed in the directory indicated and the installation program will automatically create icons and add EnerVista UR Setup to the Windows start menu.

10. Click **Finish** to end the installation. The N60 device will be added to the list of installed IEDs in the enerVista Launchpad window, as shown below.



1.3.3 CONNECTING ENERVISTA UR SETUP WITH THE N60

This section is intended as a quick start guide to using the EnerVista UR Setup software. Please refer to the EnerVista UR Setup Help File and Chapter 4 of this manual for more information.

a) CONFIGURING AN ETHERNET CONNECTION

Before starting, verify that the Ethernet network cable is properly connected to the Ethernet port on the back of the relay. To setup the relay for Ethernet communications, it will be necessary to define a Site, then add the relay as a Device at that site.

1. Install and start the latest version of the EnerVista UR Setup software (available from the GE enerVista CD or online from <http://www.GEindustrial.com/multilin> (see previous section for installation instructions).
2. Select the "UR" device from the enerVista Launchpad to start EnerVista UR Setup.
3. Click the **Device Setup** button to open the Device Setup window, then click the **Add Site** button to define a new site.
4. Enter the desired site name in the "Site Name" field. If desired, a short description of site can also be entered along with the display order of devices defined for the site. Click the **OK** button when complete.
5. The new site will appear in the upper-left list in the EnerVista UR Setup window. Click on the new site name and then click the **Device Setup** button to re-open the Device Setup window.
6. Click the **Add Device** button to define the new device.
7. Enter the desired name in the "Device Name" field and a description (optional) of the site.
8. Select "Ethernet" from the **Interface** drop-down list. This will display a number of interface parameters that must be entered for proper Ethernet functionality.
 - Enter the relay IP address (from **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **NETWORK** ⇒ **IP ADDRESS**) in the "IP Address" field.
 - Enter the relay Modbus address (from the **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **MODBUS PROTOCOL** ⇒ **MODBUS SLAVE ADDRESS** setting) in the "Slave Address" field.
 - Enter the Modbus port address (from the **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **MODBUS PROTOCOL** ⇒ **MODBUS TCP PORT NUMBER** setting) in the "Modbus Port" field.
9. Click the **Read Order Code** button to connect to the N60 device and upload the order code. If an communications error occurs, ensure that the three EnerVista UR Setup values entered in the previous step correspond to the relay setting values.

10. Click **OK** when the relay order code has been received. The new device will be added to the Site List window (or Online window) located in the top left corner of the main EnerVista UR Setup window.

The Site Device has now been configured for Ethernet communications. Proceed to Section c) below to begin communications.

b) CONFIGURING AN RS232 CONNECTION

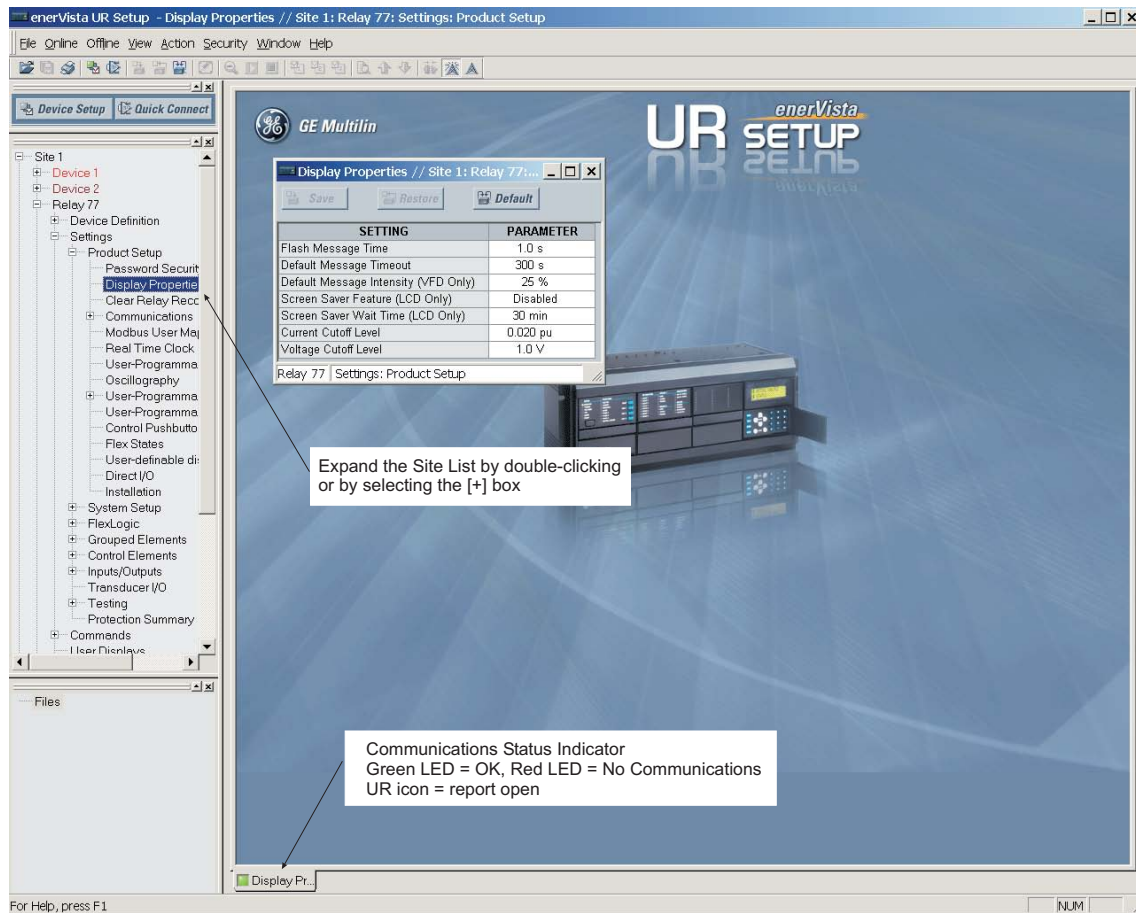
Before starting, verify that the RS232 serial cable is properly connected to the RS232 port on the front panel of the relay.

1. Install and start the latest version of the EnerVista UR Setup software (available from the GE enerVista CD or online from <http://www.GEindustrial.com/multilin>).
2. Select the **Device Setup** button to open the Device Setup window and click the **Add Site** button to define a new site.
3. Enter the desired site name in the "Site Name" field. If desired, a short description of site can also be entered along with the display order of devices defined for the site. Click the **OK** button when complete.
4. The new site will appear in the upper-left list in the EnerVista UR Setup window. Click on the new site name and then click the **Device Setup** button to re-open the Device Setup window.
5. Click the **Add Device** button to define the new device.
6. Enter the desired name in the "Device Name" field and a description (optional) of the site.
7. Select "Serial" from the **Interface** drop-down list. This will display a number of interface parameters that must be entered for proper serial communications.
 - Enter the relay slave address and COM port values (from the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **SERIAL PORTS** menu) in the "Slave Address" and "COM Port" fields.
 - Enter the physical communications parameters (baud rate and parity settings) in their respective fields.
8. Click the **Read Order Code** button to connect to the N60 device and upload the order code. If an communications error occurs, ensure that the EnerVista UR Setup serial communications values entered in the previous step correspond to the relay setting values.
9. Click "OK" when the relay order code has been received. The new device will be added to the Site List window (or Online window) located in the top left corner of the main EnerVista UR Setup window.

The Site Device has now been configured for RS232 communications. Proceed to Section c) Connecting to the Relay below to begin communications.

c) CONNECTING TO THE RELAY

1. Open the Display Properties window through the Site List tree as shown below:



842743A1.CDR

- The Display Properties window will open with a status indicator on the lower left of the EnerVista UR Setup window.
- If the status indicator is red, verify that the Ethernet network cable is properly connected to the Ethernet port on the back of the relay and that the relay has been properly setup for communications (steps A and B earlier).
If a relay icon appears in place of the status indicator, then a report (such as an oscillography or event record) is open. Close the report to re-display the green status indicator.
- The Display Properties settings can now be edited, printed, or changed according to user specifications.



NOTE

Refer to Chapter 4 in this manual and the EnerVista UR Setup Help File for more information about the using the EnerVista UR Setup software interface.

1.4.1 MOUNTING AND WIRING

Please refer to Chapter 3: Hardware for detailed mounting and wiring instructions. Review all **WARNINGS** and **CAUTIONS** carefully.

1.4.2 COMMUNICATIONS

The EnerVista UR Setup software communicates to the relay via the faceplate RS232 port or the rear panel RS485 / Ethernet ports. To communicate via the faceplate RS232 port, a standard “straight-through” serial cable is used. The DB-9 male end is connected to the relay and the DB-9 or DB-25 female end is connected to the PC COM1 or COM2 port as described in the CPU Communications Ports section of Chapter 3.

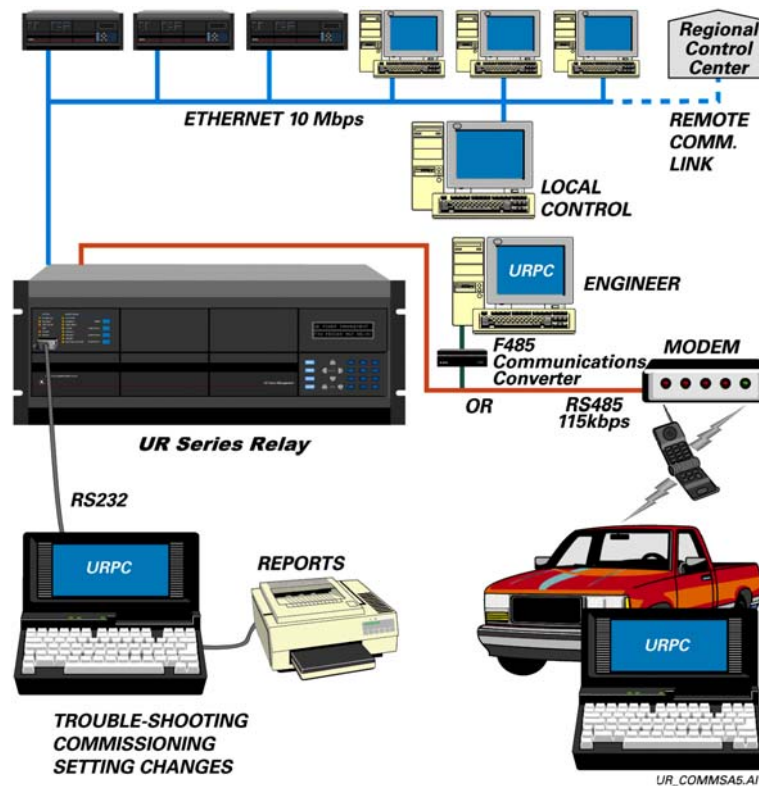


Figure 1-4: RELAY COMMUNICATIONS OPTIONS

To communicate through the N60 rear RS485 port from a PC RS232 port, the GE Multilin RS232/RS485 converter box is required. This device (catalog number F485) connects to the computer using a “straight-through” serial cable. A shielded twisted-pair (20, 22, or 24 AWG) connects the F485 converter to the N60 rear communications port. The converter terminals (+, –, GND) are connected to the N60 communication module (+, –, COM) terminals. Refer to the *CPU Communications Ports* section in Chapter 3 for option details. The line should be terminated with an R-C network (i.e. 120 Ω , 1 nF) as described in the Chapter 3.

1.4.3 FACEPLATE DISPLAY

All messages are displayed on a 2 × 20 character vacuum fluorescent display to make them visible under poor lighting conditions. An optional liquid crystal display (LCD) is also available. Messages are displayed in English and do not require the aid of an instruction manual for deciphering. While the keypad and display are not actively being used, the display will default to defined messages. Any high priority event driven message will automatically override the default message and appear on the display.

1.5.1 FACEPLATE KEYPAD

1

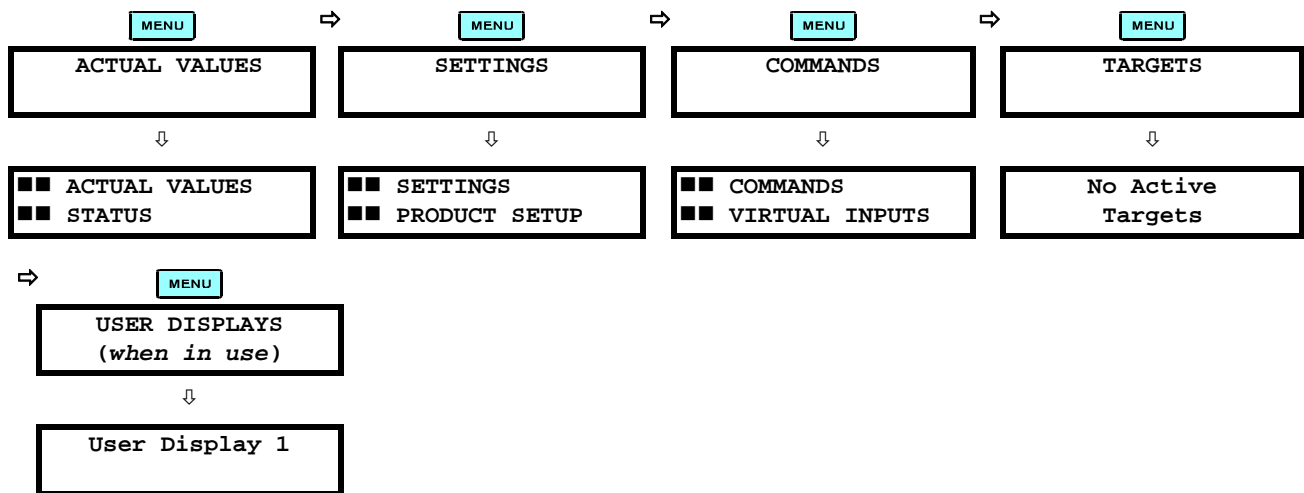
Display messages are organized into 'pages' under the following headings: Actual Values, Settings, Commands, and Targets. The **MENU** key navigates through these pages. Each heading page is broken down further into logical subgroups.

The **▲** **◀** MESSAGE **▶** **▼** keys navigate through the subgroups. The **▲** VALUE **▼** keys scroll increment or decrement numerical setting values when in programming mode. These keys also scroll through alphanumeric values in the text edit mode. Alternatively, values may also be entered with the numeric keypad.

The **♦** key initiates and advance to the next character in text edit mode or enters a decimal point. The **HELP** key may be pressed at any time for context sensitive help messages. The **ENTER** key stores altered setting values.

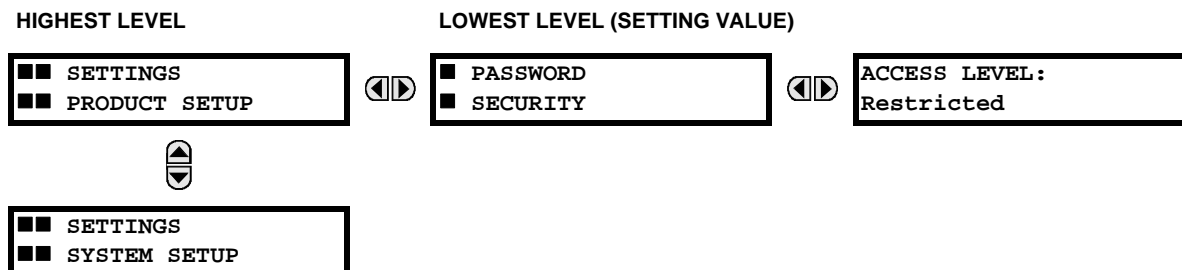
1.5.2 MENU NAVIGATION

Press the **MENU** key to select the desired header display page (top-level menu). The header title appears momentarily followed by a header display page menu item. Each press of the **MENU** key advances through the main heading pages as illustrated below.



1.5.3 MENU HIERARCHY

The setting and actual value messages are arranged hierarchically. The header display pages are indicated by double scroll bar characters (■), while sub-header pages are indicated by single scroll bar characters (■). The header display pages represent the highest level of the hierarchy and the sub-header display pages fall below this level. The MESSAGE **▲** and **▼** keys move within a group of headers, sub-headers, setting values, or actual values. Continually pressing the MESSAGE **▶** key from a header display displays specific information for the header category. Conversely, continually pressing the **◀** MESSAGE key from a setting value or actual value display returns to the header display.



1.5.4 RELAY ACTIVATION

The relay is defaulted to the “Not Programmed” state when it leaves the factory. This safeguards against the installation of a relay whose settings have not been entered. When powered up successfully, the Trouble LED will be on and the In Service LED off. The relay in the “Not Programmed” state will block signaling of any output relay. These conditions will remain until the relay is explicitly put in the “Programmed” state.

Select the menu message **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **INSTALLATION** ⇒ **RELAY SETTINGS**

RELAY SETTINGS:
 Not Programmed

To put the relay in the “Programmed” state, press either of the **VALUE** keys once and then press **ENTER**. The faceplate Trouble LED will turn off and the In Service LED will turn on. The settings for the relay can be programmed manually (refer to Chapter 5) via the faceplate keypad or remotely (refer to the EnerVista UR Setup Help file) via the EnerVista UR Setup software interface.

1.5.5 RELAY PASSWORDS

It is recommended that passwords be set up for each security level and assigned to specific personnel. There are two user password security access levels, COMMAND and SETTING:

1. COMMAND

The COMMAND access level restricts the user from making any settings changes, but allows the user to perform the following operations:

- change state of virtual inputs
- clear event records
- clear oscillography records
- operate user-programmable pushbuttons

2. SETTING

The SETTING access level allows the user to make any changes to any of the setting values.



Refer to the *Changing Settings* section in Chapter 4 for complete instructions on setting up security level passwords.

1.5.6 FLEXLOGIC™ CUSTOMIZATION

FlexLogic™ equation editing is required for setting up user-defined logic for customizing the relay operations. See the *Flex-Logic™* section in Chapter 5 for additional details.

Templated tables for charting all the required settings before entering them via the keypad are available from the GE Multilin website at <http://www.GEindustrial.com/multilin>.

The N60 requires a minimum amount of maintenance when it is commissioned into service. The N60 is a microprocessor-based relay and its characteristics do not change over time. As such no further functional tests are required.

Furthermore the N60 performs a number of ongoing self-tests and takes the necessary action in case of any major errors (see the *Relay Self-Test* section in Chapter 7 for details). However, it is recommended that maintenance on the N60 be scheduled with other system maintenance. This maintenance may involve the following.

In-service maintenance:

1. Visual verification of the analog values integrity such as voltage and current (in comparison to other devices on the corresponding system).
2. Visual verification of active alarms, relay display messages, and LED indications.
3. LED test.
4. Visual inspection for any damage, corrosion, dust, or loose wires.
5. Event recorder file download with further events analysis.

Out-of-service maintenance:

1. Check wiring connections for firmness.
2. Analog values (currents, voltages, RTDs, analog inputs) injection test and metering accuracy verification. Calibrated test equipment is required.
3. Protection elements setpoints verification (analog values injection or visual verification of setting file entries against relay settings schedule).
4. Contact inputs and outputs verification. This test can be conducted by direct change of state forcing or as part of the system functional testing.
5. Visual inspection for any damage, corrosion, or dust.
6. Event recorder file download with further events analysis.
7. LED Test and pushbutton continuity check.

Unscheduled maintenance such as during a disturbance causing system interruption:

1. View the event recorder and oscillography or fault report for correct operation of inputs, outputs, and elements.

If it is concluded that the relay or one of its modules is of concern, contact GE Multilin or one of its representatives for prompt service.

2.1.1 OVERVIEW

The N60 Network Stability and Synchrophasor Measurement System is a flexible microprocessor-based device intended for development of load shedding and special protection schemes.

Owing to its modular architecture, the N60 can be configured to monitor from one through five three-phase power circuits. The relay provides for variety of metering functions, including: active, reactive and apparent power on a per-phase and three-phase basis; true RMS value, phasors and symmetrical components of currents and voltages; and power factor and frequency. The latter could be measured independently and simultaneously from up to six different signals.

The N60 allows interfacing other analog signals via optional transducer modules to monitor equipment temperature, transformer tap positions, weather data, and other information.

In addition to a standard collection of communications protocols that can be exercised simultaneously and independently over a range of communication ports, including the redundant Ethernet port, the relay supports an independent mechanism for direct, fast and secure digital inter-IED communications. This allows both reducing wiring and development time for all the local connections in a substation, as well as building wide-area protection and control schemes.

Up to 64 on/off signals can be exchanged between any two N60 devices via digital communications. The relays could be configured in rings with up to 16 devices each using direct fiber (C37.94), G.704 and RS422 interfaces. An optional redundant (dual-channel) communication card supports combinations of the three interfaces allowing different physical connections in each channel. Dual-ring communication architecture could be selected for redundancy. Open ring or crossover configurations could be utilized to increase the number of devices in the scheme.

The effective message delivery time depends of number of other N60 devices located between the sending and receiving IEDs. A two-cycle or one-cycle worst-case message delivery times could be comfortably achieved for comparatively large N60 schemes.

Sophisticated self-monitoring and diagnostic functions are incorporated such as the 32-bit CRC, unreturned messages count or lost packets counts. The N60 supports both multiplexed and direct fiber (up to 100 km) inter-substation connections.

The N60 allows sending and receiving any analog value measured by the relay using the dedicated inter-IED communication mechanisms. Power, voltage and current magnitudes, frequency, transducer inputs and other values can be freely configured for the inter-IED exchanges. The analog values are transmitted with the 8-bit resolution. Upon reception, any remote analog value could be re-sent, compared with another value or a constant threshold, added to or subtracted from other local or remote analog value, subjected to the rate-of-change monitoring, etc. This powerful feature allows advanced applications such as balancing power over wide areas, or adding extra security by comparing local and remote measurements for consistency. It also facilitates simple telemetry.

Diagnostic features include an event recorder capable of storing 1024 time-tagged events, oscillography capable of storing up to 64 records with programmable trigger, content and sampling rate, and data logger acquisition of up to 16 channels, with programmable content and sampling rate. The internal clock used for time-tagging can be synchronized with an IRIG-B signal or via the SNTP protocol over the Ethernet port. This precise time stamping allows the sequence of events to be determined throughout the system. Events can also be programmed (via FlexLogic™ equations) to trigger oscillography data capture which may be set to record the measured parameters before and after the event for viewing on a personal computer (PC). These tools significantly reduce troubleshooting time and simplify report generation in the event of a system fault.

A faceplate RS232 port may be used to connect to a PC for the programming of settings and the monitoring of actual values. A variety of communications modules are available. Two rear RS485 ports allow independent access by operating and engineering staff. All serial ports use the Modbus® RTU protocol. The RS485 ports may be connected to system computers with baud rates up to 115.2 kbps. The RS232 port has a fixed baud rate of 19.2 kbps. Optional communications modules include a 10BaseF Ethernet interface which can be used to provide fast, reliable communications in noisy environments. Another option provides two 10BaseF fiber optic ports for redundancy. The Ethernet port supports MMS/UCA2, Modbus®/TCP, and TFTP protocols, and allows access to the relay via any standard web browser (UR web pages). The IEC 60870-5-104 protocol is supported on the Ethernet port. DNP 3.0 and IEC 60870-5-104 cannot be enabled at the same time.

The N60 IEDs use flash memory technology which allows field upgrading as new features are added. The following Single Line Diagram illustrates the relay functionality using ANSI (American National Standards Institute) device numbers.

Table 2–1: ANSI DEVICE NUMBERS AND FUNCTIONS

DEVICE	FUNCTION	DEVICE	FUNCTION
25	Synchrocheck	59P	Phase overvoltage
27P	Phase undervoltage	68	Power swing blocking
32	Sensitive directional power	81O	Overfrequency
50DD	Disturbance detector	81U	Underfrequency
50P	Phase instantaneous overcurrent		

Table 2–2: OTHER DEVICE FUNCTIONS

FUNCTION	FUNCTION	FUNCTION
8-bit switch	FlexLogic™ equations	Oscillography
Breaker control	Generic comparator	Setting groups (6)
Contact inputs (up to 96)	Metering: current, demand, energy, frequency, power, power factor, voltage	Synchrophasors
Contact outputs (up to 64)	MMS/UCA communications	Time synchronization over SNTP
Control pushbuttons	MMS/UCA remote inputs/outputs ("GOOSE")	Transducer inputs/outputs
Data logger	Modbus communications	User-definable displays
Digital counters (8)	Modbus user map	User-programmable LEDs
Digital elements (16)	Non-volatile latches	User-programmable pushbuttons
Digitizer	Non-volatile selector switch	User-programmable self-tests
Direct inputs/outputs (64)	Open pole detect	Virtual inputs (64)
DNP 3.0 or IEC 60870-5-104 comms		Virtual outputs (96)
Event recorder		VT fuse failure
FlexElements™ (16)		

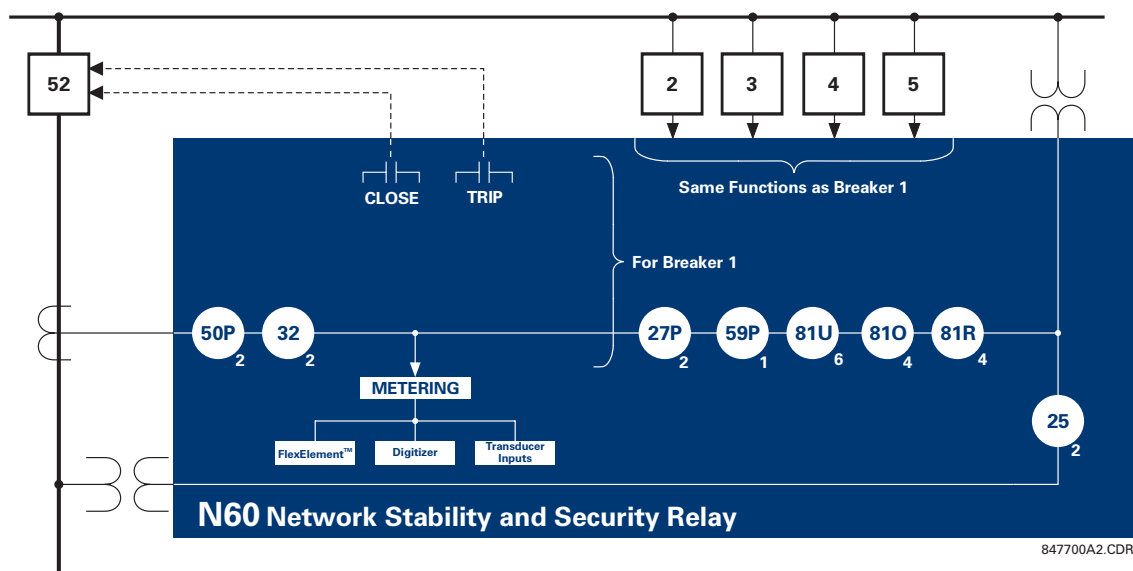


Figure 2–1: SINGLE LINE DIAGRAM

2.1.2 ORDERING

The relay is available as a 19-inch rack horizontal mount unit and consists of the following modules: power supply, CPU, digital input/output. Each of these modules can be supplied in a number of configurations specified at the time of ordering. The information required to completely specify the relay is provided in the following tables (see Chapter 3 for full details of relay modules).

Table 2–3: N60 ORDER CODES

	N60	-	*	*	-	*	*	-	F	*	-	H	*	-	M	*	-	P	*	-	U	*	-	W/X	*	
BASE UNIT	N60																									Full Size Horizontal Mount
CPU		E																								Base Unit
		G																								RS485 and RS485
		H																								RS485 and multi-mode ST 10Base-F
		J																								RS485 and multi-mode ST redundant 10Base-F
SOFTWARE			00																							RS485 and multi-mode ST 100Base-FX
			03																							No Software Options
			06																							IEC 61850; not available for Type E CPUs
			07																							Phasor Measurement Unit (PMU)
MOUNT/COATING				H																						Phasor Measurement Unit (PMU) and IEC 61850
				A																						Horizontal (19" rack)
FACEPLATE/ DISPLAY					C																					Horizontal (19" rack) with harsh environmental coating
					P																					English display
					A																					English display with 4 small and 12 large programmable pushbuttons
					B																					Chinese display
					D																					Chinese display with 4 small and 12 large programmable pushbuttons
					G																					French display
POWER SUPPLY						H																				French display with 4 small and 12 large programmable pushbuttons
						L																				125 / 250 V AC/DC power supply
CT/VT MODULES							8F						8F													24 to 48 V (DC only) power supply
							8G						8G													Standard 4CT/4VT
							8H						8H													Sensitive Ground 4CT/4VT
							8J						8J													Standard 8CT
																										Sensitive Ground 8CT
DIGITAL INPUTS/OUTPUTS													XX		XX		XX		XX		XX					No Module
							4L		4L		4L		4L		4L		4L		4L		4L					14 Form-A (no monitoring) Latchable outputs
							67		67		67		67		67		67		67		67					8 Form-A (no monitoring) outputs
							6C		6C		6C		6C		6C		6C		6C		6C					8 Form-C outputs
							6D		6D		6D		6D		6D		6D		6D		6D					16 digital inputs
							6E		6E		6E		6E		6E		6E		6E		6E					4 Form-C outputs, 8 digital inputs
							6F		6F		6F		6F		6F		6F		6F		6F					8 Fast Form-C outputs
							6P		6P		6P		6P		6P		6P		6P		6P					6 Form-A (current with optional voltage) outputs, 4 digital inputs
							6R		6R		6R		6R		6R		6R		6R		6R					2 Form-A (no monitoring) and 2 Form-C outputs, 8 digital inputs
							6S		6S		6S		6S		6S		6S		6S		6S					2 Form-A (no monitoring) and 4 Form-C outputs, 4 digital inputs
							6T		6T		6T		6T		6T		6T		6T		6T					4 Form-A (no monitoring) outputs, 8 digital inputs
							6U		6U		6U		6U		6U		6U		6U		6U					6 Form-A (no monitoring) outputs, 4 digital inputs
TRANSDUCER INPUTS/OUTPUTS (maximum of 3 per unit)							5A		5A		5A		5A		5A		5A		5A		5A					4 dcmA inputs, 4 dcmA outputs (only one 5A module is allowed)
							5C		5C		5C		5C		5C		5C		5C		5C					8 RTD inputs
INTER-RELAY COMMUNICATIONS (select a maximum of 1 per unit)																						7H				7H 820 nm, multi-mode, LED, 2 Channels
																						7I				7I 1300 nm, multi-mode, LED, 2 Channels
																						7M				7M Channel 1 - RS422; Channel 2 - 1300 nm, multi-mode, LED
																						7S				7S G.703, 2 Channels
																						7W				7W RS422, 2 Channels

The order codes for replacement modules to be ordered separately are shown in the following table. When ordering a replacement CPU module or faceplate, please provide the serial number of your existing unit.

Table 2–4: ORDER CODES FOR REPLACEMENT MODULES

	UR	-	**	-	*
POWER SUPPLY (redundant supply only available in horizontal units; must be same type as main supply)	1H				125 / 250 V AC/DC
	1L				24 to 48 V (DC only)
	RH				redundant 125 / 250 V AC/DC
	RH				redundant 24 to 48 V (DC only)
CPU	9E				RS485 and RS485 (Modbus RTU, DNP 3.0)
	9G				RS485 and 10Base-F (Ethernet, Modbus TCP/IP, DNP 3.0)
	9H				RS485 and Redundant 10Base-F (Ethernet, Modbus TCP/IP, DNP 3.0)
	9J				RS485 and multi-mode ST 100Base-FX (Ethernet, Modbus TCP/IP, DNP 3.0)
	9K				RS485 and multi-mode ST redundant 100Base-FX (Ethernet, Modbus TCP/IP, DNP 3.0)
	9L				RS485 and single mode SC 100Base-FX (Ethernet, Modbus TCP/IP, DNP 3.0)
	9M				RS485 and single mode SC redundant 100Base-FX (Ethernet, Modbus TCP/IP, DNP 3.0)
	9N				RS485 and 10/100Base-T (Ethernet, Modbus TCP/IP, DNP 3.0)
	9P				RS485 and single mode ST 100Base-FX (Ethernet, Modbus TCP/IP, DNP 3.0)
	9R				RS485 and single mode ST redundant 100Base-FX (Ethernet, Modbus TCP/IP, DNP 3.0)
FACEPLATE/DISPLAY	3C				Horizontal faceplate with keypad and English display
	3P				Horizontal faceplate with keypad, user-programmable pushbuttons, and English display
	3R				Horizontal faceplate with keypad and Russian display
	3S				Horizontal faceplate with keypad, user-programmable pushbuttons, and Russian display
	3A				Horizontal faceplate with keypad and Chinese display
	3B				Horizontal faceplate with keypad, user-programmable pushbuttons, and Chinese display
DIGITAL INPUTS/OUTPUTS	3D				Horizontal faceplate with keypad and French display
	3G				Horizontal faceplate with keypad, user-programmable pushbuttons, and French display
	4A				4 Solid-State (no monitoring) MOSFET outputs
	4B				4 Solid-State (voltage with optional current) MOSFET outputs
	4C				4 Solid-State (current with optional voltage) MOSFET outputs
	4D				16 digital inputs with Auto-Burnishing
	4L				14 Form-A (no monitoring) Latching outputs
	67				8 Form-A (no monitoring) outputs
	6A				2 Form-A (voltage with optional current) and 2 Form-C outputs, 8 digital inputs
	6B				2 Form-A (voltage with optional current) and 4 Form-C outputs, 4 digital inputs
	6C				8 Form-C outputs
	6D				16 digital inputs
	6E				4 Form-C outputs, 8 digital inputs
	6F				8 Fast Form-C outputs
	6G				4 Form-A (voltage with optional current) outputs, 8 digital inputs
	6H				6 Form-A (voltage with optional current) outputs, 4 digital inputs
	6K				4 Form-C and 4 Fast Form-C outputs
	6L				2 Form-A (current with optional voltage) and 2 Form-C outputs, 8 digital inputs
	6M				2 Form-A (current with optional voltage) and 4 Form-C outputs, 4 digital inputs
	6N				4 Form-A (current with optional voltage) outputs, 8 digital inputs
CT/VT MODULES (NOT AVAILABLE FOR THE C30)	6P				6 Form-A (current with optional voltage) outputs, 4 digital inputs
	6R				2 Form-A (no monitoring) and 2 Form-C outputs, 8 digital inputs
	6S				2 Form-A (no monitoring) and 4 Form-C outputs, 4 digital inputs
	6T				4 Form-A (no monitoring) outputs, 8 digital inputs
UR INTER-RELAY COMMUNICATIONS	6U				6 Form-A (no monitoring) outputs, 4 digital inputs
	8F				Standard 4CT/4VT
	8G				Sensitive Ground 4CT/4VT
	8H				Standard 8CT
	8J				Sensitive Ground 8CT
	2A				C37.94SM, 1300nm single-mode, ELED, 1 channel single-mode
	2B				C37.94SM, 1300nm single-mode, ELED, 2 channel single-mode
	72				1550 nm, single-mode, LASER, 1 Channel
	73				1550 nm, single-mode, LASER, 2 Channel
	74				Channel 1 - RS422; Channel 2 - 1550 nm, single-mode, LASER
TRANSUCER INPUTS/OUTPUTS	75				Channel 1 - G.703; Channel 2 - 1550 nm, Single-mode LASER
	76				IEEE C37.94, 820 nm, multimode, LED, 1 Channel
	77				IEEE C37.94, 820 nm, multimode, LED, 2 Channels
	7A				820 nm, multi-mode, LED, 1 Channel
	7B				1300 nm, multi-mode, LED, 1 Channel
	7C				1300 nm, single-mode, ELED, 1 Channel
	7D				1300 nm, single-mode, LASER, 1 Channel
	7E				Channel 1 - G.703; Channel 2 - 820 nm, multi-mode
	7F				Channel 1 - G.703; Channel 2 - 1300 nm, multi-mode
	7G				Channel 1 - G.703; Channel 2 - 1300 nm, single-mode ELED
	7H				820 nm, multi-mode, LED, 2 Channels
	7I				1300 nm, multi-mode, LED, 2 Channels
	7J				1300 nm, single-mode, ELED, 2 Channels
	7K				1300 nm, single-mode, LASER, 2 Channels
	7L				Channel 1 - RS422; Channel 2 - 820 nm, multi-mode, LED
	7M				Channel 1 - RS422; Channel 2 - 1300 nm, multi-mode, LED
	7N				Channel 1 - RS422; Channel 2 - 1300 nm, single-mode, ELED
	7P				Channel 1 - RS422; Channel 2 - 1300 nm, single-mode, LASER
	7Q				Channel 1 - G.703; Channel 2 - 1300 nm, single-mode LASER
	7R				G.703, 1 Channel
	7S				G.703, 2 Channels
	7T				RS422, 1 Channel
	7W				RS422, 2 Channels
TRANSUCER INPUTS/OUTPUTS	5A				4 dcmA inputs, 4 dcmA outputs (only one 5A module is allowed)
	5C				8 RTD inputs
	5D				4 RTD inputs, 4 dcmA outputs (only one 5D module is allowed)
	5E				4 dcmA inputs, 4 RTD inputs
	5F				8 dcmA inputs

SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE

2.2.1 PROTECTION ELEMENTS



The operating times below include the activation time of a trip rated Form-A output contact unless otherwise indicated. FlexLogic™ operands of a given element are 4 ms faster. This should be taken into account when using FlexLogic™ to interconnect with other protection or control elements of the relay, building FlexLogic™ equations, or interfacing with other IEDs or power system devices via communications or different output contacts.

PHASE IOC

Pickup level:	0.000 to 30.000 pu in steps of 0.001
Dropout level:	97 to 98% of pickup
Level accuracy:	
0.1 to 2.0 × CT rating:	±0.5% of reading or ±1% of rated (whichever is greater)
> 2.0 × CT rating	±1.5% of reading
Overreach:	<2%
Pickup delay:	0.00 to 600.00 s in steps of 0.01
Reset delay:	0.00 to 600.00 s in steps of 0.01
Operate time:	<16 ms at 3 × Pickup at 60 Hz (Phase/Ground IOC) <20 ms at 3 × Pickup at 60 Hz (Neutral IOC)
Timing accuracy:	Operate at 1.5 × Pickup ±3% or ±4 ms (whichever is greater)

SENSITIVE DIRECTIONAL POWER

Measured power:	3-phase, true RMS
Number of stages:	2
Characteristic angle:	0 to 359° in steps of 1
Calibration angle:	0.00 to 0.95° in steps of 0.05
Minimum power:	−1.200 to 1.200 pu in steps of 0.001
Pickup level accuracy:	±1% or ±0.001 pu, whichever is greater
Hysteresis:	2% or 0.001 pu, whichever is greater
Pickup delay:	0 to 600.00 s in steps of 0.01
Time accuracy:	±3% or ±4 ms, whichever is greater
Operate time:	50 ms

PHASE UNDERVOLTAGE

Pickup level:	0.000 to 3.000 pu in steps of 0.001
Dropout level:	102 to 103% of pickup
Level accuracy:	±0.5% of reading from 10 to 208 V
Curve shapes:	GE IAV Inverse; Definite Time (0.1s base curve)
Curve multiplier:	Time dial = 0.00 to 600.00 in steps of 0.01
Timing accuracy:	Operate at < 0.90 × pickup ±3.5% of operate time or ±4 ms (whichever is greater)

PHASE OVERVOLTAGE

Voltage:	Phasor only
Pickup level:	0.000 to 3.000 pu in steps of 0.001
Dropout level:	97 to 98% of Pickup
Level accuracy:	±0.5% of reading from 10 to 208 V
Pickup delay:	0.00 to 600.00 in steps of 0.01 s
Operate time:	< 30 ms at 1.10 × Pickup at 60 Hz
Timing accuracy:	±3% or ±4 ms (whichever is greater)

UNDERFREQUENCY

Minimum signal:	0.10 to 1.25 pu in steps of 0.01
Pickup level:	20.00 to 65.00 Hz in steps of 0.01
Dropout level:	Pickup + 0.03 Hz
Level accuracy:	±0.01 Hz
Time delay:	0 to 65.535 s in steps of 0.001
Timer accuracy:	±3% or 4 ms, whichever is greater

OVERFREQUENCY

Pickup level:	20.00 to 65.00 Hz in steps of 0.01
Dropout level:	Pickup − 0.03 Hz
Level accuracy:	±0.01 Hz
Time delay:	0 to 65.535 s in steps of 0.001
Timer accuracy:	±3% or 4 ms, whichever is greater

RATE OF CHANGE OF FREQUENCY

df/dt trend:	increasing, decreasing, bi-directional
df/dt pickup level:	0.10 to 15.00 Hz/s in steps of 0.01
df/dt dropout level:	96% of pickup
df/dt level accuracy:	80 mHz/s or 3.5%, whichever is greater
Overvoltage supv.:	0.100 to 3.000 pu in steps of 0.001
Overcurrent supv.:	0.000 to 30.000 pu in steps of 0.001
Pickup delay:	0 to 65.535 s in steps of 0.001
Reset delay:	0 to 65.535 s in steps of 0.001
Time accuracy:	±3% or ±4 ms, whichever is greater
95% settling time for df/dt:	< 24 cycles
Operate time:	at 2 × pickup: 12 cycles at 3 × pickup: 8 cycles at 5 × pickup: 6 cycles

SYNCHROCHECK

Max voltage difference:	0 to 400000 V in steps of 1
Max angle difference:	0 to 100° in steps of 1
Max freq. difference:	0.00 to 2.00 Hz in steps of 0.01
Hysteresis for max. freq. diff.:	0.00 to 0.10 Hz in steps of 0.01
Dead source function:	None, LV1 & DV2, DV1 & LV2, DV1 or DV2, DV1 xor DV2, DV1 & DV2 (L = Live, D = Dead)

POWER SWING DETECT

Functions:	Power swing block, Out-of-step trip
Characteristic:	Mho or Quad
Measured impedance:	Positive-sequence
Blocking / tripping modes:	2-step or 3-step
Tripping mode:	Early or Delayed
Current supervision:	
Pickup level:	0.050 to 30.000 pu in steps of 0.001
Dropout level:	97 to 98% of Pickup
Fwd / reverse reach (sec. Ω):	0.10 to 500.00 Ω in steps of 0.01
Left and right blinders (sec. Ω):	0.10 to 500.00 Ω in steps of 0.01
Impedance accuracy:	$\pm 5\%$
Fwd / reverse angle impedances:	40 to 90° in steps of 1
Angle accuracy:	$\pm 2^\circ$
Characteristic limit angles:	40 to 140° in steps of 1
Timers:	0.000 to 65.535 s in steps of 0.001
Timing accuracy:	$\pm 3\%$ or 4 ms, whichever is greater

OPEN POLE DETECTOR

Detects an open pole condition, monitoring breaker auxiliary contacts, the current in each phase and optional voltages on the line	
Current pickup level:	0.000 to 30.000 pu in steps of 0.001
Current dropout level:	Pickup + 3%, not less than 0.05 pu

2.2.2 USER-PROGRAMMABLE ELEMENTS**FLEXLOGIC™**

Programming language:	Reverse Polish Notation with graphical visualization (keypad programmable)
Lines of code:	512
Internal variables:	64
Supported operations:	NOT, XOR, OR (2 to 16 inputs), AND (2 to 16 inputs), NOR (2 to 16 inputs), NAND (2 to 16 inputs), Latch (Reset dominant), Edge Detectors, Timers
Inputs:	any logical variable, contact, or virtual input
Number of timers:	32
Pickup delay:	0 to 60000 (ms, sec., min.) in steps of 1
Dropout delay:	0 to 60000 (ms, sec., min.) in steps of 1

FLEXCURVES™

Number:	4 (A through D)
Reset points:	40 (0 through 1 of pickup)
Operate points:	80 (1 through 20 of pickup)
Time delay:	0 to 65535 ms in steps of 1

FLEX STATES

Number:	up to 256 logical variables grouped under 16 Modbus addresses
Programmability:	any logical variable, contact, or virtual input

FLEXELEMENTS™

Number of elements:	16
Operating signal:	any analog actual value, or two values in differential mode
Operating signal mode:	Signed or Absolute Value
Operating mode:	Level, Delta
Comparator direction:	Over, Under
Pickup Level:	-30.000 to 30.000 pu in steps of 0.001
Hysteresis:	0.1 to 50.0% in steps of 0.1
Delta dt:	20 ms to 60 days
Pickup & dropout delay:	0.000 to 65.535 s in steps of 0.001

NON-VOLATILE LATCHES

Type:	Set-dominant or Reset-dominant
Number:	16 (individually programmed)
Output:	Stored in non-volatile memory
Execution sequence:	As input prior to protection, control, and FlexLogic™

USER-PROGRAMMABLE LEDs

Number:	48 plus Trip and Alarm
Programmability:	from any logical variable, contact, or virtual input
Reset mode:	Self-reset or Latched

LED TEST

Initiation:	from any digital input or user-programmable condition
Number of tests:	3, interruptible at any time
Duration of full test:	approximately 3 minutes
Test sequence 1:	all LEDs on
Test sequence 2:	all LEDs off, one LED at a time on for 1 s
Test sequence 3:	all LEDs on, one LED at a time off for 1 s

USER-DEFINABLE DISPLAYS

Number of displays:	16
Lines of display:	2 × 20 alphanumeric characters
Parameters:	up to 5, any Modbus register addresses
Invoking and scrolling:	keypad, or any user-programmable condition, including pushbuttons

CONTROL PUSHBUTTONS

Number of pushbuttons:	7
Operation:	drive FlexLogic™ operands

USER-PROGRAMMABLE PUSHBUTTONS (OPTIONAL)

Number of pushbuttons:	12
Mode:	Self-Reset, Latched
Display message:	2 lines of 20 characters each

SELECTOR SWITCH

Number of elements:	2
Upper position limit:	1 to 7 in steps of 1
Selecting mode:	Time-out or Acknowledge
Time-out timer:	3.0 to 60.0 s in steps of 0.1
Control inputs:	step-up and 3-bit
Power-up mode:	restore from non-volatile memory or syn- chronize to a 3-bit control input or Synch/ Restore mode

DIGITIZER

Input signal:	any FlexAnalog parameter
Independent elements:	1 per CT bank, to a maximum of 5
Independent elements:	5
Response time:	< 8 ms at 60 Hz, < 10 ms at 50 Hz
Upper / lower limit for input signal:	0.050 to 90 pu in steps of 0.001
Resolution:	8 bits / full-scale
Maximum error:	±0.6% of full-scale
Rounding method:	nearest with a one-count dead-band

8-BIT COMPARATOR

Number of elements:	6
Operating signal:	up to two 8-bit integers via FlexLogic™ operands
Operating mode:	add or subtract, controlled from any FlexLogic™ operand
Operating signal mode:	Signed, Absolute Value
Comparator direction:	Over, Under
Pickup level:	–25400 to 25400 in steps of 1
Hysteresis:	0 to 5000 in steps of 1
Pickup/Dropout delay:	0 to 65.535 s in steps of 0.01
Scaling factor for display:	0.01 to 100.00 in steps of 0.01

8-BIT SWITCH

Number of elements:	6
Input signals:	two 8-bit integers via FlexLogic™ oper- ands
Control signal:	any FlexLogic™ operand
Response time:	< 8 ms at 60 Hz, < 10 ms at 50 Hz

2.2.3 MONITORING**OSCILLOGRAPHY**

Maximum records:	64
Sampling rate:	64 samples per power cycle
Triggers:	Any element pickup, dropout or operate Digital input change of state Digital output change of state FlexLogic™ equation
Data:	AC input channels Element state Digital input state Digital output state
Data storage:	In non-volatile memory

EVENT RECORDER

Capacity:	1024 events
Time-tag:	to 1 microsecond
Triggers:	Any element pickup, dropout or operate Digital input change of state Digital output change of state Self-test events
Data storage:	In non-volatile memory

DATA LOGGER

Number of channels:	1 to 16
Parameters:	Any available analog actual value
Sampling rate:	15 to 3600000 ms in steps of 1
Trigger:	any FlexLogic™ operand
Mode:	continuous or triggered
Storage capacity:	(NN is dependent on memory)
1-second rate:	01 channel for NN days 16 channels for NN days
↓	↓
60-minute rate:	01 channel for NN days 16 channels for NN days

2.2.4 METERING

RMS CURRENT: PHASE, NEUTRAL, AND GROUND

Accuracy at
 0.1 to $2.0 \times \text{CT rating}$: $\pm 0.25\%$ of reading or $\pm 0.1\%$ of rated (whichever is greater)
 $> 2.0 \times \text{CT rating}$: $\pm 1.0\%$ of reading

RMS VOLTAGE

Accuracy: $\pm 0.5\%$ of reading from 10 to 208 V

REAL POWER (WATTS)

Accuracy: $\pm 1.0\%$ of reading at
 $-0.8 < \text{PF} \leq -1.0$ and $0.8 < \text{PF} \leq 1.0$

REACTIVE POWER (VARs)

Accuracy: $\pm 1.0\%$ of reading at $-0.2 \leq \text{PF} \leq 0.2$

APPARENT POWER (VA)

Accuracy: $\pm 1.0\%$ of reading

WATT-HOURS (POSITIVE AND NEGATIVE)

Accuracy: $\pm 2.0\%$ of reading
 Range: ± 0 to 2×10^9 MWh
 Parameters: 3-phase only
 Update rate: 50 ms

VAR-HOURS (POSITIVE AND NEGATIVE)

Accuracy: $\pm 2.0\%$ of reading
 Range: ± 0 to 2×10^9 Mvarh
 Parameters: 3-phase only
 Update rate: 50 ms

FREQUENCY

Accuracy at
 $V = 0.8$ to 1.2 pu: ± 0.01 Hz (when voltage signal is used for frequency measurement)
 $I = 0.1$ to 0.25 pu: ± 0.05 Hz
 $I > 0.25$ pu: ± 0.02 Hz (when current signal is used for frequency measurement)

DEMAND

Measurements: Phases A, B, and C present and maximum measured currents
 3-Phase Power (P, Q, and S) present and maximum measured currents
 Accuracy: $\pm 2.0\%$

2.2.5 INPUTS

AC CURRENT

CT rated primary: 1 to 50000 A
 CT rated secondary: 1 A or 5 A by connection
 Nominal frequency: 20 to 65 Hz
 Relay burden: < 0.2 VA at rated secondary
 Conversion range:
 Standard CT: 0.02 to $46 \times \text{CT rating}$ RMS symmetrical
 Sensitive Ground module: 0.002 to $4.6 \times \text{CT rating}$ RMS symmetrical
 Current withstand: 20 ms at 250 times rated
 1 sec. at 100 times rated
 continuous at 3 times rated

AC VOLTAGE

VT rated secondary: 50.0 to 240.0 V
 VT ratio: 1.00 to 24000.00
 Nominal frequency: 20 to 65 Hz
 Relay burden: < 0.25 VA at 120 V
 Conversion range: 1 to 275 V
 Voltage withstand: continuous at 260 V to neutral
 1 min./hr at 420 V to neutral

CONTACT INPUTS

Dry contacts: 1000 Ω maximum
 Wet contacts: 300 V DC maximum
 Selectable thresholds: 17 V, 33 V, 84 V, 166 V
 Tolerance: $\pm 10\%$
 Contacts per common return: 4
 Recognition time: < 1 ms
 Debounce time: 0.0 to 16.0 ms in steps of 0.5
 Continuous current draw: 3 mA (when energized)

CONTACT INPUTS WITH AUTO-BURNISHING

Dry contacts: 1000 Ω maximum
 Wet contacts: 300 V DC maximum
 Selectable thresholds: 17 V, 33 V, 84 V, 166 V
 Tolerance: $\pm 10\%$
 Contacts per common return: 2
 Recognition time: < 1 ms
 Debounce time: 0.0 to 16.0 ms in steps of 0.5
 Continuous current draw: 3 mA (when energized)
 Auto-burnish impulse current: 50 to 70 mA
 Duration of auto-burnish impulse: 25 to 50 ms

DCMA INPUTS

Current input (mA DC): 0 to -1 , 0 to $+1$, -1 to $+1$, 0 to 5, 0 to 10, 0 to 20, 4 to 20 (programmable)
 Input impedance: 379 $\Omega \pm 10\%$
 Conversion range: -1 to $+20$ mA DC
 Accuracy: $\pm 0.2\%$ of full scale
 Type: Passive

RTD INPUTS

Types (3-wire): 100 Ω Platinum, 100 & 120 Ω Nickel, 10 Ω Copper
 Sensing current: 5 mA
 Range: -50 to $+250^\circ\text{C}$
 Accuracy: $\pm 2^\circ\text{C}$
 Isolation: 36 V pk-pk

IRIG-B INPUT

Amplitude modulation: 1 to 10 V pk-pk
 DC shift: TTL
 Input impedance: 22 k Ω
 Isolation: 2 kV

REMOTE INPUTS (MMS GOOSE)

Number of input points: 64, configured from 64 incoming bit pairs
 Number of remote devices: 16
 Default states on loss of comms.: On, Off, Latest/Off, Latest/On

DIRECT INPUTS

Number of input points: 64
 No. of remote devices: 16
 Default states on loss of comms.: On, Off, Latest/Off, Latest/On
 Ring configuration: Yes, No
 Data rate: 64 or 128 kbps
 CRC: 32-bit
 CRC alarm:
 Responding to: Rate of messages failing the CRC
 Monitoring message count: 10 to 10000 in steps of 1
 Alarm threshold: 1 to 1000 in steps of 1
 Unreturned message alarm:
 Responding to: Rate of unreturned messages in the ring configuration
 Monitoring message count: 10 to 10000 in steps of 1
 Alarm threshold: 1 to 1000 in steps of 1

2.2.6 POWER SUPPLY**LOW RANGE**

Nominal DC voltage: 24 to 48 V
 Min/max DC voltage: 20 / 60 V
 Voltage loss hold-up: 20 ms duration at nominal
 NOTE: Low range is DC only.

HIGH RANGE

Nominal DC voltage: 125 to 250 V
 Min/max DC voltage: 88 / 300 V
 Nominal AC voltage: 100 to 240 V at 50/60 Hz
 Min/max AC voltage: 88 / 265 V at 25 to 100 Hz
 Voltage loss hold-up: 200 ms duration at nominal

ALL RANGES

Volt withstand: $2 \times$ Highest Nominal Voltage for 10 ms
 Power consumption: typical = 15 to 20 W/VA
 maximum = 50 W/VA
 contact factory for exact order code consumption

INTERNAL FUSE RATINGS

Low range power supply: 8 A / 250 V
 High range power supply: 4 A / 250 V

INTERRUPTING CAPACITY

AC: 100 000 A RMS symmetrical
 DC: 10 000 A

2.2.7 OUTPUTS**FORM-A RELAY**

Make and carry for 0.2 s: 30 A as per ANSI C37.90
 Carry continuous: 6 A
 Break (DC inductive, L/R = 40 ms):

VOLTAGE	CURRENT
24 V	1 A
48 V	0.5 A
125 V	0.3 A
250 V	0.2 A

Operate time: < 4 ms
 Contact material: silver alloy

LATCHING RELAY

Make and carry for 0.2 s: 30 A as per ANSI C37.90
 Carry continuous: 6 A
 Break at L/R of 40 ms: 0.25 A DC max.
 Operate time: < 4 ms
 Contact material: silver alloy
 Control: separate operate and reset inputs
 Control mode: operate-dominant or reset-dominant

FORM-A VOLTAGE MONITOR

Applicable voltage: approx. 15 to 250 V DC
 Trickle current: approx. 1 to 2.5 mA

FORM-A CURRENT MONITOR

Threshold current: approx. 80 to 100 mA

FORM-C AND CRITICAL FAILURE RELAY

Make and carry for 0.2 s: 30 A as per ANSI C37.90
 Carry continuous: 8 A
 Break (DC inductive, L/R = 40 ms):

VOLTAGE	CURRENT
24 V	1 A
48 V	0.5 A
125 V	0.3 A
250 V	0.2 A

Operate time: < 8 ms
 Contact material: silver alloy

FAST FORM-C RELAY

Make and carry: 0.1 A max. (resistive load)

Minimum load impedance:

INPUT VOLTAGE	IMPEDANCE	
	2 W RESISTOR	1 W RESISTOR
250 V DC	20 K Ω	50 K Ω
120 V DC	5 K Ω	2 K Ω
48 V DC	2 K Ω	2 K Ω
24 V DC	2 K Ω	2 K Ω

Note: values for 24 V and 48 V are the same due to a required 95% voltage drop across the load impedance.

Operate time: < 0.6 ms

Internal Limiting Resistor: 100 Ω , 2 W

SOLID-STATE OUTPUT RELAY

Operate and release time: <100 μ s

Maximum voltage: 265 V DC

Maximum continuous current: 5 A at 45°C; 4 A at 65°C

Make and carry:

for 0.2 s: 30 A as per ANSI C37.90

for 0.03 s: 300 A

Breaking capacity:

	UL508	Utility application (autoreclose scheme)	Industrial application
Operations/interval	5000 ops / 1 s-On, 9 s-Off	5 ops / 0.2 s-On, 0.2 s-Off within 1 minute	10000 ops / 0.2 s-On, 30 s-Off
	1000 ops / 0.5 s-On, 0.5 s-Off		
Break capability (0 to 250 V DC)	3.2 A L/R = 10 ms	10 A L/R = 40 ms	10 A L/R = 40 ms
	1.6 A L/R = 20 ms		
	0.8 A L/R = 40 ms		

IRIG-B OUTPUT

Amplitude: 10 V peak-peak RS485 level

Maximum load: 100 ohms

Time delay: 1 ms for AM input
40 μ s for DC-shift input

Isolation: 2 kV

CONTROL POWER EXTERNAL OUTPUT (FOR DRY CONTACT INPUT)

Capacity: 100 mA DC at 48 V DC

Isolation: \pm 300 Vpk

REMOTE OUTPUTS (IEC 61850 GSSE/GOOSE)

Standard output points: 32

User output points: 32

DIRECT OUTPUTS

Output points: 64

DCMA OUTPUTS

Range: -1 to 1 mA, 0 to 1 mA, 4 to 20 mA

Max. load resistance: 12 k Ω for -1 to 1 mA range

12 k Ω for 0 to 1 mA range

600 Ω for 4 to 20 mA range

Accuracy: \pm 0.75% of full-scale for 0 to 1 mA range
 \pm 0.5% of full-scale for -1 to 1 mA range
 \pm 0.75% of full-scale for 0 to 20 mA range

99% Settling time to a step change: 100 ms

Isolation: 1.5 kV

Driving signal: any FlexAnalog quantity

Upper and lower limit for the driving signal: -90 to 90 pu in steps of 0.001

2.2.8 COMMUNICATIONS**RS232**

Front port: 19.2 kbps, Modbus[®] RTU

RS485

1 or 2 rear ports: Up to 115 kbps, Modbus[®] RTU, isolated together at 36 Vpk

Typical distance: 1200 m

Isolation: 2 kV

ETHERNET (FIBER)

PARAMETER	FIBER TYPE		
	10MB MULTI-MODE	100MB MULTI-MODE	100MB SINGLE-MODE
Wavelength	820 nm	1310 nm	1310 nm
Connector	ST	ST	SC
Transmit power	-20 dBm	-20 dBm	-15 dBm
Receiver sensitivity	-30 dBm	-30 dBm	-30 dBm
Power budget	10 dB	10 dB	15 dB
Maximum input power	-7.6 dBm	-14 dBm	-7 dBm
Typical distance	1.65 km	2 km	15 km
Duplex	full/half	full/half	full/half
Redundancy	yes	yes	yes

ETHERNET (COPPER)

Modes: 10 MB, 10/100 MB (auto-detect)

Connector: RJ45

SNTP clock synchronization error: <10 ms (typical)

2.2.9 INTER-RELAY COMMUNICATIONS

SHIELDED TWISTED-PAIR INTERFACE OPTIONS

INTERFACE TYPE	TYPICAL DISTANCE
RS422	1200 m
G.703	100 m



RS422 distance is based on transmitter power and does not take into consideration the clock source provided by the user.

LINK POWER BUDGET

EMITTER, FIBER TYPE	TRANSMIT POWER	RECEIVED SENSITIVITY	POWER BUDGET
820 nm LED, Multimode	-20 dBm	-30 dBm	10 dB
1300 nm LED, Multimode	-21 dBm	-30 dBm	9 dB
1300 nm ELED, Singlemode	-21 dBm	-30 dBm	9 dB
1300 nm Laser, Singlemode	-1 dBm	-30 dBm	29 dB
1550 nm Laser, Singlemode	+5 dBm	-30 dBm	35 dB



These Power Budgets are calculated from the manufacturer's worst-case transmitter power and worst case receiver sensitivity.

MAXIMUM OPTICAL INPUT POWER

EMITTER, FIBER TYPE	MAX. OPTICAL INPUT POWER
820 nm LED, Multimode	-7.6 dBm
1300 nm LED, Multimode	-11 dBm
1300 nm ELED, Singlemode	-14 dBm
1300 nm Laser, Singlemode	-14 dBm
1550 nm Laser, Singlemode	-14 dBm

TYPICAL LINK DISTANCE

EMITTER TYPE	FIBER TYPE	CONNECTOR TYPE	TYPICAL DISTANCE
820 nm LED	Multimode	ST	1.65 km
1300 nm LED	Multimode	ST	3.8 km
1300 nm ELED	Singlemode	ST	11.4 km
1300 nm Laser	Singlemode	ST	64 km
1550 nm Laser	Singlemode	ST	105 km



Typical distances listed are based on the following assumptions for system loss. As actual losses will vary from one installation to another, the distance covered by your system may vary.

CONNECTOR LOSSES (TOTAL OF BOTH ENDS)

ST connector 2 dB

FIBER LOSSES

820 nm multimode 3 dB/km

1300 nm multimode 1 dB/km

1300 nm singlemode 0.35 dB/km

1550 nm singlemode 0.25 dB/km

Splice losses: One splice every 2 km, at 0.05 dB loss per splice.

SYSTEM MARGIN

3 dB additional loss added to calculations to compensate for all other losses.

Compensated difference in transmitting and receiving (channel asymmetry) channel delays using GPS satellite clock: 10 ms

2.2.10 ENVIRONMENTAL

AMBIENT TEMPERATURES

Operating: -40 to 60°C

Storage: -40 to 80°C



The LCD contrast may be impaired at temperatures less than -20°C.

OTHER

Humidity (non-condensing): IEC 60068-2-30, 95%, Variant 1, 6 days

Altitude: Up to 2000 m

Installation Category: II

2.2.11 TYPE TESTS

Electrical fast transient:	ANSI/IEEE C37.90.1 IEC 61000-4-4 IEC 60255-22-4
Oscillatory transient:	ANSI/IEEE C37.90.1 IEC 61000-4-12
Insulation resistance:	IEC 60255-5
Dielectric strength:	IEC 60255-6 ANSI/IEEE C37.90
Electrostatic discharge:	EN 61000-4-2
Surge immunity:	EN 61000-4-5
RFI susceptibility:	ANSI/IEEE C37.90.2 IEC 61000-4-3 IEC 60255-22-3 Ontario Hydro C-5047-77
Conducted RFI:	IEC 61000-4-6

Voltage dips/interruptions/variatio	IEC 61000-4-11 IEC 60255-11
Power frequency magnetic field immunity:	IEC 61000-4-8
Pulse magnetic field immunity:	IEC 61000-4-9
Vibration test (sinusoidal):	IEC 60255-21-1
Shock and bump:	IEC 60255-21-2
Seismic:	IEC 60255-21-3 IEEE C37.98
Cold:	IEC 60028-2-1, 16 h at -40°C
Dry heat:	IEC 60028-2-2, 16 h at 85°C



NOTE

Type test report available upon request.

2.2.12 PRODUCTION TESTS

THERMAL

Products go through an environmental test based upon an Accepted Quality Level (AQL) sampling process.

2.2.13 APPROVALS

APPROVALS

UL Listed for the USA and Canada

CE:

LVD 73/23/EEC:	IEC 1010-1
EMC 81/336/EEC:	EN 50081-2, EN 50082-2

2.2.14 MAINTENANCE

MOUNTING

Attach mounting brackets using 20 inch-pounds (± 2 inch-pounds) of torque.

CLEANING

Normally, cleaning is not required; but for situations where dust has accumulated on the faceplate display, a dry cloth can be used.



NOTE

Units that are stored in a de-energized state should be powered up once per year, for one hour continuously, to avoid deterioration of electrolytic capacitors.

3.1.1 PANEL CUTOUT

The relay is available as a 19-inch rack horizontal mount unit with a removable faceplate. The modular design allows the relay to be easily upgraded or repaired by a qualified service person. The faceplate is hinged to allow easy access to the removable modules, and is itself removable to allow mounting on doors with limited rear depth. There is also a removable dust cover that fits over the faceplate, which must be removed when attempting to access the keypad or RS232 communications port.

The horizontal case dimensions are shown below, along with panel cutout details for panel mounting. When planning the location of your panel cutout, ensure that provision is made for the faceplate to swing open without interference to or from adjacent equipment.

The relay must be mounted such that the faceplate sits semi-flush with the panel or switchgear door, allowing the operator access to the keypad and the RS232 communications port. The relay is secured to the panel with the use of four screws supplied with the relay.

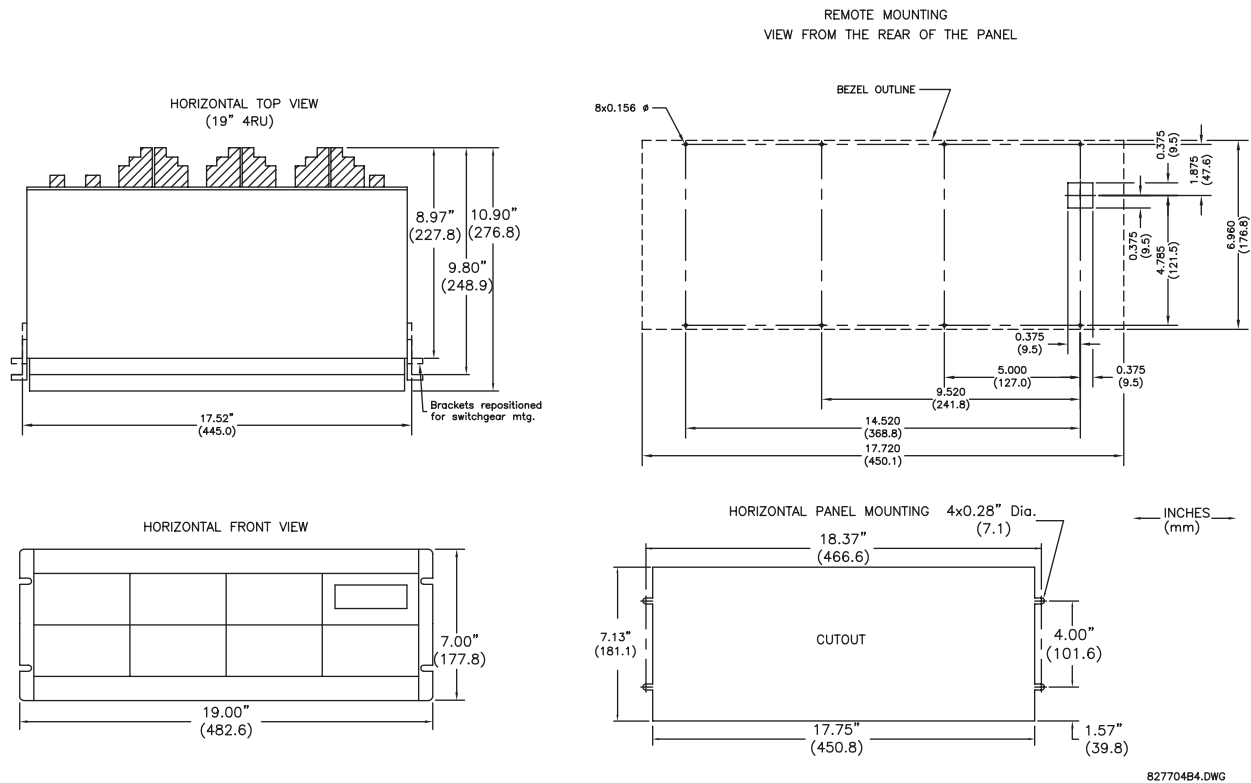


Figure 3-1: N60 HORIZONTAL MOUNTING AND DIMENSIONS

3.1.2 MODULE WITHDRAWAL AND INSERTION



Module withdrawal and insertion may only be performed when control power has been removed from the unit. Inserting an incorrect module type into a slot may result in personal injury, damage to the unit or connected equipment, or undesired operation!



Proper electrostatic discharge protection (i.e. a static strap) must be used when coming in contact with modules while the relay is energized!

The relay, being modular in design, allows for the withdrawal and insertion of modules. Modules must only be replaced with like modules in their original factory configured slots. The faceplate can be opened to the left, once the sliding latch on the right side has been pushed up, as shown below. This allows for easy accessibility of the modules for withdrawal.



Figure 3-2: UR MODULE WITHDRAWAL/INSERTION

- **MODULE WITHDRAWAL:** The ejector/inserter clips, located at the top and bottom of each module, must be pulled simultaneously to release the module for removal. Before performing this action, **control power must be removed from the relay**. Record the original location of the module to ensure that the same or replacement module is inserted into the correct slot. Modules with current input provide automatic shorting of external CT circuits.
- **MODULE INSERTION:** Ensure that the **correct** module type is inserted into the **correct** slot position. The ejector/insertor clips located at the top and at the bottom of each module must be in the disengaged position as the module is smoothly inserted into the slot. Once the clips have cleared the raised edge of the chassis, engage the clips simultaneously. When the clips have locked into position, the module will be fully inserted.



All CPU modules except the 9E are equipped with 10Base-T or 10Base-F Ethernet connectors. These connectors must be individually disconnected from the module before it can be removed from the chassis.



The version 4.0 release of the N60 relay includes new hardware (CPU and CT/VT modules). The new CPU modules are specified with the following order codes: 9E, 9G, 9H, 9J, 9K, 9L, 9M, 9N, 9P, 9R. The new CT/VT modules are specified with the following order codes: 8F, 8G, 8H, 8J.

The new CT/VT modules (8F, 8G, 8H, 8J) can only be used with new CPUs (9E, 9G, 9H, 9J, 9K, 9L, 9M, 9N, 9P, 9R); similarly, old CT/VT modules (8A, 8B, 8C, 8D) can only be used with old CPUs (9A, 9C, 9D). To prevent hardware mismatches, the new modules have blue labels and a warning sticker stating “**Attn.: Ensure CPU and DSP module label colors are the same!**”. In the event that there is a mismatch between the CPU and CT/VT module, the relay will not function and a **DSP ERROR** or **HARDWARE MISMATCH** error will be displayed.

All other input/output modules are compatible with the new hardware. Firmware versions 4.0x and higher are only compatible with the new CPU and CT/VT modules. Previous versions of the firmware (3.4x and earlier) are only compatible with the older CPU and CT/VT modules.

3.1.3 REAR TERMINAL LAYOUT

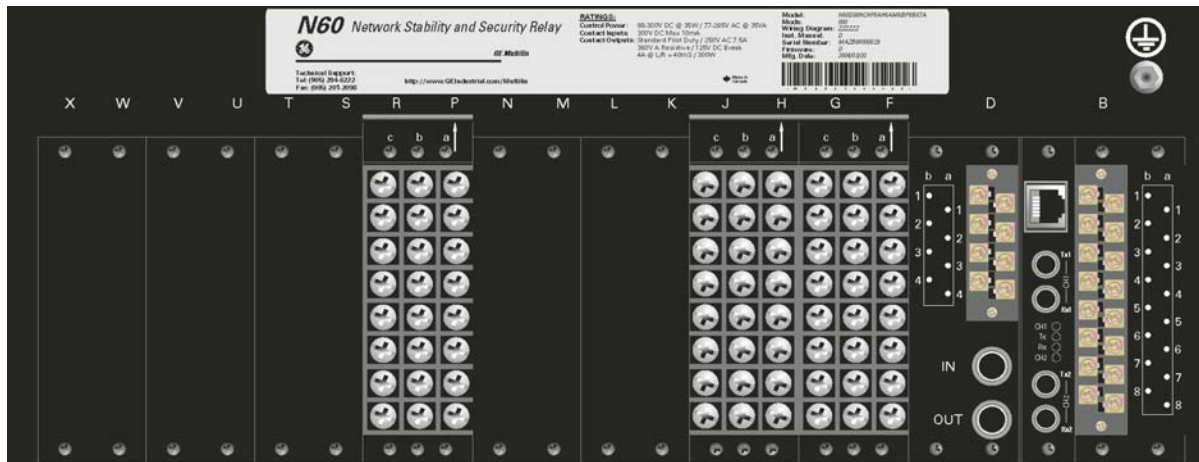


Figure 3–3: REAR TERMINAL VIEW



Do not touch any rear terminals while the relay is energized!

The relay follows a convention with respect to terminal number assignments which are three characters long assigned in order by module slot position, row number, and column letter. Two-slot wide modules take their slot designation from the first slot position (nearest to CPU module) which is indicated by an arrow marker on the terminal block. See the following figure for an example of rear terminal assignments.

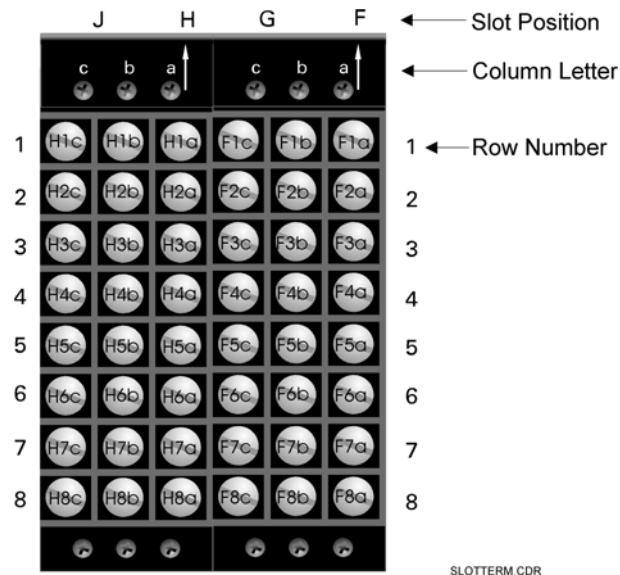


Figure 3–4: EXAMPLE OF MODULES IN F AND H SLOTS

3.2.1 TYPICAL WIRING

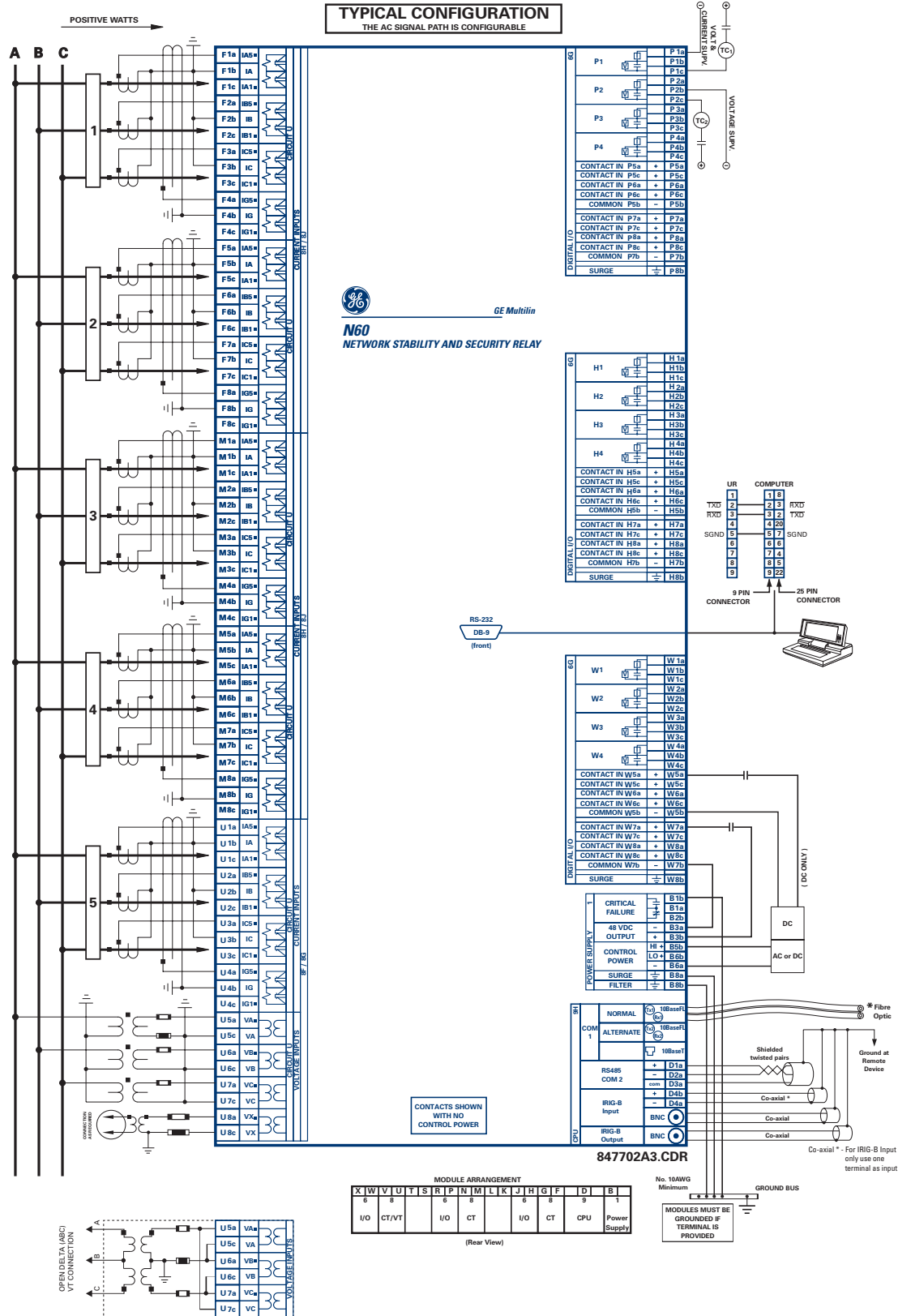


Figure 3-5: TYPICAL WIRING DIAGRAM

3.2.2 DIELECTRIC STRENGTH

The dielectric strength of the UR-series module hardware is shown in the following table:

Table 3–1: DIELECTRIC STRENGTH OF UR-SERIES MODULE HARDWARE

MODULE TYPE	MODULE FUNCTION	TERMINALS		DIELECTRIC STRENGTH (AC)
		FROM	TO	
1	Power Supply	High (+); Low (+); (–)	Chassis	2000 V AC for 1 minute
1	Power Supply	48 V DC (+) and (–)	Chassis	2000 V AC for 1 minute
1	Power Supply	Relay Terminals	Chassis	2000 V AC for 1 minute
2	Reserved	N/A	N/A	N/A
3	Reserved	N/A	N/A	N/A
4	Reserved	N/A	N/A	N/A
5	Analog Inputs/Outputs	All except 8b	Chassis	< 50 V DC
6	Digital Inputs/Outputs	All	Chassis	2000 V AC for 1 minute
7	G.703	All except 2b, 3a, 7b, 8a	Chassis	2000 V AC for 1 minute
	RS422	All except 6a, 7b, 8a	Chassis	< 50 V DC
8	CT/VT	All	Chassis	2000 V AC for 1 minute
9	CPU	All	Chassis	2000 V AC for 1 minute

Filter networks and transient protection clamps are used in the hardware to prevent damage caused by high peak voltage transients, radio frequency interference (RFI), and electromagnetic interference (EMI). These protective components **can be damaged** by application of the ANSI/IEEE C37.90 specified test voltage for a period longer than the specified one minute.

3.2.3 CONTROL POWER



CONTROL POWER SUPPLIED TO THE RELAY MUST BE CONNECTED TO THE MATCHING POWER SUPPLY RANGE OF THE RELAY. IF THE VOLTAGE IS APPLIED TO THE WRONG TERMINALS, DAMAGE MAY OCCUR!



The N60 relay, like almost all electronic relays, contains electrolytic capacitors. These capacitors are well known to be subject to deterioration over time if voltage is not applied periodically. Deterioration can be avoided by powering the relays up once a year.

The power supply module can be ordered for two possible voltage ranges, with or without a redundant power option. Each range has a dedicated input connection for proper operation. The ranges are as shown below (see the *Technical Specifications* section of Chapter 2 for additional details):

LO range: 24 to 48 V (DC only) nominal
 HI range: 125 to 250 V nominal

The power supply module provides power to the relay and supplies power for dry contact input connections.

The power supply module provides 48 V DC power for dry contact input connections and a critical failure relay (see the *Typical Wiring Diagram* earlier). The critical failure relay is a Form-C that will be energized once control power is applied and the relay has successfully booted up with no critical self-test failures. If on-going self-test diagnostic checks detect a critical failure (see the *Self-Test Errors* table in Chapter 7) or control power is lost, the relay will de-energize.

For high reliability systems, the N60 has a redundant option in which two N60 power supplies are placed in parallel on the bus. If one of the power supplies become faulted, the second power supply will assume the full load of the relay without any interruptions. Each power supply has a green LED on the front of the module to indicate it is functional. The critical fail relay of the module will also indicate a faulted power supply.

An LED on the front of the module shows the status of the power supply:

LED INDICATION	POWER SUPPLY
ON	OK
ON / OFF CYCLING	Failure
OFF	Failure

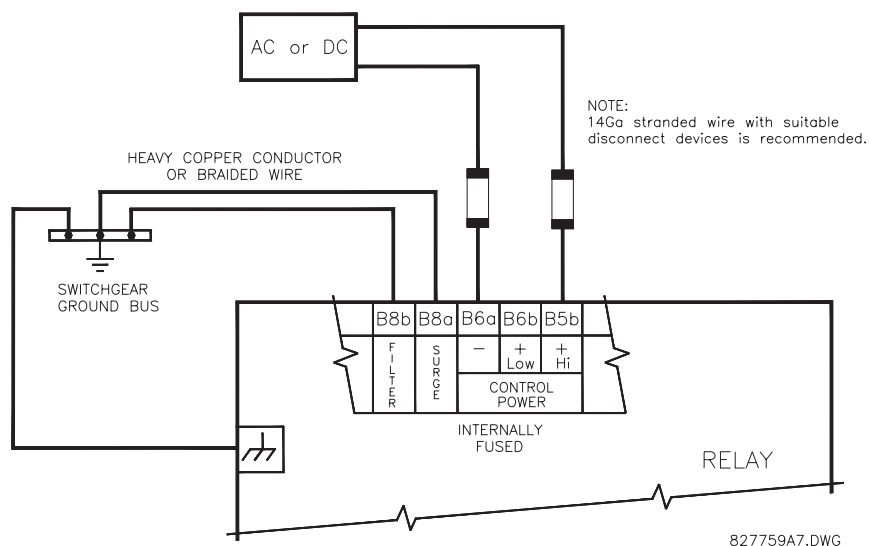


Figure 3-6: CONTROL POWER CONNECTION

3.2.4 CT/VT MODULES

A CT/VT module may have voltage inputs on Channels 1 through 4 inclusive, or Channels 5 through 8 inclusive. Channels 1 and 5 are intended for connection to Phase A, and are labeled as such in the relay. Channels 2 and 6 are intended for connection to Phase B, and are labeled as such in the relay. Channels 3 and 7 are intended for connection to Phase C and are labeled as such in the relay. Channels 4 and 8 are intended for connection to a single phase source. If voltage, this channel is labelled the auxiliary voltage (VX). If current, this channel is intended for connection to a CT between a system neutral and ground, and is labelled the ground current (IG).

a) CT INPUTS



VERIFY THAT THE CONNECTION MADE TO THE RELAY NOMINAL CURRENT OF 1 A OR 5 A MATCHES THE SECONDARY RATING OF THE CONNECTED CTs. UNMATCHED CTs MAY RESULT IN EQUIPMENT DAMAGE OR INADEQUATE PROTECTION.

The CT/VT module may be ordered with a standard ground current input that is the same as the phase current inputs (Type 8F) or with a sensitive ground input (Type 8G) which is 10 times more sensitive (see the Technical Specifications section for additional details). Each AC current input has an isolating transformer and an automatic shorting mechanism that shorts the input when the module is withdrawn from the chassis. There are no internal ground connections on the current inputs. Current transformers with 1 to 50000 A primaries and 1 A or 5 A secondaries may be used.

CT connections for both ABC and ACB phase rotations are identical as shown in the *Typical Wiring Diagram*.

The exact placement of a zero-sequence CT so that ground fault current will be detected is shown below. Twisted pair cabling on the zero-sequence CT is recommended.

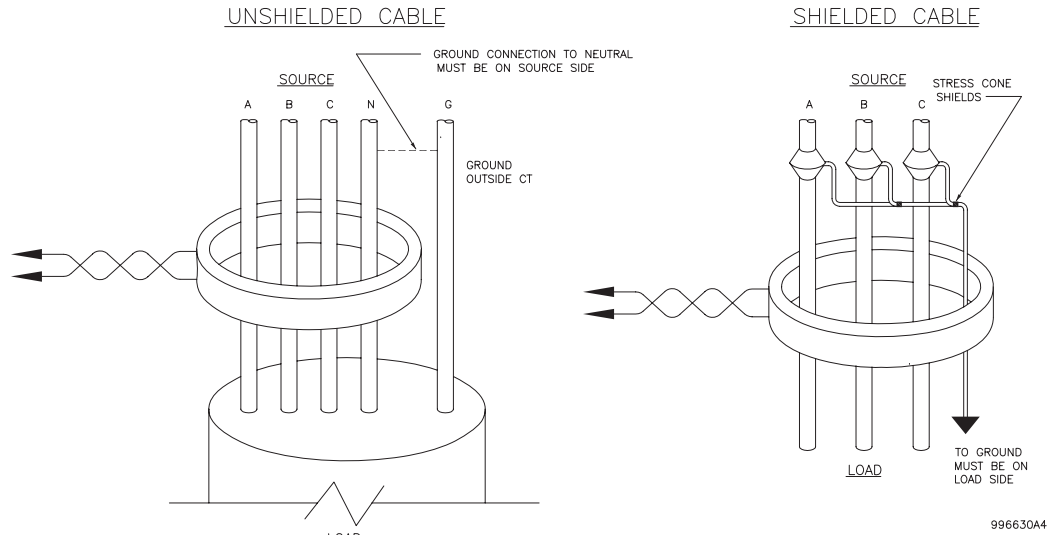


Figure 3-7: ZERO-SEQUENCE CORE BALANCE CT INSTALLATION

b) VT INPUTS

The phase voltage channels are used for most metering and protection purposes. The auxiliary voltage channel is used as input for the Synchrocheck and Volts/Hertz features.

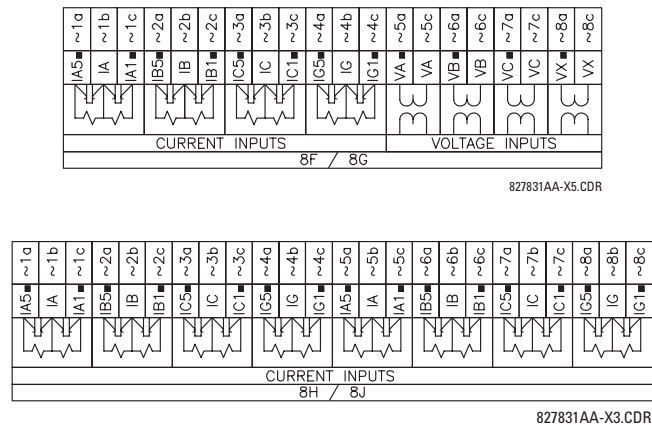


Figure 3-8: CT/VT MODULE WIRING



Wherever a tilde “~” symbol appears, substitute with the Slot Position of the module.

3.2.5 CONTACT INPUTS/OUTPUTS

Every digital input/output module has 24 terminal connections. They are arranged as three terminals per row, with eight rows in total. A given row of three terminals may be used for the outputs of one relay. For example, for Form-C relay outputs, the terminals connect to the normally open (NO), normally closed (NC), and common contacts of the relay. For a Form-A output, there are options of using current or voltage detection for feature supervision, depending on the module ordered. The terminal configuration for contact inputs is different for the two applications.

The digital inputs are grouped with a common return. The N60 has two versions of grouping: four inputs per common return and two inputs per common return. When a digital input/output module is ordered, four inputs per common is used. The four inputs per common allows for high-density inputs in combination with outputs, with a compromise of four inputs sharing one common. If the inputs must be isolated per row, then two inputs per common return should be selected (4D module).

The tables and diagrams on the following pages illustrate the module types (6A, etc.) and contact arrangements that may be ordered for the relay. Since an entire row is used for a single contact output, the name is assigned using the module slot position and row number. However, since there are two contact inputs per row, these names are assigned by module slot position, row number, and column position.

UR-SERIES FORM-A / SOLID STATE (SSR) OUTPUT CONTACTS:

Some Form-A/SSR outputs include circuits to monitor the DC voltage across the output contact when it is open, and the DC current through the output contact when it is closed. Each of the monitors contains a level detector whose output is set to logic “On = 1” when the current in the circuit is above the threshold setting. The voltage monitor is set to “On = 1” when the current is above about 1 to 2.5 mA, and the current monitor is set to “On = 1” when the current exceeds about 80 to 100 mA. The voltage monitor is intended to check the health of the overall trip circuit, and the current monitor can be used to seal-in the output contact until an external contact has interrupted current flow. The block diagrams of the circuits are below above for the Form-A outputs with:

- a) optional voltage monitor
- b) optional current monitor
- c) with no monitoring

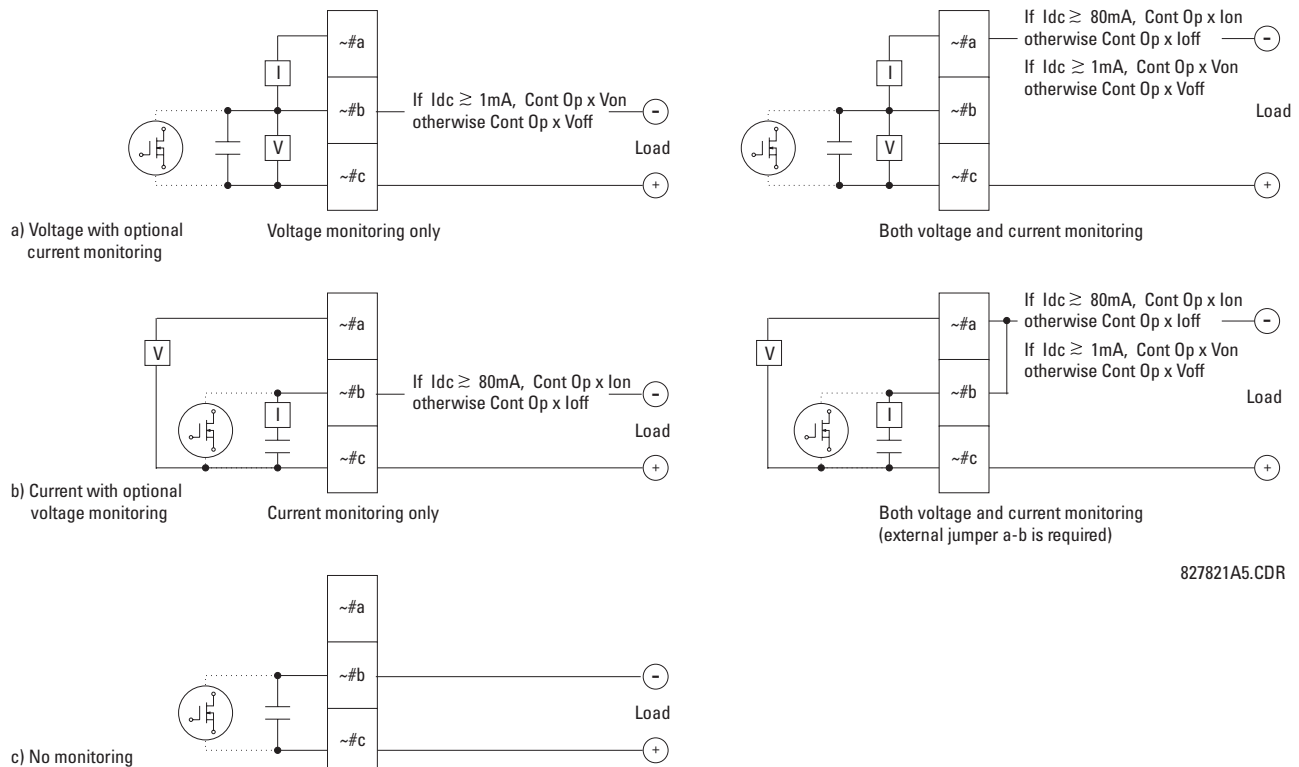


Figure 3-9: FORM-A /SOLID STATE CONTACT FUNCTIONS

The operation of voltage and current monitors is reflected with the corresponding FlexLogic™ operands (Cont Op # Von, Cont Op # Voff, Cont Op # Ion, and Cont Op # Ioff) which can be used in protection, control and alarm logic. The typical application of the voltage monitor is breaker trip circuit integrity monitoring; a typical application of the current monitor is seal-in of the control command. Refer to the *Digital Elements* section of Chapter 5 for an example of how Form-A/SSR contacts can be applied for breaker trip circuit integrity monitoring.



WARNING

Relay contacts must be considered unsafe to touch when the unit is energized! If the relay contacts need to be used for low voltage accessible applications, it is the customer's responsibility to ensure proper insulation levels!



NOTE

USE OF FORM-A/SSR OUTPUTS IN HIGH IMPEDANCE CIRCUITS

For Form-A/SSR output contacts internally equipped with a voltage measuring circuit across the contact, the circuit has an impedance that can cause a problem when used in conjunction with external high input impedance monitoring equipment such as modern relay test set trigger circuits. These monitoring circuits may continue to read the Form-A contact as being closed after it has closed and subsequently opened, when measured as an impedance.

The solution to this problem is to use the voltage measuring trigger input of the relay test set, and connect the Form-A contact through a voltage-dropping resistor to a DC voltage source. If the 48 V DC output of the power supply is used as a source, a 500 Ω, 10 W resistor is appropriate. In this configuration, the voltage across either the Form-A contact or the resistor can be used to monitor the state of the output.



NOTE

Wherever a tilde “~” symbol appears, substitute with the slot position of the module; wherever a number sign “#” appears, substitute the contact number



NOTE

When current monitoring is used to seal-in the Form-A/SSR contact outputs, the FlexLogic™ operand driving the contact output should be given a reset delay of 10 ms to prevent damage of the output contact (in situations when the element initiating the contact output is bouncing, at values in the region of the pickup value).

Table 3–2: DIGITAL INPUT/OUTPUT MODULE ASSIGNMENTS

~6A MODULE		~6B MODULE		~6C MODULE		~6D MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT	TERMINAL ASSIGNMENT	OUTPUT OR INPUT	TERMINAL ASSIGNMENT	OUTPUT	TERMINAL ASSIGNMENT	OUTPUT
~1	Form-A	~1	Form-A	~1	Form-C	~1a, ~1c	2 Inputs
~2	Form-A	~2	Form-A	~2	Form-C	~2a, ~2c	2 Inputs
~3	Form-C	~3	Form-C	~3	Form-C	~3a, ~3c	2 Inputs
~4	Form-C	~4	Form-C	~4	Form-C	~4a, ~4c	2 Inputs
~5a, ~5c	2 Inputs	~5	Form-C	~5	Form-C	~5a, ~5c	2 Inputs
~6a, ~6c	2 Inputs	~6	Form-C	~6	Form-C	~6a, ~6c	2 Inputs
~7a, ~7c	2 Inputs	~7a, ~7c	2 Inputs	~7	Form-C	~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs	~8a, ~8c	2 Inputs	~8	Form-C	~8a, ~8c	2 Inputs

~6E MODULE		~6F MODULE		~6G MODULE		~6H MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT	TERMINAL ASSIGNMENT	OUTPUT	TERMINAL ASSIGNMENT	OUTPUT OR INPUT	TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-C	~1	Fast Form-C	~1	Form-A	~1	Form-A
~2	Form-C	~2	Fast Form-C	~2	Form-A	~2	Form-A
~3	Form-C	~3	Fast Form-C	~3	Form-A	~3	Form-A
~4	Form-C	~4	Fast Form-C	~4	Form-A	~4	Form-A
~5a, ~5c	2 Inputs	~5	Fast Form-C	~5a, ~5c	2 Inputs	~5	Form-A
~6a, ~6c	2 Inputs	~6	Fast Form-C	~6a, ~6c	2 Inputs	~6	Form-A
~7a, ~7c	2 Inputs	~7	Fast Form-C	~7a, ~7c	2 Inputs	~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs	~8	Fast Form-C	~8a, ~8c	2 Inputs	~8a, ~8c	2 Inputs

~6K MODULE	
TERMINAL ASSIGNMENT	OUTPUT
~1	Form-C
~2	Form-C
~3	Form-C
~4	Form-C
~5	Fast Form-C
~6	Fast Form-C
~7	Fast Form-C
~8	Fast Form-C

~6L MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-C
~4	Form-C
~5a, ~5c	2 Inputs
~6a, ~6c	2 Inputs
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~6M MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-C
~4	Form-C
~5	Form-C
~6	Form-C
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~6N MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-A
~4	Form-A
~5a, ~5c	2 Inputs
~6a, ~6c	2 Inputs
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~6P MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-A
~4	Form-A
~5	Form-A
~6	Form-A
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~6R MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-C
~4	Form-C
~5a, ~5c	2 Inputs
~6a, ~6c	2 Inputs
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~6S MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-C
~4	Form-C
~5	Form-C
~6	Form-C
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~6T MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-A
~4	Form-A
~5a, ~5c	2 Inputs
~6a, ~6c	2 Inputs
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~6U MODULE	
TERMINAL ASSIGNMENT	OUTPUT OR INPUT
~1	Form-A
~2	Form-A
~3	Form-A
~4	Form-A
~5	Form-A
~6	Form-A
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~67 MODULE	
TERMINAL ASSIGNMENT	OUTPUT
~1	Form-A
~2	Form-A
~3	Form-A
~4	Form-A
~5	Form-A
~6	Form-A
~7	Form-A
~8	Form-A

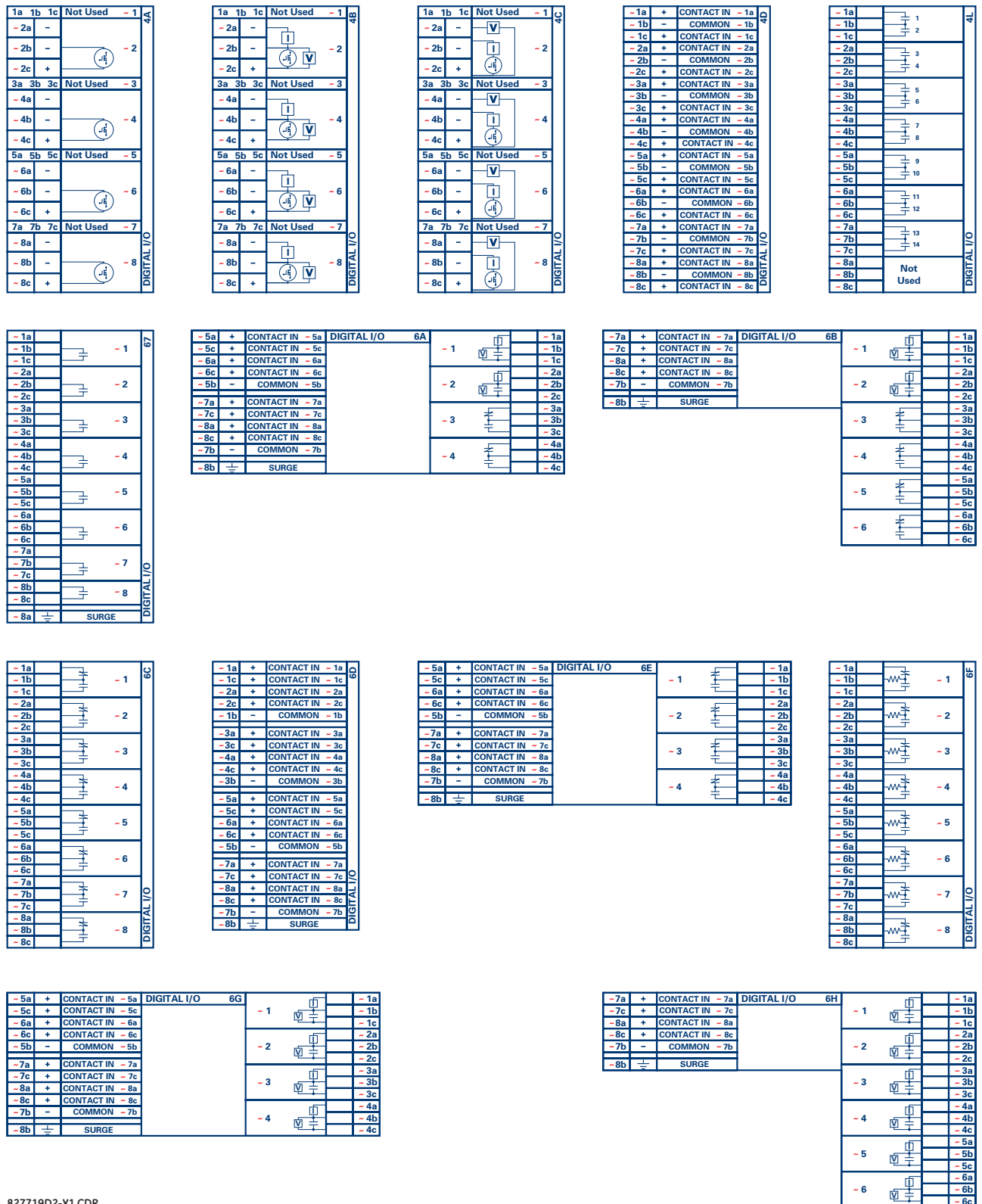
~4A MODULE	
TERMINAL ASSIGNMENT	OUTPUT
~1	Not Used
~2	Solid-State
~3	Not Used
~4	Solid-State
~5	Not Used
~6	Solid-State
~7	Not Used
~8	Solid-State

~4B MODULE	
TERMINAL ASSIGNMENT	OUTPUT
~1	Not Used
~2	Solid-State
~3	Not Used
~4	Solid-State
~5	Not Used
~6	Solid-State
~7	Not Used
~8	Solid-State

~4C MODULE	
TERMINAL ASSIGNMENT	OUTPUT
~1	Not Used
~2	Solid-State
~3	Not Used
~4	Solid-State
~5	Not Used
~6	Solid-State
~7	Not Used
~8	Solid-State

~4D MODULE	
TERMINAL ASSIGNMENT	OUTPUT
~1a, ~1c	2 Inputs
~2a, ~2c	2 Inputs
~3a, ~3c	2 Inputs
~4a, ~4c	2 Inputs
~5a, ~5c	2 Inputs
~6a, ~6c	2 Inputs
~7a, ~7c	2 Inputs
~8a, ~8c	2 Inputs

~4L MODULE	
TERMINAL ASSIGNMENT	OUTPUT
~1	2 Outputs
~2	2 Outputs
~3	2 Outputs
~4	2 Outputs
~5	2 Outputs
~6	2 Outputs
~7	2 Outputs
~8	Not Used



827719D2-X1.CDR

Figure 3-10: DIGITAL INPUT/OUTPUT MODULE WIRING (1 of 2)

3



CAUTION

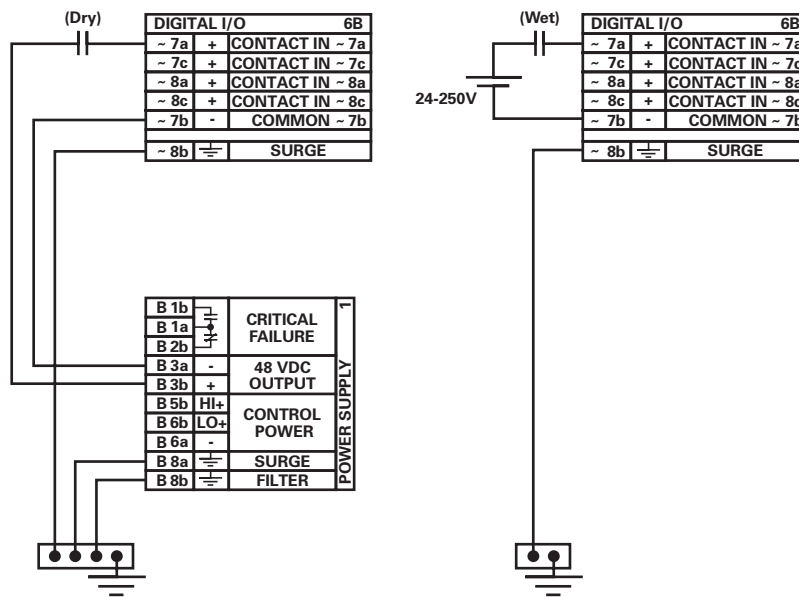
GE Multilin

CONTACT INPUTS:

A dry contact has one side connected to Terminal B3b. This is the positive 48 V DC voltage rail supplied by the power supply module. The other side of the dry contact is connected to the required contact input terminal. Each contact input group has its own common (negative) terminal which must be connected to the DC negative terminal (B3a) of the power supply module. When a dry contact closes, a current of 1 to 3 mA will flow through the associated circuit.

A wet contact has one side connected to the positive terminal of an external DC power supply. The other side of this contact is connected to the required contact input terminal. In addition, the negative side of the external source must be connected to the relay common (negative) terminal of each contact input group. The maximum external source voltage for this arrangement is 300 V DC.

The voltage threshold at which each group of four contact inputs will detect a closed contact input is programmable as 17 V DC for 24 V sources, 33 V DC for 48 V sources, 84 V DC for 110 to 125 V sources, and 166 V DC for 250 V sources.



827741A4.CDR

Figure 3-12: DRY AND WET CONTACT INPUT CONNECTIONS



Wherever a tilde “~” symbol appears, substitute with the Slot Position of the module.

NOTE

CONTACT OUTPUTS:

Contact outputs may be ordered as Form-A or Form-C. The Form A contacts may be connected for external circuit supervision. These contacts are provided with voltage and current monitoring circuits used to detect the loss of DC voltage in the circuit, and the presence of DC current flowing through the contacts when the Form-A contact closes. If enabled, the current monitoring can be used as a seal-in signal to ensure that the Form-A contact does not attempt to break the energized inductive coil circuit and weld the output contacts.



There is no provision in the relay to detect a DC ground fault on 48 V DC control power external output. We recommend using an external DC supply.

NOTE

USE OF CONTACT INPUTS WITH AUTO-BURNISHING:

The contact inputs sense a change of the state of the external device contact based on the measured current. When external devices are located in a harsh industrial environment (either outdoor or indoor), their contacts can be exposed to various types of contamination. Normally, there is a thin film of insulating sulfidation, oxidation, or contaminants on the surface of the contacts, sometimes making it difficult or impossible to detect a change of the state. This film must be removed to establish circuit continuity – an impulse of higher than normal current can accomplish this.

The contact inputs with auto-burnish create a high current impulse when the threshold is reached to burn off this oxidation layer as a maintenance to the contacts. Afterwards the contact input current is reduced to a steady-state current. The impulse will have a 5 second delay after a contact input changes state.

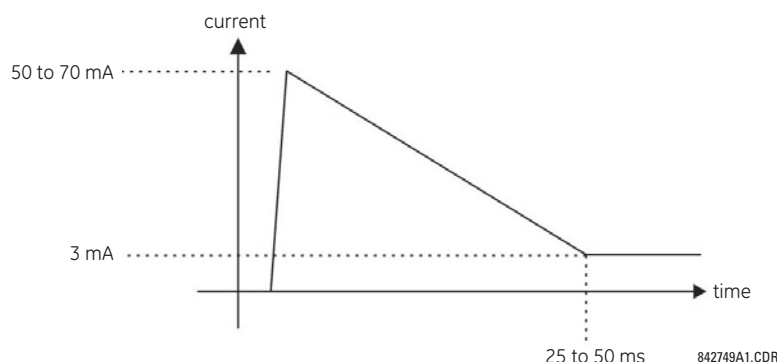


Figure 3-13: CURRENT THROUGH CONTACT INPUTS WITH AUTO-BURNISHING

Regular contact inputs limit current to less than 3 mA to reduce station battery burden. In contrast, contact inputs with auto-burnishing allow currents up to 50 to 70 mA at the first instance when the change of state was sensed. Then, within 25 to 50 ms, this current is slowly reduced to 3 mA as indicated above. The 50 to 70 mA peak current burns any film on the contacts, allowing for proper sensing of state changes. If the external device contact is bouncing, the auto-burnishing starts when external device contact bouncing is over.

Another important difference between the auto-burnishing input module and the regular input modules is that only two contact inputs have common ground, as opposed to four contact inputs sharing one common ground (refer to the *Digital Input/Output Module Wiring* diagrams). This is beneficial when connecting contact inputs to separate voltage sources. Consequently, the threshold voltage setting is also defined per group of two contact inputs.

The auto-burnish feature can be disabled or enabled using the DIP switches found on each daughter card. There is a DIP switch for each contact, for a total of 16 inputs.

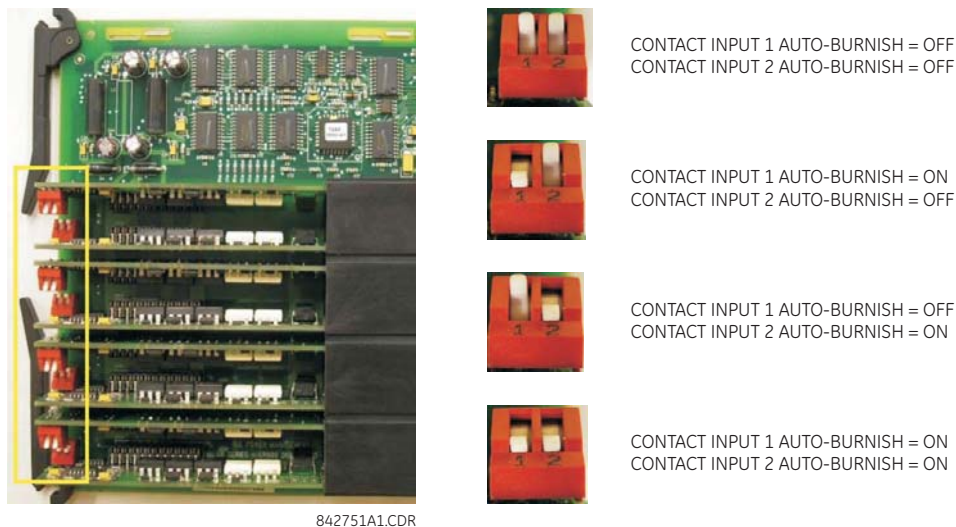


Figure 3-14: AUTO-BURNISH DIP SWITCHES



The auto-burnish circuitry has an internal fuse for safety purposes. During regular maintenance, the auto-burnish functionality can be checked using an oscilloscope.

3.2.6 TRANSDUCER INPUTS/OUTPUTS

Transducer input modules can receive input signals from external dcmA output transducers (dcmA In) or resistance temperature detectors (RTD). Hardware and software is provided to receive signals from these external transducers and convert these signals into a digital format for use as required.

Transducer output modules provide DC current outputs in several standard dcmA ranges. Software is provided to configure virtually any analog quantity used in the relay to drive the analog outputs.

Every transducer input/output module has a total of 24 terminal connections. These connections are arranged as three terminals per row with a total of eight rows. A given row may be used for either inputs or outputs, with terminals in column "a" having positive polarity and terminals in column "c" having negative polarity. Since an entire row is used for a single input/output channel, the name of the channel is assigned using the module slot position and row number.

Each module also requires that a connection from an external ground bus be made to Terminal 8b. The current outputs require a twisted-pair shielded cable, where the shield is grounded at one end only. The figure below illustrates the transducer module types (5A, 5C, 5D, 5E, and 5F) and channel arrangements that may be ordered for the relay.



Wherever a tilde “~” symbol appears, substitute with the Slot Position of the module.

~1a	+	dcmA In ~1	5A
~1c	-		
~2a	+	dcmA In ~2	
~2c	-		
~3a	+	dcmA In ~3	
~3c	-		
~4a	+	dcmA In ~4	
~4c	-		
~5a	+	dcmA Out ~5	
~5c	-		
~6a	+	dcmA Out ~6	
~6c	-		
~7a	+	dcmA Out ~7	
~7c	-		
~8a	+	dcmA Out ~8	
~8c	-		
~8b	⏏	SURGE	ANALOG I/O

~1a	Hot	RTD ~1	5C
~1c	Comp		
~1b	Return	for RTD ~1& ~2	
~2a	Hot	RTD ~2	
~2c	Comp		
~3a	Hot	RTD ~3	
~3c	Comp		
~3b	Return	for RTD ~3& ~4	
~4a	Hot	RTD ~4	
~4c	Comp		
~5a	Hot	RTD ~5	
~5c	Comp		
~5b	Return	for RTD ~5& ~6	
~6a	Hot	RTD ~6	
~6c	Comp		
~7a	Hot	RTD ~7	
~7c	Comp		
~7b	Return	for RTD ~7& ~8	
~8a	Hot	RTD ~8	
~8c	Comp		
~8b	⏏	SURGE	ANALOG I/O

~1a	Hot	RTD ~1	5D
~1c	Comp		
~1b	Return	for RTD ~1& ~2	
~2a	Hot	RTD ~2	
~2c	Comp		
~3a	Hot	RTD ~3	
~3c	Comp		
~3b	Return	for RTD ~3& ~4	
~4a	Hot	RTD ~4	
~4c	Comp		
~5a	+	dcmA Out ~5	
~5c	-		
~6a	+	dcmA Out ~6	
~6c	-		
~7a	+	dcmA Out ~7	
~7c	-		
~8a	+	dcmA Out ~8	
~8c	-		
~8b	⏏	SURGE	ANALOG I/O

~1a	+	dcmA In ~1	5E
~1c	-		
~2a	+	dcmA In ~2	
~2c	-		
~3a	+	dcmA In ~3	
~3c	-		
~4a	+	dcmA In ~4	
~4c	-		
~5a	Hot	RTD ~5	
~5c	Comp		
~5b	Return	for RTD ~5& ~6	
~6a	Hot	RTD ~6	
~6c	Comp		
~7a	Hot	RTD ~7	
~7c	Comp		
~7b	Return	for RTD ~7& ~8	
~8a	Hot	RTD ~8	
~8c	Comp		
~8b	⏏	SURGE	ANALOG I/O

~1a	+	dcmA In ~1	5F
~1c	-		
~2a	+	dcmA In ~2	
~2c	-		
~3a	+	dcmA In ~3	
~3c	-		
~4a	+	dcmA In ~4	
~4c	-		
~5a	+	dcmA In ~5	
~5c	-		
~6a	+	dcmA In ~6	
~6c	-		
~7a	+	dcmA In ~7	
~7c	-		
~8a	+	dcmA In ~8	
~8c	-		
~8b	⏏	SURGE	ANALOG I/O

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Figure 3–15: TRANSDUCER INPUT/OUTPUT MODULE WIRING

3.2.7 RS232 FACEPLATE PORT

A 9-pin RS232C serial port is located on the relay's faceplate for programming with a portable (personal) computer. All that is required to use this interface is a personal computer running the EnerVista UR Setup software provided with the relay. Cabling for the RS232 port is shown in the following figure for both 9 pin and 25 pin connectors.



The baud rate for this port is fixed at **19200 bps**.

NOTE

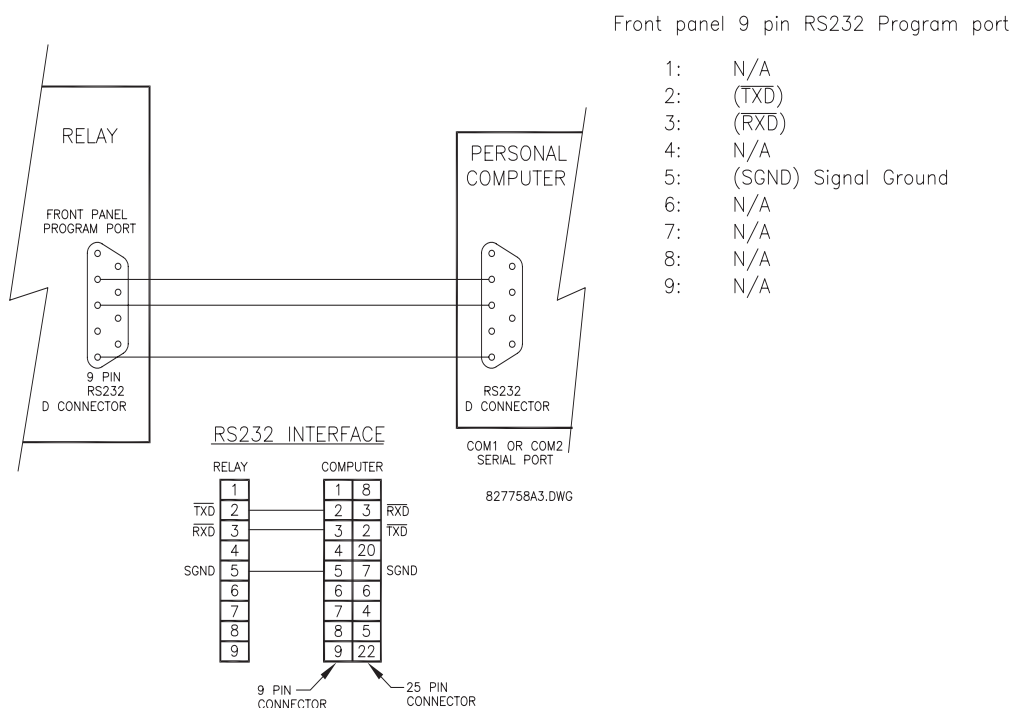


Figure 3–16: RS232 FACEPLATE PORT CONNECTION

3.2.8 CPU COMMUNICATION PORTS

a) OPTIONS

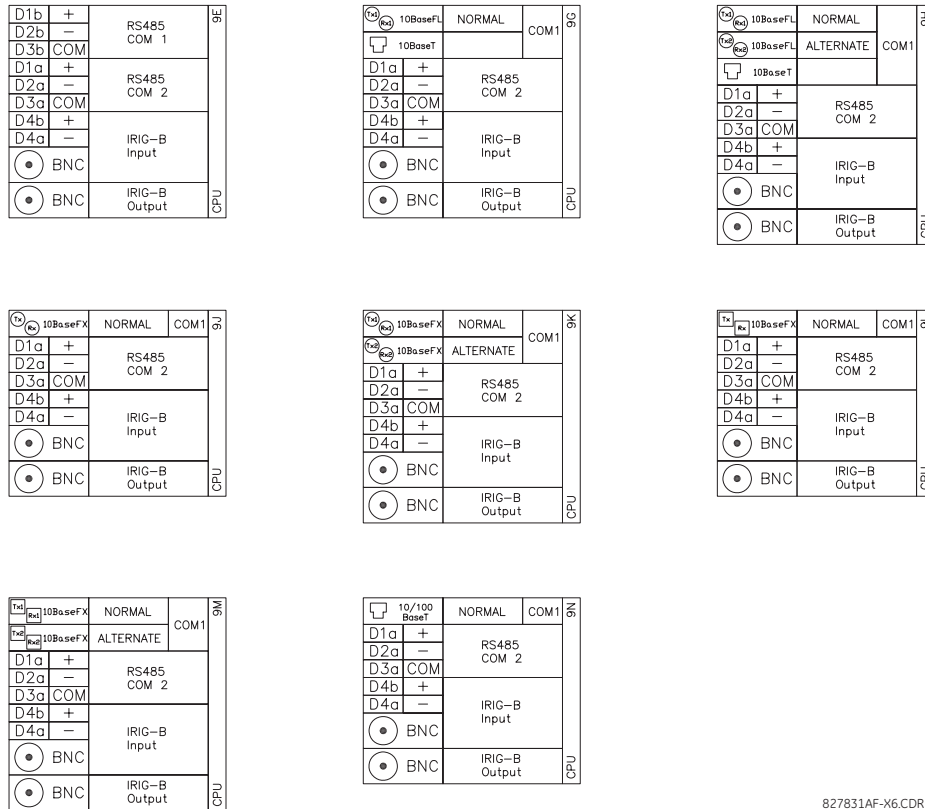
In addition to the RS232 port on the faceplate, the relay provides the user with two additional communication port(s) depending on the CPU module installed.



The CPU modules do not require a surge ground connection.

NOTE

CPU TYPE	COM1	COM2
9E	RS485	RS485
9G	10Base-F and 10Base-T	RS485
9H	Redundant 10Base-F	RS485
9J	10Base-FX	RS485
9K	Redundant 10Base-FX	RS485
9L	100Base-FX	RS485
9M	Redundant 100Base-FX	RS485
9N	10/100Base-T	RS485
9P	100Base-FX	RS485
9R	Redundant 100Base-FX	RS485



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Figure 3-17: CPU MODULE COMMUNICATIONS WIRING

b) RS485 PORTS

RS485 data transmission and reception are accomplished over a single twisted pair with transmit and receive data alternating over the same two wires. Through the use of these port(s), continuous monitoring and control from a remote computer, SCADA system or PLC is possible.

To minimize errors from noise, the use of shielded twisted pair wire is recommended. Correct polarity must also be observed. For instance, the relays must be connected with all RS485 "+" terminals connected together, and all RS485 "-" terminals connected together. The COM terminal should be connected to the common wire inside the shield, when provided. To avoid loop currents, the shield should be grounded at one point only. Each relay should also be daisy chained to the next one in the link. A maximum of 32 relays can be connected in this manner without exceeding driver capability. For larger systems, additional serial channels must be added. It is also possible to use commercially available repeaters to increase the number of relays on a single channel to more than 32. Star or stub connections should be avoided entirely.

Lightning strikes and ground surge currents can cause large momentary voltage differences between remote ends of the communication link. For this reason, surge protection devices are internally provided at both communication ports. An isolated power supply with an optocoupled data interface also acts to reduce noise coupling. To ensure maximum reliability, all equipment should have similar transient protection devices installed.

Both ends of the RS485 circuit should also be terminated with an impedance as shown below.

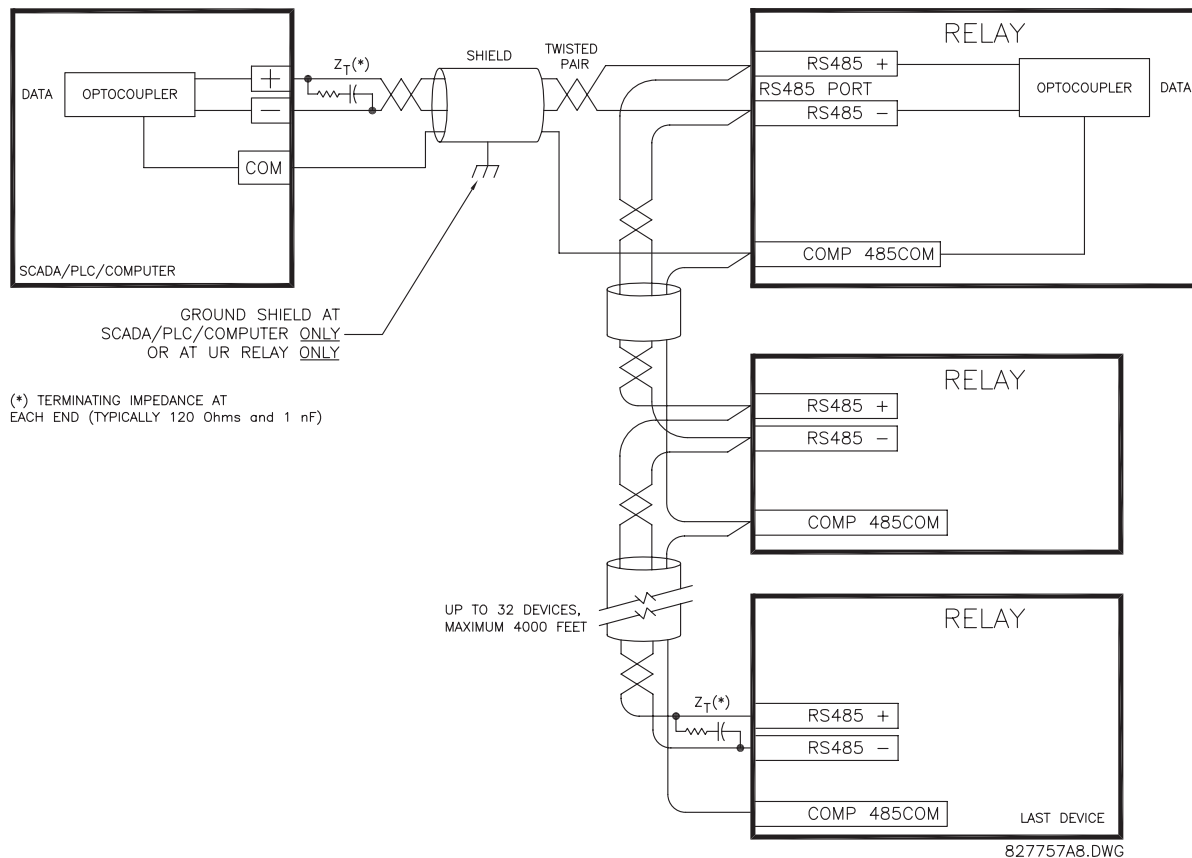


Figure 3–18: RS485 SERIAL CONNECTION

c) 10BASE-FL AND 100BASE-FX FIBER OPTIC PORTS



ENSURE THE DUST COVERS ARE INSTALLED WHEN THE FIBER IS NOT IN USE. DIRTY OR SCRATCHED CONNECTORS CAN LEAD TO HIGH LOSSES ON A FIBER LINK.



OBSERVING ANY FIBER TRANSMITTER OUTPUT MAY CAUSE INJURY TO THE EYE.

The fiber optic communication ports allow for fast and efficient communications between relays at 10 or 100Mbps. Optical fiber may be connected to the relay supporting a wavelength of 820 nm in multi-mode or 1310 nm in multi-mode and single-mode. The 10 Mbps rate is available for CPU modules 9G and 9H; 100Mbps is available for modules 9J, 9K, 9L, and 9M. The 9H, 9K and 9M modules have a second pair of identical optical fiber transmitter and receiver for redundancy.

The optical fiber sizes supported include 50/125 μm , 62.5/125 μm and 100/140 μm for 10 Mbps. The fiber optic port is designed such that the response times will not vary for any core that is 100 μm or less in diameter, 62.5 μm for 100 Mbps. For optical power budgeting, splices are required every 1 km for the transmitter/receiver pair. When splicing optical fibers, the diameter and numerical aperture of each fiber must be the same. In order to engage or disengage the ST type connector, only a quarter turn of the coupling is required.

3.2.9 IRIG-B

IRIG-B is a standard time code format that allows stamping of events to be synchronized among connected devices within 1 millisecond. The IRIG time code formats are serial, width-modulated codes which can be either DC level shifted or amplitude modulated (AM). Third party equipment is available for generating the IRIG-B signal; this equipment may use a GPS satellite system to obtain the time reference so that devices at different geographic locations can also be synchronized.

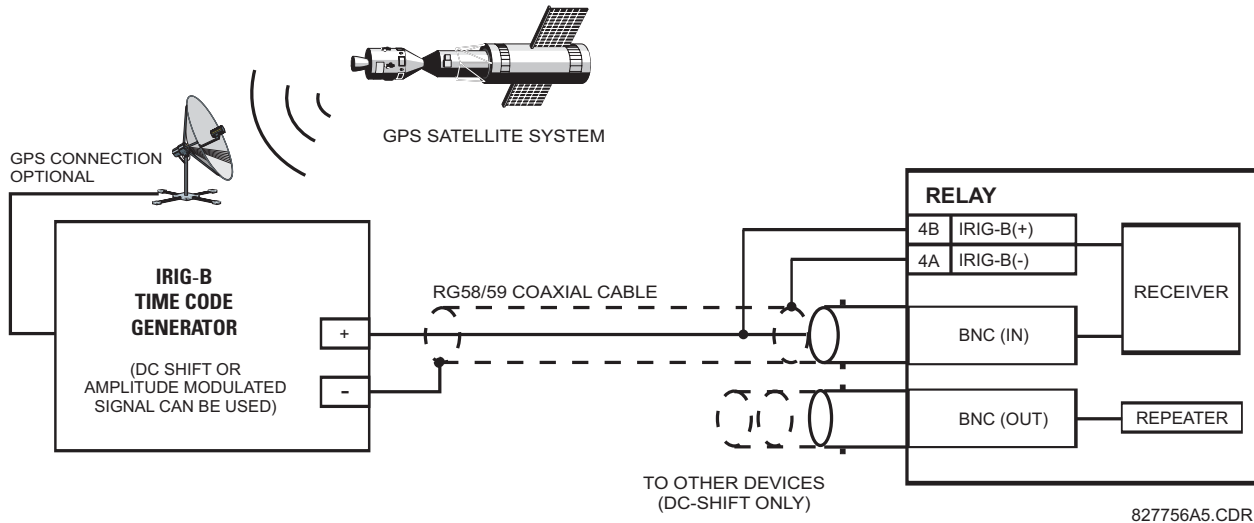


Figure 3-19: IRIG-B CONNECTION

The IRIG-B repeater provides an amplified DC-shift IRIG-B signal to other equipment. By using one IRIG-B serial connection, several UR-series relays can be synchronized. The IRIG-B repeater has a bypass function to maintain the time signal even when a relay in the series is powered down.

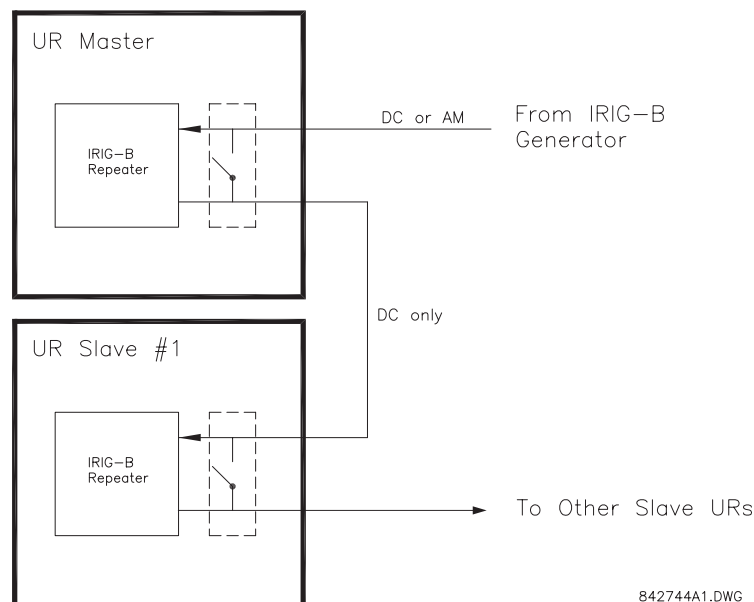


Figure 3-20: IRIG-B REPEATER

3.3.1 DESCRIPTION

The N60 direct inputs/outputs feature makes use of the Type 7 series of communications modules. These modules are also used by the L90 Line Differential Relay for inter-relay communications. The direct input/output feature uses the communications channel(s) provided by these modules to exchange digital state information between relays. This feature is available on all UR-series relay models except for the L90 Line Differential relay.

The communications channels are normally connected in a ring configuration as shown below. The transmitter of one module is connected to the receiver of the next module. The transmitter of this second module is then connected to the receiver of the next module in the ring. This is continued to form a communications ring. The figure below illustrates a ring of four UR-series relays with the following connections: UR1-Tx to UR2-Rx, UR2-Tx to UR3-Rx, UR3-Tx to UR4-Rx, and UR4-Tx to UR1-Rx. A maximum of sixteen (16) UR-series relays can be connected in a single ring

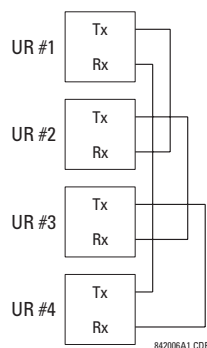


Figure 3–21: DIRECT INPUT/OUTPUT SINGLE CHANNEL CONNECTION

The interconnection for dual-channel Type 7 communications modules is shown below. Two channel modules allow for a redundant ring configuration. That is, two rings can be created to provide an additional independent data path. The required connections are: UR1-Tx1 to UR2-Rx1, UR2-Tx1 to UR3-Rx1, UR3-Tx1 to UR4-Rx1, and UR4-Tx1 to UR1-Rx1 for the first ring; and UR1-Tx2 to UR4-Rx2, UR4-Tx2 to UR3-Rx2, UR3-Tx2 to UR2-Rx2, and UR2-Tx2 to UR1-Rx2 for the second ring.

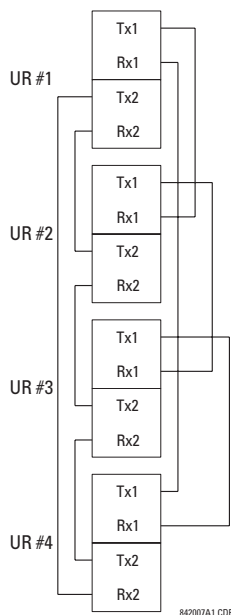


Figure 3–22: DIRECT INPUT/OUTPUT DUAL CHANNEL CONNECTION

The following diagram shows the connection for three UR-series relays using two independent communication channels. UR1 and UR3 have single Type 7 communication modules; UR2 has a dual-channel module. The two communication channels can be of different types, depending on the Type 7 modules used. To allow the direct input/output data to 'cross-over' from Channel 1 to Channel 2 on UR2, the **DIRECT I/O CHANNEL CROSSOVER** setting should be "Enabled" on UR2. This forces UR2 to forward messages received on Rx1 out Tx2, and messages received on Rx2 out Tx1.

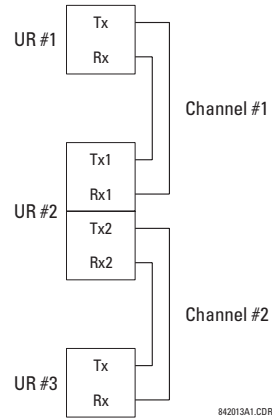


Figure 3–23: DIRECT INPUT/OUTPUT SINGLE/DUAL CHANNEL COMBINATION CONNECTION

The interconnection requirements are described in further detail in this section for each specific variation of Type 7 communications module. These modules are listed in the following table. All fiber modules use ST type connectors.

Table 3–3: CHANNEL COMMUNICATION OPTIONS

MODULE	SPECIFICATION
2A	C37.94SM, 1300 nm, single-mode, ELED, 1 channel single-mode
2B	C37.94SM, 1300 nm, single-mode, ELED, 2 channel single-mode
7A	820 nm, multi-mode, LED, 1 channel
7B	1300 nm, multi-mode, LED, 1 channel
7C	1300 nm, single-mode, ELED, 1 channel
7D	1300 nm, single-mode, LASER, 1 channel
7E	Channel 1: G.703, Channel 2: 820 nm, multi-mode
7F	Channel 1: G.703, Channel 2: 1300 nm, multi-mode
7G	Channel 1: G.703, Channel 2: 1300 nm, single-mode ELED
7H	820 nm, multi-mode, LED, 2 channels
7I	1300 nm, multi-mode, LED, 2 channels
7J	1300 nm, single-mode, ELED, 2 channels
7K	1300 nm, single-mode, LASER, 2 channels
7L	Channel 1: RS422, Channel 2: 820 nm, multi-mode, LED
7M	Channel 1: RS422, Channel 2: 1300 nm, multi-mode, LED
7N	Channel 1: RS422, Channel 2: 1300 nm, single-mode, ELED
7P	Channel 1: RS422, Channel 2: 1300 nm, single-mode, LASER
7Q	Channel 1: G.703, Channel 2: 1300 nm, single-mode, LASER
7R	G.703, 1 channel
7S	G.703, 2 channels
7T	RS422, 1 channel
7W	RS422, 2 channels
72	1550 nm, single-mode, LASER, 1 channel
73	1550 nm, single-mode, LASER, 2 channels
74	Channel 1 - RS422; Channel 2 - 1550 nm, single-mode, LASER
75	Channel 1 - G.703; Channel 2 - 1550 nm, single-mode, LASER
76	IEEE C37.94, 820 nm, multi-mode, LED, 1 channel
77	IEEE C37.94, 820 nm, multi-mode, LED, 2 channels



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3.3.2 FIBER: LED AND ELED TRANSMITTERS

The following figure shows the configuration for the 7A, 7B, 7C, 7H, 7I, and 7J fiber-only modules.

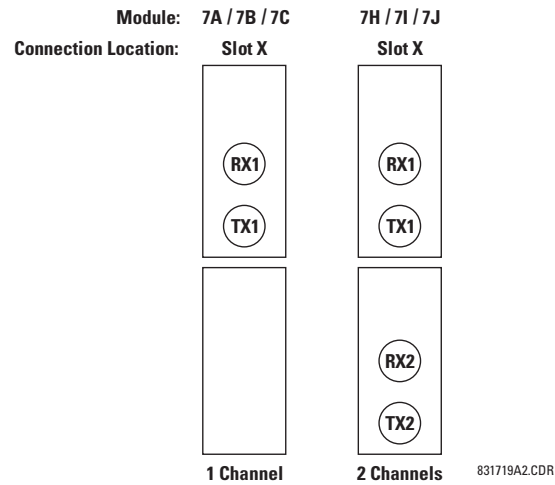


Figure 3–24: LED AND ELED FIBER MODULES

3.3.3 FIBER-LASER TRANSMITTERS

The following figure shows the configuration for the 72, 73, 7D, and 7K fiber-laser module.

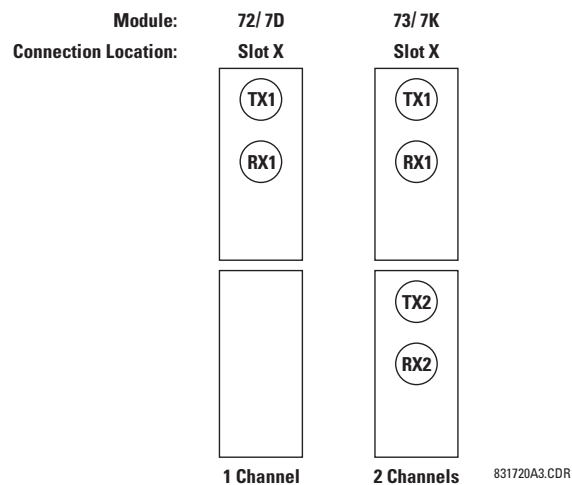


Figure 3–25: LASER FIBER MODULES



When using a LASER Interface, attenuators may be necessary to ensure that you do not exceed Maximum Optical Input Power to the receiver.

3.3.4 G.703 INTERFACE

a) DESCRIPTION

The following figure shows the 64K ITU G.703 co-directional interface configuration.



The G.703 module is fixed at 64 kbps only. The SETTINGS ⇒ PRODUCT SETUP ⇒ DIRECT I/O ⇒ DIRECT I/O DATA RATE setting is not applicable to this module.

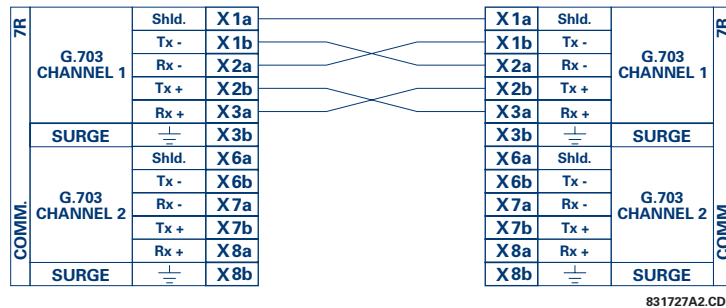
AWG 24 twisted shielded pair is recommended for external connections, with the shield grounded only at one end. Connecting the shield to Pin X1a or X6a grounds the shield since these pins are internally connected to ground. Thus, if Pin X1a or X6a is used, do not ground at the other end. This interface module is protected by surge suppression devices.

7R	G.703 CHANNEL 1	Shld.	X1a
		Tx -	X1b
		Rx -	X2a
		Tx +	X2b
		Rx +	X3a
	SURGE	⏏	X3b
	G.703 CHANNEL 2	Shld.	X6a
		Tx -	X6b
		Rx -	X7a
		Tx +	X7b
		Rx +	X8a
COMM.	SURGE	⏏	X8b

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Figure 3-26: G.703 INTERFACE CONFIGURATION

The following figure shows the typical pin interconnection between two G.703 interfaces. For the actual physical arrangement of these pins, see the Rear Terminal Assignments section earlier in this chapter. All pin interconnections are to be maintained for a connection to a multiplexer.



831727A2.CDR

Figure 3-27: TYPICAL PIN INTERCONNECTION BETWEEN TWO G.703 INTERFACES



Pin nomenclature may differ from one manufacturer to another. Therefore, it is not uncommon to see pinouts numbered TxA, TxB, RxA and RxB. In such cases, it can be assumed that “A” is equivalent to “+” and “B” is equivalent to “-”.

b) G.703 SELECTION SWITCH PROCEDURES

1. Remove the G.703 module (7R or 7S):

The ejector/inserter clips located at the top and at the bottom of each module, must be pulled simultaneously in order to release the module for removal. Before performing this action, **control power must be removed from the relay**. The original location of the module should be recorded to help ensure that the same or replacement module is inserted into the correct slot.

2. Remove the module cover screw.
3. Remove the top cover by sliding it towards the rear and then lift it upwards.
4. Set the Timing Selection Switches (Channel 1, Channel 2) to the desired timing modes.
5. Replace the top cover and the cover screw.

6. Re-insert the G.703 module. Take care to ensure that the **correct** module type is inserted into the **correct** slot position. The ejector/insertor clips located at the top and at the bottom of each module must be in the disengaged position as the module is smoothly inserted into the slot. Once the clips have cleared the raised edge of the chassis, engage the clips simultaneously. When the clips have locked into position, the module will be fully inserted.

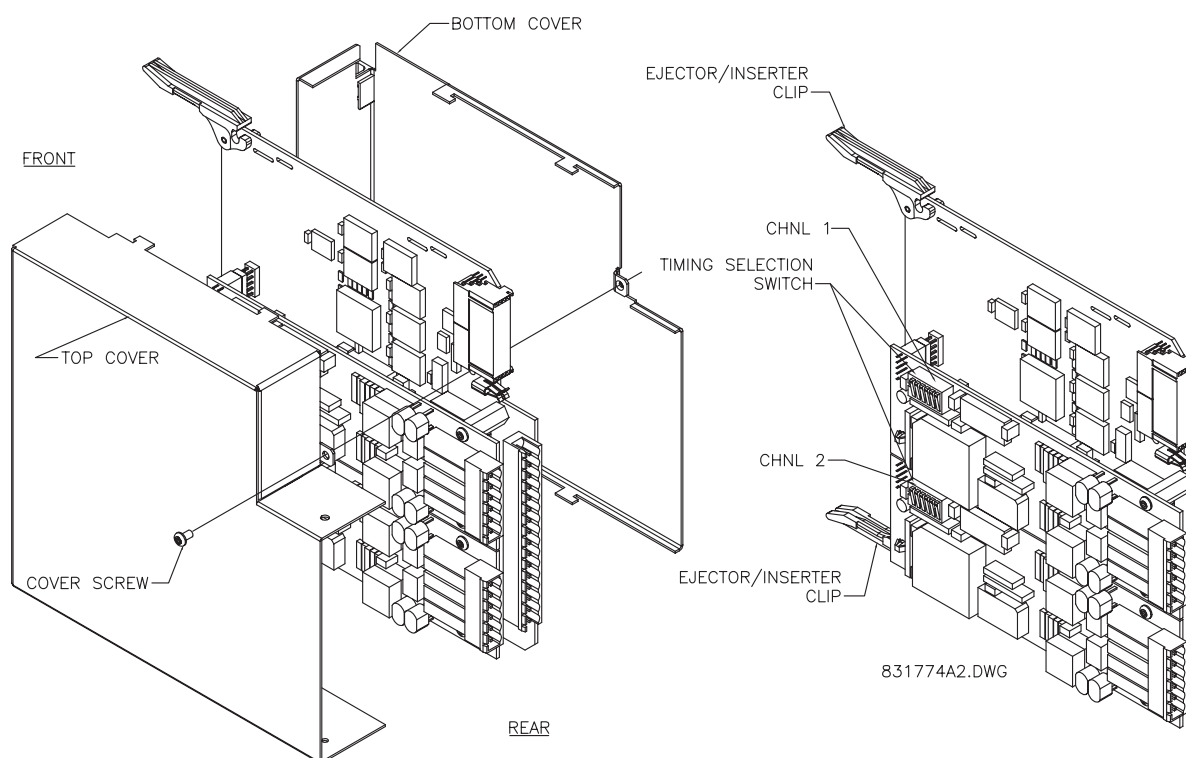


Figure 3-28: G.703 TIMING SELECTION SWITCH SETTING

Table 3-4: G.703 TIMING SELECTIONS

SWITCHES	FUNCTION
S1	OFF → Octet Timing Disabled ON → Octet Timing 8 kHz
S5 and S6	S5 = OFF and S6 = OFF → Loop Timing Mode S5 = ON and S6 = OFF → Internal Timing Mode S5 = OFF and S6 = ON → Minimum Remote Loopback Mode S5 = ON and S6 = ON → Dual Loopback Mode

c) OCTET TIMING (SWITCH S1)

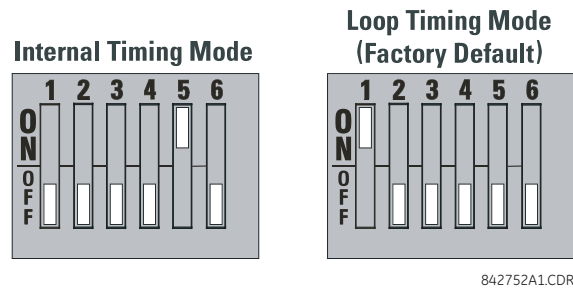
If Octet Timing is enabled (ON), this 8 kHz signal will be asserted during the violation of Bit 8 (LSB) necessary for connecting to higher order systems. When N60s are connected back to back, Octet Timing should be disabled (OFF).

d) TIMING MODES (SWITCHES S5 AND S6)

There are two timing modes for the G.703 module: internal timing mode and loop timing mode (default).

- **Internal Timing Mode:** The system clock is generated internally. Therefore, the G.703 timing selection should be in the Internal Timing Mode for back-to-back (UR-to-UR) connections. For back-to-back connections, set for Octet Timing (S1 = OFF) and Timing Mode = Internal Timing (S5 = ON and S6 = OFF).
- **Loop Timing Mode:** The system clock is derived from the received line signal. Therefore, the G.703 timing selection should be in Loop Timing Mode for connections to higher order systems. For connection to a higher order system (UR-to-multiplexer, factory defaults), set to Octet Timing (S1 = ON) and set Timing Mode = Loop Timing (S5 = OFF and S6 = OFF).

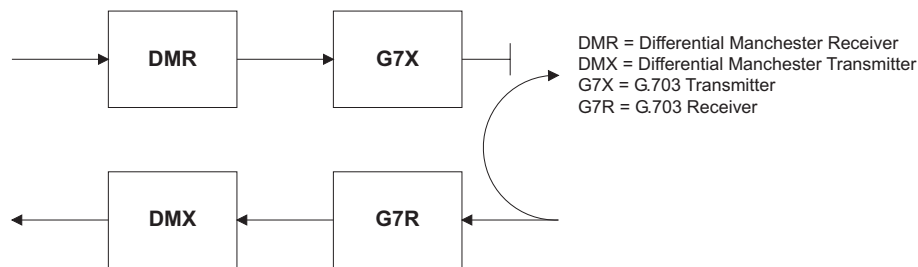
The switch settings for the internal and loop timing modes are shown below:



e) TEST MODES (SWITCHES S5 AND S6)

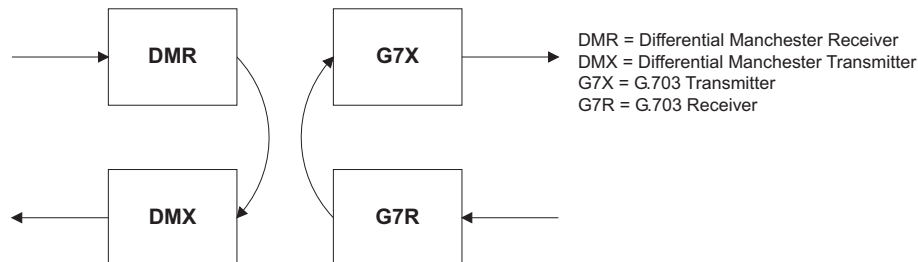
MINIMUM REMOTE LOOPBACK MODE:

In Minimum Remote Loopback mode, the multiplexer is enabled to return the data from the external interface without any processing to assist in diagnosing G.703 Line Side problems irrespective of clock rate. Data enters from the G.703 inputs, passes through the data stabilization latch which also restores the proper signal polarity, passes through the multiplexer and then returns to the transmitter. The Differential Received Data is processed and passed to the G.703 Transmitter module after which point the data is discarded. The G.703 Receiver module is fully functional and continues to process data and passes it to the Differential Manchester Transmitter module. Since timing is returned as it is received, the timing source is expected to be from the G.703 line side of the interface.



DUAL LOOPBACK MODE:

In Dual Loopback Mode, the multiplexers are active and the functions of the circuit are divided into two with each receiver/transmitter pair linked together to deconstruct and then reconstruct their respective signals. Differential Manchester data enters the Differential Manchester Receiver module and then is returned to the Differential Manchester Transmitter module. Likewise, G.703 data enters the G.703 Receiver module and is passed through to the G.703 Transmitter module to be returned as G.703 data. Because of the complete split in the communications path and because, in each case, the clocks are extracted and reconstructed with the outgoing data, in this mode there must be two independent sources of timing. One source lies on the G.703 line side of the interface while the other lies on the Differential Manchester side of the interface.



3.3.5 RS422 INTERFACE

a) DESCRIPTION

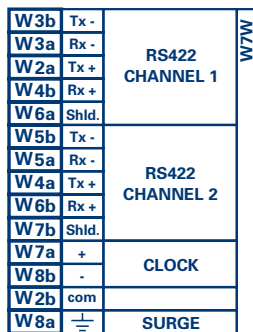
The following figure shows the RS422 2-terminal interface configuration at 64 kbps. AWG 24 twisted shielded pair is recommended for external connections. This interface module is protected by surge suppression devices which optically isolated.

SHIELD TERMINATION

The shield pins (6a and 7b) are internally connected to the ground pin (8a). Proper shield termination is as follows:

Site 1: Terminate shield to pins 6a and/or 7b; Site 2: Terminate shield to 'COM' pin 2b.

The clock terminating impedance should match the impedance of the line.



RS422.CDR
p/o 827831A6.CDR

Figure 3-29: RS422 INTERFACE CONFIGURATION

The following figure shows the typical pin interconnection between two RS422 interfaces. All pin interconnections are to be maintained for a connection to a multiplexer.

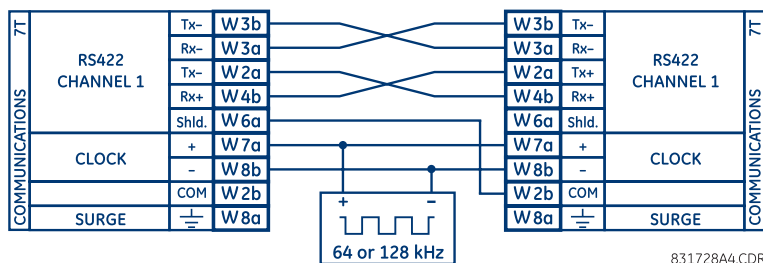
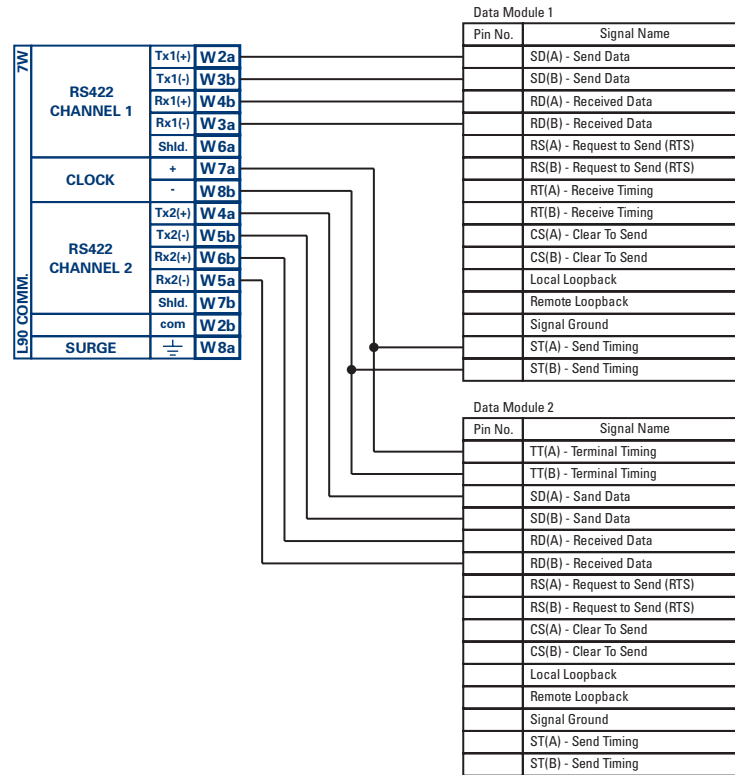


Figure 3-30: TYPICAL PIN INTERCONNECTION BETWEEN TWO RS422 INTERFACES

b) TWO CHANNEL APPLICATIONS VIA MULTIPLEXERS

The RS422 Interface may be used for single or two channel applications over SONET/SDH and/or multiplexed systems. When used in single channel applications, the RS422 interface links to higher order systems in a typical fashion observing transmit (Tx), receive (Rx), and send timing (ST) connections. However, when used in two channel applications, certain criteria must be followed since there is one clock input for the two RS422 channels. The system will function correctly if the following connections are observed and your data module has a terminal timing feature. Terminal timing is a common feature to most synchronous data units that allows the module to accept timing from an external source. Using the terminal timing feature, two channel applications can be achieved if these connections are followed: The send timing outputs from the multiplexer (data module 1), will connect to the Clock inputs of the UR-RS422 interface in the usual fashion. In addition, the send timing outputs of data module 1 will also be paralleled to the terminal timing inputs of data module 2. By using this configuration, the timing for both data modules and both UR-RS422 channels will be derived from a single clock source. As a result, data sampling for both of the UR-RS422 channels will be synchronized via the send timing leads on data module 1 as shown below. If the terminal timing feature is not available or this type of connection is not desired, the G.703 interface is a viable option that does not impose timing restrictions.



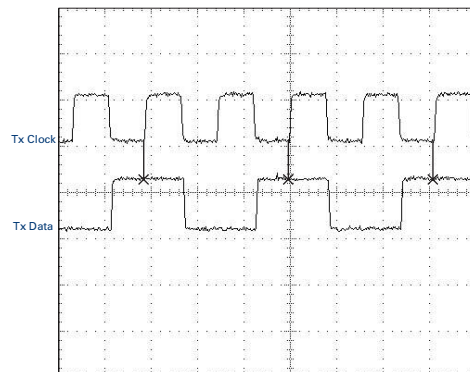
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Figure 3–31: TIMING CONFIGURATION FOR RS422 TWO-CHANNEL, 3-TERMINAL APPLICATION

Data module 1 provides timing to the N60 RS422 interface via the ST(A) and ST(B) outputs. Data module 1 also provides timing to data module 2 TT(A) and TT(B) inputs via the ST(A) and AT(B) outputs. The data module pin numbers have been omitted in the figure above since they may vary depending on the manufacturer.

c) TRANSIT TIMING

The RS422 Interface accepts one clock input for Transmit Timing. It is important that the rising edge of the 64 kHz Transmit Timing clock of the Multiplexer Interface is sampling the data in the center of the Transmit Data window. Therefore, it is important to confirm Clock and Data Transitions to ensure Proper System Operation. For example, the following figure shows the positive edge of the Tx Clock in the center of the Tx Data bit.

**Figure 3–32: CLOCK AND DATA TRANSITIONS**

d) RECEIVE TIMING

The RS422 Interface utilizes NRZI-MARK Modulation Code and; therefore, does not rely on an Rx Clock to recapture data. NRZI-MARK is an edge-type, invertible, self-clocking code.

To recover the Rx Clock from the data-stream, an integrated DPLL (Digital Phase Lock Loop) circuit is utilized. The DPLL is driven by an internal clock, which is over-sampled 16X, and uses this clock along with the data-stream to generate a data clock that can be used as the SCC (Serial Communication Controller) receive clock.

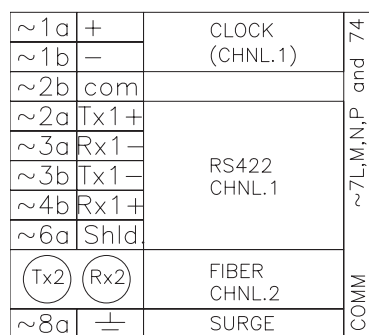
3.3.6 RS422 AND FIBER INTERFACE

The following figure shows the combined RS422 plus Fiber interface configuration at 64K baud. The 7L, 7M, 7N, 7P, and 74 modules are used in 2-terminal with a redundant channel or 3-terminal configurations where Channel 1 is employed via the RS422 interface (possibly with a multiplexer) and Channel 2 via direct fiber.

AWG 24 twisted shielded pair is recommended for external RS422 connections and the shield should be grounded only at one end. For the direct fiber channel, power budget issues should be addressed properly.



When using a LASER Interface, attenuators may be necessary to ensure that you do not exceed Maximum Optical Input Power to the receiver.



L907LNMPCDR
P/O 827831AE.DWG

Figure 3–33: RS422 AND FIBER INTERFACE CONNECTION

Connections shown above are for multiplexers configured as DCE (Data Communications Equipment) units.

3.3.7 G.703 AND FIBER INTERFACE

The figure below shows the combined G.703 plus Fiber interface configuration at 64K baud. The 7E, 7F, 7G, 7Q, and 75 modules are used in configurations where Channel 1 is employed via the G.703 interface (possibly with a multiplexer) and Channel 2 via direct fiber. AWG 24 twisted shielded pair is recommended for external G.703 connections connecting the shield to Pin 1A at one end only. For the direct fiber channel, power budget issues should be addressed properly. See previous sections for more details on the G.703 and Fiber interfaces.



When using a LASER Interface, attenuators may be necessary to ensure that you do not exceed Maximum Optical Input Power to the receiver.

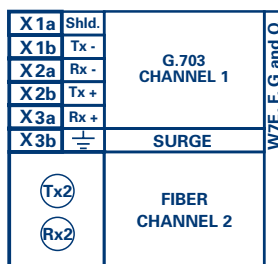


Figure 3–34: G.703 AND FIBER INTERFACE CONNECTION

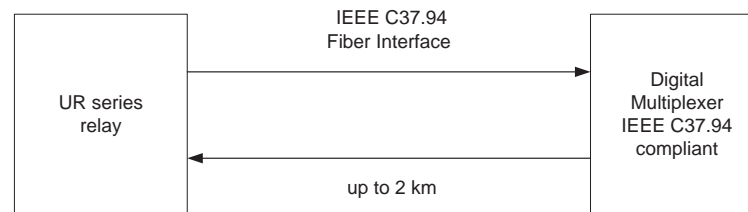
3.3.8 IEEE C37.94 INTERFACE

The UR-series IEEE C37.94 communication modules (76 and 77) are designed to interface with IEEE C37.94 compliant digital multiplexers and/or an IEEE C37.94 compliant interface converter for use with direct input/output applications for firmware revisions 3.30 and higher. The IEEE C37.94 standard defines a point-to-point optical link for synchronous data between a multiplexer and a teleprotection device. This data is typically 64 kbps, but the standard provides for speeds up to 64*n* kbps, where *n* = 1, 2, ..., 12. The UR-series C37.94 communication module is 64 kbps only with *n* fixed at 1. The frame is a valid International Telecommunications Union (ITU-T) recommended G.704 pattern from the standpoint of framing and data rate. The frame is 256 bits and is repeated at a frame rate of 8000 Hz, with a resultant bit rate of 2048 kbps.

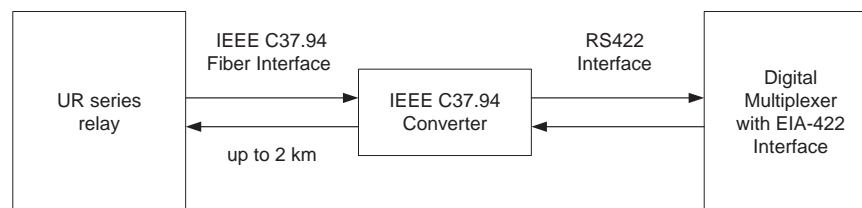
The specifications for the module are as follows:

- IEEE standard: C37.94 for 1 × 64 kbps optical fiber interface
Fiber optic cable type: 50 mm or 62.5 mm core diameter optical fiber
Fiber optic mode: multi-mode
Fiber optic cable length: up to 2 km
Fiber optic connector: type ST
Wavelength: 830 ±40 nm
Connection: as per all fiber optic connections, a Tx to Rx connection is required.

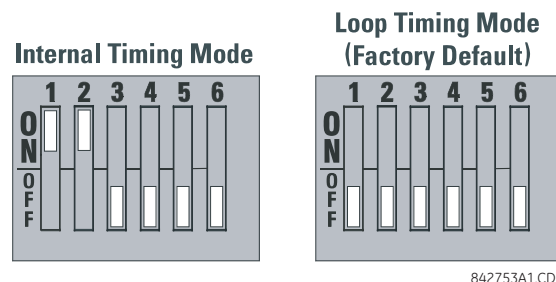
The UR-series C37.94 communication module can be connected directly to any compliant digital multiplexer that supports the IEEE C37.94 standard as shown below.



The UR-series C37.94 communication module can be connected to the electrical interface (G.703, RS422, or X.21) of a non-compliant digital multiplexer via an optical-to-electrical interface converter that supports the IEEE C37.94 standard, as shown below.



The UR-series C37.94 communication module has six (6) switches that are used to set the clock configuration. The functions of these control switches is shown below.



For the Internal Timing Mode, the system clock is generated internally. Therefore, the timing switch selection should be Internal Timing for Relay 1 and Loop Timed for Relay 2. There must be only one timing source configured.

For the Looped Timing Mode, the system clock is derived from the received line signal. Therefore, the timing selection should be in Loop Timing Mode for connections to higher order systems.

The C37.94 communications module cover removal procedure is as follows:

1. Remove the C37.94 module (76 or 77):

The ejector/inserter clips located at the top and at the bottom of each module, must be pulled simultaneously in order to release the module for removal. Before performing this action, **control power must be removed from the relay**. The original location of the module should be recorded to help ensure that the same or replacement module is inserted into the correct slot.

2. Remove the module cover screw.
3. Remove the top cover by sliding it towards the rear and then lift it upwards.
4. Set the Timing Selection Switches (Channel 1, Channel 2) to the desired timing modes (see description above).
5. Replace the top cover and the cover screw.
6. Re-insert the C37.94 module Take care to ensure that the **correct** module type is inserted into the **correct** slot position. The ejector/inserters located at the top and at the bottom of each module must be in the disengaged position as the module is smoothly inserted into the slot. Once the clips have cleared the raised edge of the chassis, engage the clips simultaneously. When the clips have locked into position, the module will be fully inserted.

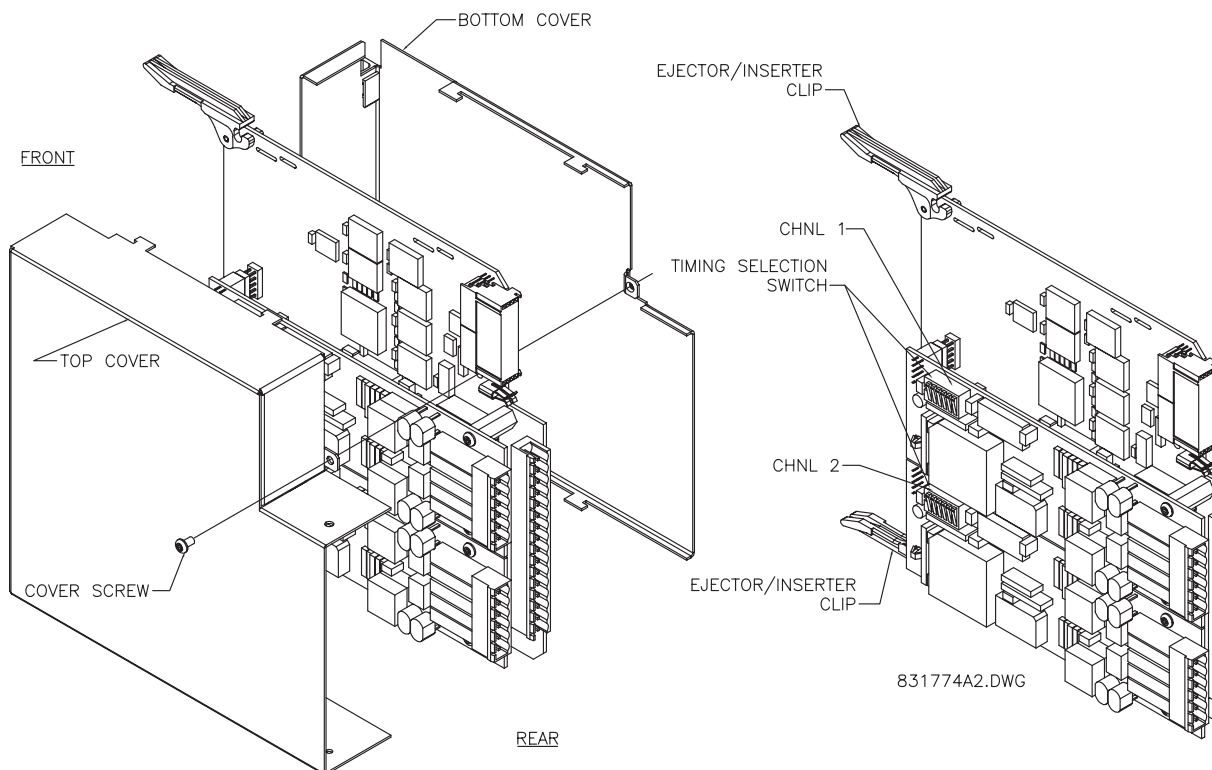


Figure 3–35: IEEE C37.94 TIMING SELECTION SWITCH SETTING

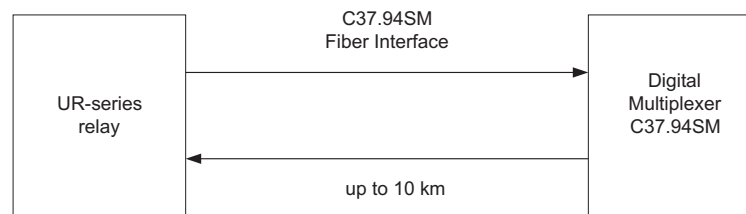
3.3.9 C37.94SM INTERFACE

The UR-series C37.94SM communication modules (2A and 2B) are designed to interface with modified IEEE C37.94 compliant digital multiplexers and/or IEEE C37.94 compliant interface converters that have been converted from 820 nm multi-mode fiber optics to 1300 nm ELED single-mode fiber optics. The IEEE C37.94 standard defines a point-to-point optical link for synchronous data between a multiplexer and a teleprotection device. This data is typically 64 kbps, but the standard provides for speeds up to $64n$ kbps, where $n = 1, 2, \dots, 12$. The UR-series C37.94SM communication module is 64 kbps only with n fixed at 1. The frame is a valid International Telecommunications Union (ITU-T) recommended G.704 pattern from the standpoint of framing and data rate. The frame is 256 bits and is repeated at a frame rate of 8000 Hz, with a resultant bit rate of 2048 kbps.

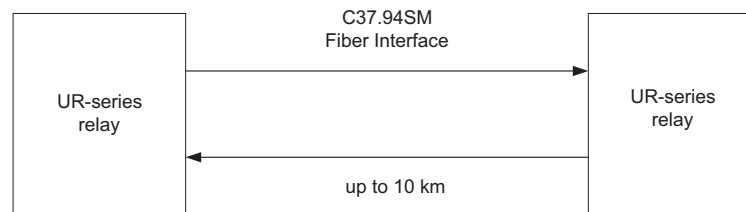
The specifications for the module are as follows:

- Emulated IEEE standard: emulates C37.94 for 1×64 kbps optical fiber interface (modules set to $n = 1$ or 64 kbps)
- Fiber optic cable type: 9/125 μm core diameter optical fiber
- Fiber optic mode: single-mode, ELED compatible with HP HFBR-1315T transmitter and HP HFBR-2316T receiver
- Fiber optic cable length: up to 10 km
- Fiber optic connector: type ST
- Wavelength: 1300 ± 40 nm
- Connection: as per all fiber optic connections, a Tx to Rx connection is required.

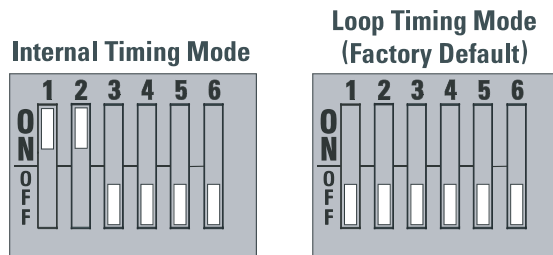
The UR-series C37.94SM communication module can be connected **directly** to any compliant digital multiplexer that supports C37.94SM as shown below.



It can also be connected **directly** to any other UR-series relay with a C37.94SM module as shown below.



The UR-series C37.94SM communication module has six (6) switches that are used to set the clock configuration. The functions of these control switches is shown below.



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For the Internal Timing Mode, the system clock is generated internally. Therefore, the timing switch selection should be Internal Timing for Relay 1 and Loop Timed for Relay 2. There must be only one timing source configured.

For the Looped Timing Mode, the system clock is derived from the received line signal. Therefore, the timing selection should be in Loop Timing Mode for connections to higher order systems.

The C37.94SM communications module cover removal procedure is as follows:

1. Remove the C37.94SM module (modules 2A or 2B):

The ejector/inserter clips located at the top and at the bottom of each module, must be pulled simultaneously in order to release the module for removal. Before performing this action, **control power must be removed from the relay**. The original location of the module should be recorded to help ensure that the same or replacement module is inserted into the correct slot.

2. Remove the module cover screw.
3. Remove the top cover by sliding it towards the rear and then lift it upwards.
4. Set the Timing Selection Switches (Channel 1, Channel 2) to the desired timing modes (see description above).
5. Replace the top cover and the cover screw.
6. Re-insert the C37.94SM module. Take care to ensure that the **correct** module type is inserted into the **correct** slot position. The ejector/inserter clips located at the top and at the bottom of each module must be in the disengaged position as the module is smoothly inserted into the slot. Once the clips have cleared the raised edge of the chassis, engage the clips simultaneously. When the clips have locked into position, the module will be fully inserted.

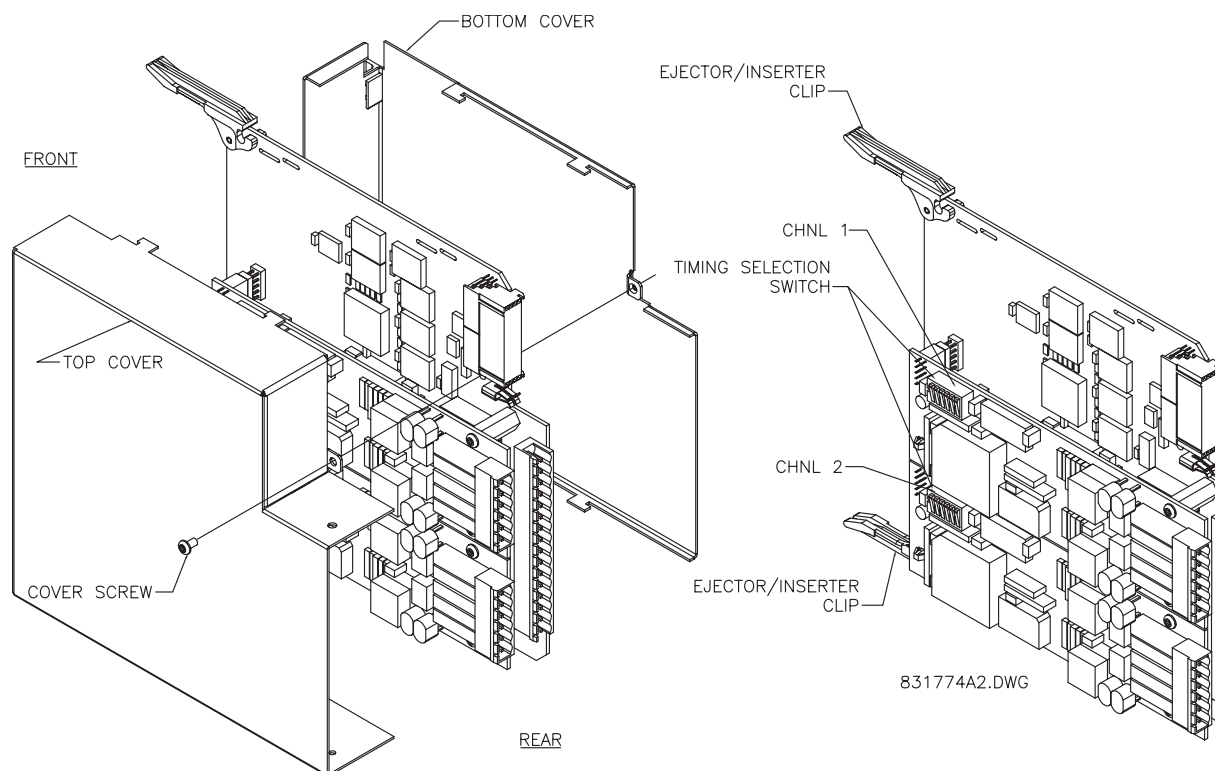


Figure 3–36: C37.94SM TIMING SELECTION SWITCH SETTING

4.1.1 INTRODUCTION

The EnerVista UR Setup software provides a graphical user interface (GUI) as one of two human interfaces to a UR device. The alternate human interface is implemented via the device's faceplate keypad and display (see Faceplate Interface section in this chapter).

The EnerVista UR Setup software provides a single facility to configure, monitor, maintain, and trouble-shoot the operation of relay functions, connected over local or wide area communication networks. It can be used while disconnected (i.e. off-line) or connected (i.e. on-line) to a UR device. In off-line mode, settings files can be created for eventual downloading to the device. In on-line mode, you can communicate with the device in real-time.

The EnerVista UR Setup software, provided with every N60 relay, can be run from any computer supporting Microsoft Windows® 95, 98, NT, 2000, ME, and XP. This chapter provides a summary of the basic EnerVista UR Setup software interface features. The EnerVista UR Setup Help File provides details for getting started and using the EnerVista UR Setup software interface.

4.1.2 CREATING A SITE LIST

To start using the EnerVista UR Setup software, a site definition and device definition must first be created. See the EnerVista UR Setup Help File or refer to the *Connecting EnerVista UR Setup with the N60* section in Chapter 1 for details.

4.1.3 ENERVISTA UR SETUP SOFTWARE OVERVIEW

a) ENGAGING A DEVICE

The EnerVista UR Setup software may be used in on-line mode (relay connected) to directly communicate with a UR relay. Communicating relays are organized and grouped by communication interfaces and into sites. Sites may contain any number of relays selected from the UR product series.

b) USING SETTINGS FILES

The EnerVista UR Setup software interface supports three ways of handling changes to relay settings:

- In off-line mode (relay disconnected) to create or edit relay settings files for later download to communicating relays.
- While connected to a communicating relay to directly modify any relay settings via relay data view windows, and then save the settings to the relay.
- You can create/edit settings files and then write them to the relay while the interface is connected to the relay.

Settings files are organized on the basis of file names assigned by the user. A settings file contains data pertaining to the following types of relay settings:

- Device Definition
- Product Setup
- FlexLogic™
- Control Elements
- Inputs/Outputs
- Testing

Factory default values are supplied and can be restored after any changes.

c) CREATING AND EDITING FLEXLOGIC™

You can create or edit a FlexLogic™ equation in order to customize the relay. You can subsequently view the automatically generated logic diagram.

d) VIEWING ACTUAL VALUES

You can view real-time relay data such as input/output status and measured parameters.

e) VIEWING TRIGGERED EVENTS

While the interface is in either on-line or off-line mode, you can view and analyze data generated by triggered specified parameters, via one of the following:

- **Event Recorder facility:** The event recorder captures contextual data associated with the last 1024 events, listed in chronological order from most recent to oldest.
- **Oscillography facility:** The oscillography waveform traces and digital states are used to provide a visual display of power system and relay operation data captured during specific triggered events.

f) FILE SUPPORT

- **Execution:** Any EnerVista UR Setup file which is double clicked or opened will launch the application, or provide focus to the already opened application. If the file was a settings file (has a `URS` extension) which had been removed from the Settings List tree menu, it will be added back to the Settings List tree menu.
- **Drag and Drop:** The Site List and Settings List control bar windows are each mutually a drag source and a drop target for device-order-code-compatible files or individual menu items. Also, the Settings List control bar window and any Windows Explorer directory folder are each mutually a file drag source and drop target.

New files which are dropped into the Settings List window are added to the tree which is automatically sorted alphabetically with respect to settings file names. Files or individual menu items which are dropped in the selected device menu in the Site List window will automatically be sent to the on-line communicating device.

g) FIRMWARE UPGRADES

The firmware of a N60 device can be upgraded, locally or remotely, via the EnerVista UR Setup software. The corresponding instructions are provided by the EnerVista UR Setup Help file under the topic “Upgrading Firmware”.



Modbus addresses assigned to firmware modules, features, settings, and corresponding data items (i.e. default values, minimum/maximum values, data type, and item size) may change slightly from version to version of firmware. The addresses are rearranged when new features are added or existing features are enhanced or modified. The **EEPROM DATA ERROR** message displayed after upgrading/downgrading the firmware is a resettable, self-test message intended to inform users that the Modbus addresses have changed with the upgraded firmware. This message does not signal any problems when appearing after firmware upgrades.

4.1.4 ENERVISTA UR SETUP SOFTWARE MAIN WINDOW

The EnerVista UR Setup software main window supports the following primary display components:

- Title bar which shows the pathname of the active data view
- Main window menu bar
- Main window tool bar
- Site List control bar window
- Settings List control bar window
- Device data view window(s), with common tool bar
- Settings File data view window(s), with common tool bar
- Workspace area with data view tabs
- Status bar

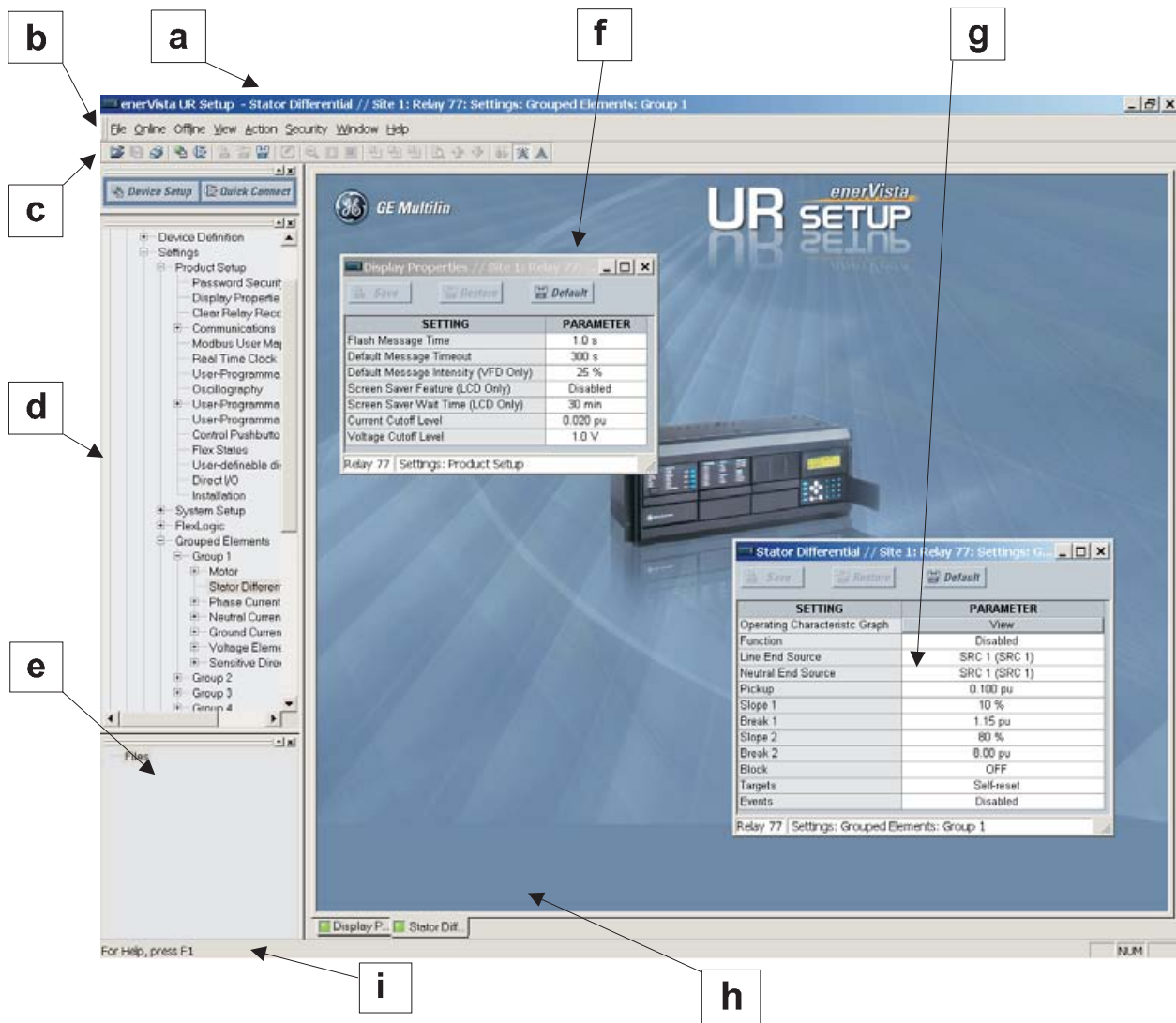


Figure 4-1: ENERVISTA UR SETUP SOFTWARE MAIN WINDOW

4.2.1 FACEPLATE

The keypad/display/LED interface is one of two alternate human interfaces supported. The other alternate human interface is implemented via the EnerVista UR Setup software. The faceplate interface consists of several functional panels.

The faceplate is hinged to allow easy access to the removable modules. There is also a removable dust cover that fits over the faceplate which must be removed in order to access the keypad panel. The following figure shows the arrangement of faceplate panels.

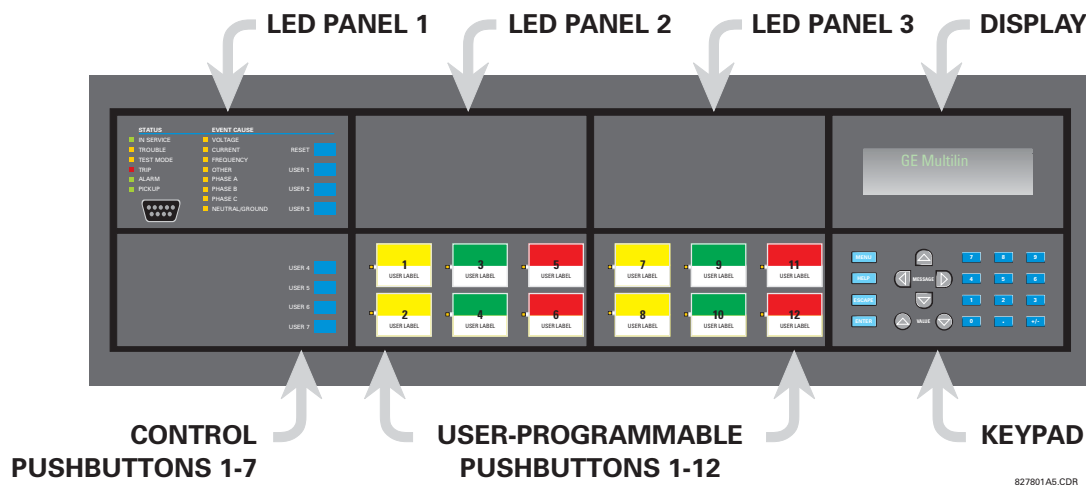


Figure 4-2: UR-SERIES HORIZONTAL FACEPLATE PANELS

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4.2.2 LED INDICATORS

a) LED PANEL 1

This panel provides several LED indicators, several keys, and a communications port. The RESET key is used to reset any latched LED indicator or target message, once the condition has been cleared (these latched conditions can also be reset via the **SETTINGS** ⇒ **INPUT/OUTPUTS** ⇒ **RESETTING** menu). The USER keys are used by the Breaker Control feature. The RS232 port is intended for connection to a portable PC.

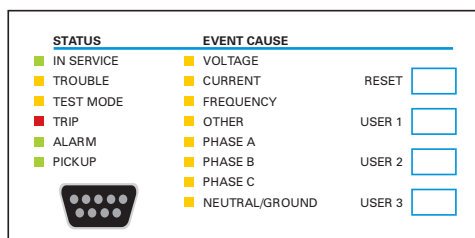


Figure 4-3: LED PANEL 1

STATUS INDICATORS:

- **IN SERVICE:** Indicates that control power is applied; all monitored inputs/outputs and internal systems are OK; the relay has been programmed.
- **TROUBLE:** Indicates that the relay has detected an internal problem.
- **TEST MODE:** Indicates that the relay is in test mode.
- **TRIP:** Indicates that the selected FlexLogic™ operand serving as a Trip switch has operated. This indicator always latches; the RESET command must be initiated to allow the latch to be reset.
- **ALARM:** Indicates that the selected FlexLogic™ operand serving as an Alarm switch has operated. This indicator is never latched.

- **PICKUP:** Indicates that an element is picked up. This indicator is never latched.

EVENT CAUSE INDICATORS:

These indicate the input type that was involved in a condition detected by an element that is operated or has a latched flag waiting to be reset.

- **VOLTAGE:** Indicates voltage was involved.
- **CURRENT:** Indicates current was involved.
- **FREQUENCY:** Indicates frequency was involved.
- **OTHER:** Indicates a composite function was involved.
- **PHASE A:** Indicates Phase A was involved.
- **PHASE B:** Indicates Phase B was involved.
- **PHASE C:** Indicates Phase C was involved.
- **NEUTRAL/GROUND:** Indicates neutral or ground was involved.

b) LED PANELS 2 AND 3

These panels provide 48 amber LED indicators whose operation is controlled by the user. Support for applying a customized label beside every LED is provided.

User customization of LED operation is of maximum benefit in installations where languages other than English are used to communicate with operators. Refer to the User-Programmable LEDs section in Chapter 5 for the settings used to program the operation of the LEDs on these panels.

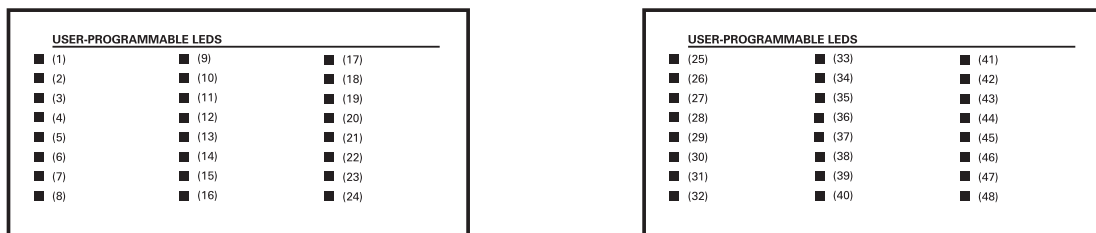


Figure 4-4: LED PANELS 2 AND 3 (INDEX TEMPLATE)

c) DEFAULT LABELS FOR LED PANEL 2

SETTINGS IN USE	BREAKER 1	SYNCHROCHECK
GROUP 1	OPEN	NO1 IN-SYNCH
GROUP 2	CLOSED	NO2 IN-SYNCH
GROUP 3	TROUBLE	
GROUP 4		RECLOSE
GROUP 5	BREAKER 2	ENABLED
GROUP 6	OPEN	DISABLED
GROUP 7	CLOSED	IN PROGRESS
GROUP 8	TROUBLE	LOCKED OUT

Figure 4-5: LED PANEL 2 (DEFAULT LABEL)

The default labels are intended to represent:

- **GROUP 1...6:** The illuminated GROUP is the active settings group.
- **BREAKER n OPEN:** The breaker is open.
- **BREAKER n CLOSED:** The breaker is closed.
- **BREAKER n TROUBLE:** A problem related to the breaker has been detected.
- **SYNCHROCHECK NO n IN-SYNCH:** Voltages have satisfied the synchrocheck element.

The relay is shipped with the default label for the LED panel 2. The LEDs, however, are not pre-programmed. To match the pre-printed label, the LED settings must be entered as shown in the *User-Programmable LEDs* section of Chapter 5. The LEDs are fully user-programmable. The default labels can be replaced by user-printed labels for both panels as explained in the following section.

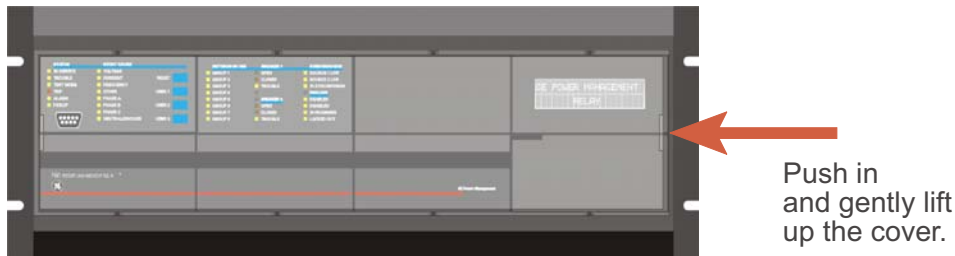
d) CUSTOM LABELING OF LEDs

Custom labeling of an LED-only panel is facilitated through a Microsoft Word file available from the following URL:

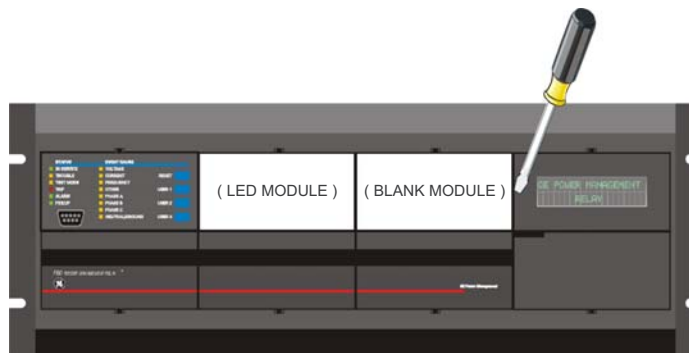
<http://www.GEindustrial.com/multilin/support/ur/>

This file provides templates and instructions for creating appropriate labeling for the LED panel. The following procedures are contained in the downloadable file. The panel templates provide relative LED locations and located example text (x) edit boxes. The following procedure demonstrates how to install/uninstall the custom panel labeling.

1. Remove the clear Lexan Front Cover (GE Multilin Part Number: 1501-0014).



2. Pop out the LED Module and/or the Blank Module with a screwdriver as shown below. Be careful not to damage the plastic.



3. Place the left side of the customized module back to the front panel frame, then snap back the right side.
4. Put the clear Lexan Front Cover back into place.

e) CUSTOMIZING THE DISPLAY MODULE

The following items are required to customize the N60 display module:

- Black and white or color printer (color preferred).
 - Microsoft Word 97 or later software for editing the template.
 - 1 each of: 8.5" x 11" white paper, exacto knife, ruler, custom display module (GE Multilin Part Number: 1516-0069), and a custom module cover (GE Multilin Part Number: 1502-0015).
1. Open the LED panel customization template with Microsoft Word. Add text in places of the **LED x** text placeholders on the template(s). Delete unused place holders as required.
 2. When complete, save the Word file to your local PC for future use.
 3. Print the template(s) to a local printer.
 4. From the printout, cut-out the Background Template from the three windows, using the cropmarks as a guide.

- Put the Background Template on top of the custom display module (GE Multilin Part Number: 1513-0069) and snap the clear custom module cover (GE Multilin Part Number: 1502-0015) over it and the templates.

4.2.3 DISPLAY

All messages are displayed on a 2×20 character vacuum fluorescent display to make them visible under poor lighting conditions. An optional liquid crystal display (LCD) is also available. Messages are displayed in English and do not require the aid of an instruction manual for deciphering. While the keypad and display are not actively being used, the display will default to defined messages. Any high priority event driven message will automatically override the default message and appear on the display.

4.2.4 KEYPAD

Display messages are organized into 'pages' under the following headings: Actual Values, Settings, Commands, and Targets. The **MENU** key navigates through these pages. Each heading page is broken down further into logical subgroups.

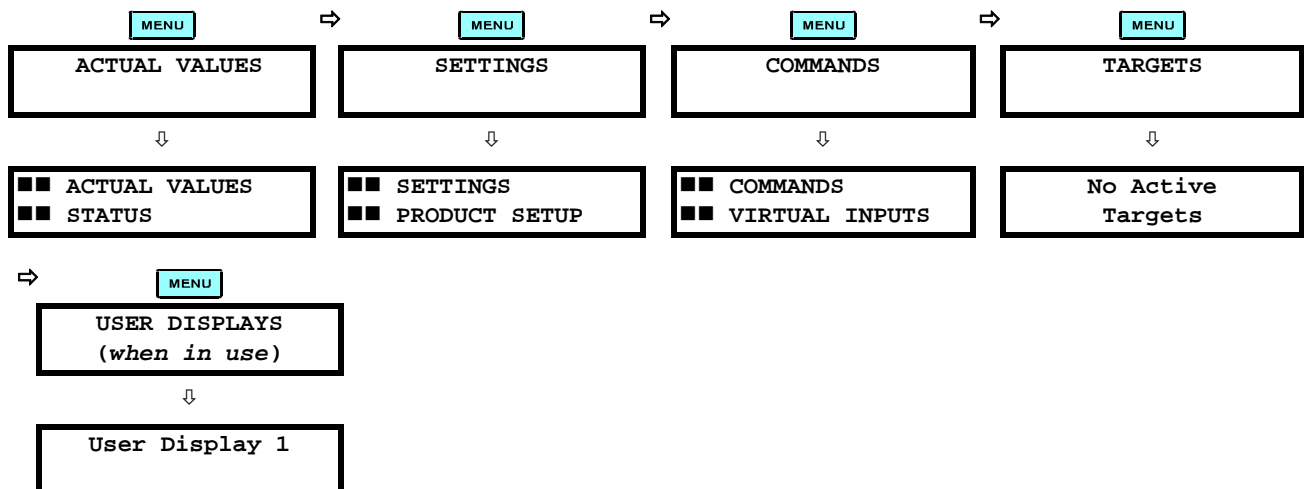
The **▲** **◀** MESSAGE **▶** **▼** keys navigate through the subgroups. The **▲** VALUE **▼** keys scroll increment or decrement numerical setting values when in programming mode. These keys also scroll through alphanumeric values in the text edit mode. Alternatively, values may also be entered with the numeric keypad.

The **■** key initiates and advance to the next character in text edit mode or enters a decimal point. The **HELP** key may be pressed at any time for context sensitive help messages. The **ENTER** key stores altered setting values.

4.2.5 MENUS

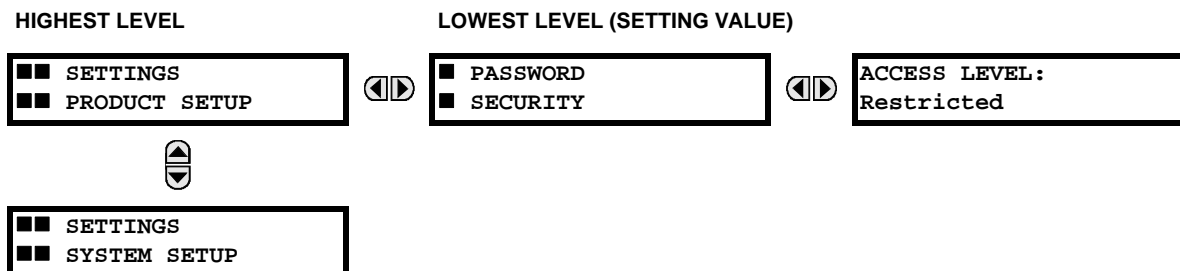
a) NAVIGATION

Press the **MENU** key to select the desired header display page (top-level menu). The header title appears momentarily followed by a header display page menu item. Each press of the **MENU** key advances through the main heading pages as illustrated below.

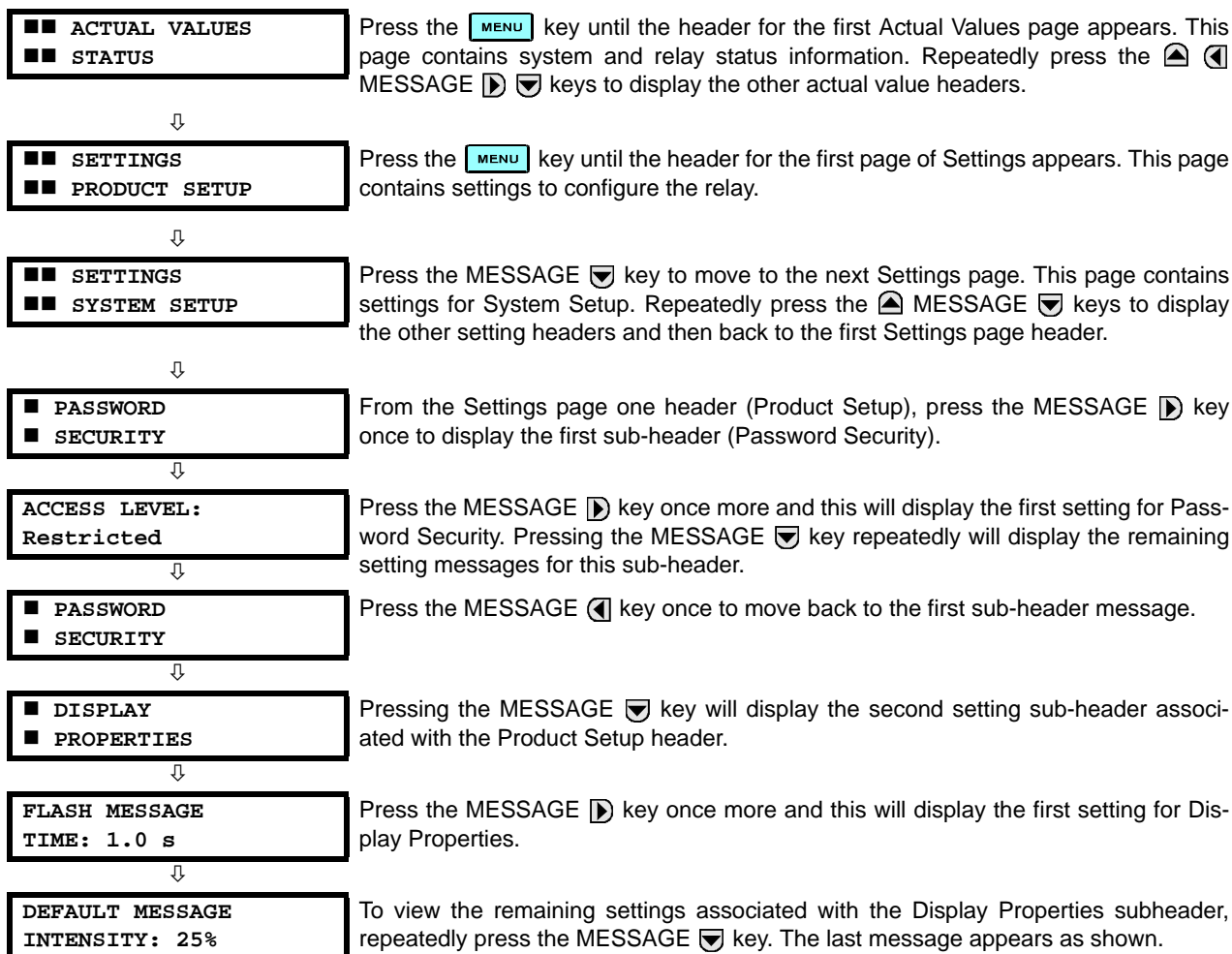


b) HIERARCHY

The setting and actual value messages are arranged hierarchically. The header display pages are indicated by double scroll bar characters (■ ■), while sub-header pages are indicated by single scroll bar characters (■). The header display pages represent the highest level of the hierarchy and the sub-header display pages fall below this level. The MESSAGE ▲ and ▼ keys move within a group of headers, sub-headers, setting values, or actual values. Continually pressing the MESSAGE ► key from a header display displays specific information for the header category. Conversely, continually pressing the ◀ MESSAGE key from a setting value or actual value display returns to the header display.



c) EXAMPLE MENU NAVIGATION



4.2.6 CHANGING SETTINGS

a) ENTERING NUMERICAL DATA

Each numerical setting has its own minimum, maximum, and increment value associated with it. These parameters define what values are acceptable for a setting.

FLASH MESSAGE TIME: 1.0 s	For example, select the SETTINGS ⇒ PRODUCT SETUP ⇒ DISPLAY PROPERTIES ⇒ FLASH MESSAGE TIME setting.
↓	
MINIMUM: 0.5 MAXIMUM: 10.0	Press the HELP key to view the minimum and maximum values. Press the HELP key again to view the next context sensitive help message.

Two methods of editing and storing a numerical setting value are available.

- **0 to 9** and **.** (decimal point): The relay numeric keypad works the same as that of any electronic calculator. A number is entered one digit at a time. The leftmost digit is entered first and the rightmost digit is entered last. Pressing the MESSAGE **⏏** key or pressing the ESCAPE key, returns the original value to the display.
- **VALUE** **▲** **▼**: The VALUE **▲** key increments the displayed value by the step value, up to the maximum value allowed. While at the maximum value, pressing the VALUE **▲** key again will allow the setting selection to continue upward from the minimum value. The VALUE **▼** key decrements the displayed value by the step value, down to the minimum value. While at the minimum value, pressing the VALUE **▼** key again will allow the setting selection to continue downward from the maximum value.

FLASH MESSAGE TIME: 2.5 s	As an example, set the flash message time setting to 2.5 seconds. Press the appropriate numeric keys in the sequence "2 . 5". The display message will change as the digits are being entered.
↓	
NEW SETTING HAS BEEN STORED	Until ENTER is pressed, editing changes are not registered by the relay. Therefore, press ENTER to store the new value in memory. This flash message will momentarily appear as confirmation of the storing process. Numerical values which contain decimal places will be rounded-off if more decimal place digits are entered than specified by the step value.

b) ENTERING ENUMERATION DATA

Enumeration settings have data values which are part of a set, whose members are explicitly defined by a name. A set is comprised of two or more members.

ACCESS LEVEL: Restricted	For example, the selections available for ACCESS LEVEL are "Restricted", "Command", "Setting", and "Factory Service".
-----------------------------	--

Enumeration type values are changed using the VALUE keys. The VALUE **▲** key displays the next selection while the VALUE **▼** key displays the previous selection.






ACCESS LEVEL: Setting	If the ACCESS LEVEL needs to be "Setting", press the VALUE keys until the proper selection is displayed. Press HELP at any time for the context sensitive help messages.
↓	
NEW SETTING HAS BEEN STORED	Changes are not registered by the relay until the ENTER key is pressed. Pressing ENTER stores the new value in memory. This flash message momentarily appears as confirmation of the storing process.

c) ENTERING ALPHANUMERIC TEXT

Text settings have data values which are fixed in length, but user-defined in character. They may be comprised of upper case letters, lower case letters, numerals, and a selection of special characters.

There are several places where text messages may be programmed to allow the relay to be customized for specific applications. One example is the Message Scratchpad. Use the following procedure to enter alphanumeric text messages.

For example: to enter the text, "Breaker #1"





1. Press  to enter text edit mode.
2. Press the VALUE keys until the character 'B' appears; press  to advance the cursor to the next position.
3. Repeat step 2 for the remaining characters: r,e,a,k,e,r, ,#,1.
4. Press  to store the text.
5. If you have any problem, press  to view context sensitive help. Flash messages will sequentially appear for several seconds each. For the case of a text setting message, pressing  displays how to edit and store new values.

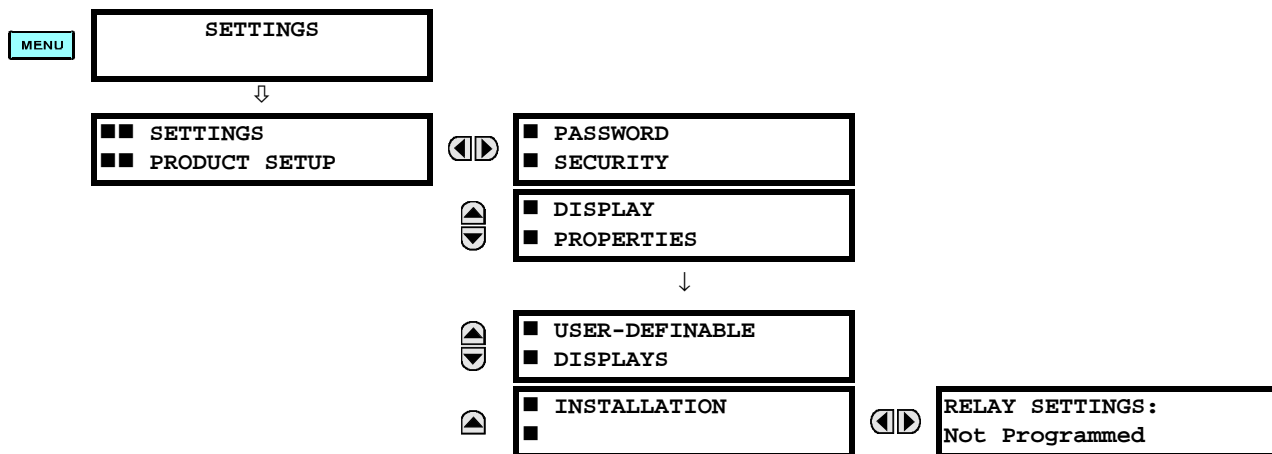
d) ACTIVATING THE RELAY


RELAY SETTINGS:
Not Programmed

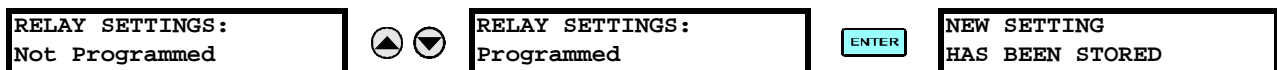
When the relay is powered up, the Trouble LED will be on, the In Service LED off, and this message displayed, indicating the relay is in the "Not Programmed" state and is safeguarding (output relays blocked) against the installation of a relay whose settings have not been entered. This message remains until the relay is explicitly put in the "Programmed" state.

To change the **RELAY SETTINGS: "Not Programmed"** mode to "Programmed", proceed as follows:

1. Press the  key until the **SETTINGS** header flashes momentarily and the **SETTINGS PRODUCT SETUP** message appears on the display.
2. Press the MESSAGE  key until the **PASSWORD SECURITY** message appears on the display.
3. Press the MESSAGE  key until the **INSTALLATION** message appears on the display.
4. Press the MESSAGE  key until the **RELAY SETTINGS: Not Programmed** message is displayed.



5. After the **RELAY SETTINGS: Not Programmed** message appears on the display, press the VALUE keys change the selection to "Programmed".
6. Press the  key.

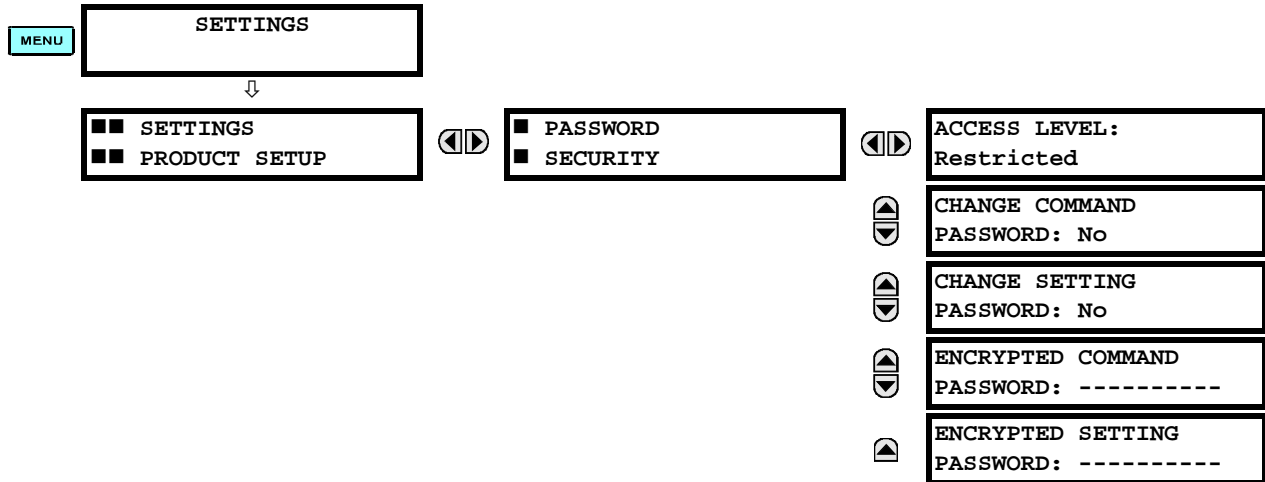


7. When the "NEW SETTING HAS BEEN STORED" message appears, the relay will be in "Programmed" state and the In Service LED will turn on.

e) ENTERING INITIAL PASSWORDS

To enter the initial Setting (or Command) Password, proceed as follows:

1. Press the **MENU** key until the **SETTINGS** header flashes momentarily and the **SETTINGS PRODUCT SETUP** message appears on the display.
2. Press the MESSAGE **▶** key until the **ACCESS LEVEL** message appears on the display.
3. Press the MESSAGE **▼** key until the **CHANGE SETTING (or COMMAND) PASSWORD** message appears on the display.



4. After the **CHANGE...PASSWORD** message appears on the display, press the VALUE **▲** key or the VALUE **▼** key to change the selection to "Yes".
5. Press the **ENTER** key and the display will prompt you to **ENTER NEW PASSWORD**.
6. Type in a numerical password (up to 10 characters) and press the **ENTER** key.
7. When the **VERIFY NEW PASSWORD** is displayed, re-type in the same password and press **ENTER**.



8. When the **NEW PASSWORD HAS BEEN STORED** message appears, your new Setting (or Command) Password will be active.

f) CHANGING EXISTING PASSWORDS

To change an existing password, follow the instructions in the previous section with the following exception. A message will prompt you to type in the existing password (for each security level) before a new password can be entered.

In the event that a password has been lost (forgotten), submit the corresponding Encrypted Password from the **PASSWORD SECURITY** menu to the Factory for decoding.

g) INVALID PASSWORD ENTRY

In the event that an incorrect Command or Setting password has been entered via the faceplate interface three times within a three-minute time span, the LOCAL ACCESS DENIED FlexLogic™ operand will be set to “On” and the N60 will not allow Settings or Command access via the faceplate interface for the next ten minutes. The **TOO MANY ATTEMPTS – BLOCKED FOR 10 MIN!** flash message will appear upon activation of the ten minute timeout or any other time a user attempts any change to the defined tier during the ten minute timeout. The LOCAL ACCESS DENIED FlexLogic™ operand will be set to “Off” after the expiration of the ten-minute timeout.

In the event that an incorrect Command or Setting password has been entered via the any external communications interface three times within a three-minute time span, the REMOTE ACCESS DENIED FlexLogic™ operand will be set to “On” and the N60 will not allow Settings or Command access via the any external communications interface for the next ten minutes. The REMOTE ACCESS DENIED FlexLogic™ operand will be set to “Off” after the expiration of the ten-minute timeout.

5.1.1 SETTINGS MAIN MENU

<div> <div>■ ■ SETTINGS</div> <div>■ ■ PRODUCT SETUP</div> </div>	◀▶	<div>■ PASSWORD</div> <div>■ SECURITY</div>	See page 5-8.
▼	▲▼	<div>■ DISPLAY</div> <div>■ PROPERTIES</div>	See page 5-9.
	▲▼	<div>■ CLEAR RELAY</div> <div>■ RECORDS</div>	See page 5-11.
	▲▼	<div>■ COMMUNICATIONS</div> <div>■</div>	See page 5-12.
	▲▼	<div>■ MODBUS USER MAP</div> <div>■</div>	See page 5-22.
	▲▼	<div>■ REAL TIME</div> <div>■ CLOCK</div>	See page 5-22..
	▲▼	<div>■ OSCILLOGRAPHY</div> <div>■</div>	See page 5-23.
	▲▼	<div>■ DATA LOGGER</div> <div>■</div>	See page 5-25.
	▲▼	<div>■ DEMAND</div> <div>■</div>	See page 5-26.
	▲▼	<div>■ USER-PROGRAMMABLE</div> <div>■ LEDS</div>	See page 5-27.
	▲▼	<div>■ USER-PROGRAMMABLE</div> <div>■ SELF TESTS</div>	See page 5-31.
	▲▼	<div>■ CONTROL</div> <div>■ PUSHBUTTONS</div>	See page 5-32.
	▲▼	<div>■ USER-PROGRAMMABLE</div> <div>■ PUSHBUTTONS</div>	See page 5-33.
	▲▼	<div>■ FLEX STATE</div> <div>■ PARAMETERS</div>	See page 5-34.
	▲▼	<div>■ USER-DEFINABLE</div> <div>■ DISPLAYS</div>	See page 5-35.
	▲▼	<div>■ DIRECT I/O</div> <div>■</div>	See page 5-37.
	▲	<div>■ INSTALLATION</div> <div>■</div>	See page 5-42.
▲			
<div> <div>■ ■ SETTINGS</div> <div>■ ■ SYSTEM SETUP</div> </div>	◀▶	<div>■ AC INPUTS</div> <div>■</div>	See page 5-43.
▼	▲▼	<div>■ POWER SYSTEM</div> <div>■</div>	See page 5-45.
	▲▼	<div>■ SIGNAL SOURCES</div> <div>■</div>	See page 5-46.
	▲▼	<div>■ BREAKERS</div> <div>■</div>	See page 5-48.

<div>▲</div> <div>■ ■ SETTINGS</div> <div>■ ■ FLEXLOGIC</div> <div>▼</div>	▲	<div>■ PHASOR MEASUREMENT</div> <div>■ UNIT</div>	See page 5-51.
	◀▶	<div>■ FLEXLOGIC</div> <div>■ EQUATION EDITOR</div>	See page 5-79.
	▲▼	<div>■ FLEXLOGIC</div> <div>■ TIMERS</div>	See page 5-79.
	▲▼	<div>■ FLEXELEMENTS</div> <div>■</div>	See page 5-80.
	▲	<div>■ NON-VOLATILE</div> <div>■ LATCHES</div>	See page 5-84.
<div>▲</div> <div>■ ■ SETTINGS</div> <div>■ ■ GROUPED ELEMENTS</div> <div>▼</div>	◀▶	<div>■ SETTING GROUP 1</div> <div>■</div>	See page 5-85.
	▲▼	<div>■ SETTING GROUP 2</div> <div>■</div>	
	▲▼	<div>■ SETTING GROUP 3</div> <div>■</div>	
	▲▼	<div>■ SETTING GROUP 4</div> <div>■</div>	
	▲▼	<div>■ SETTING GROUP 5</div> <div>■</div>	
	▲	<div>■ SETTING GROUP 6</div> <div>■</div>	
<div>▲</div> <div>■ ■ SETTINGS</div> <div>■ ■ CONTROL ELEMENTS</div> <div>▼</div>	◀▶	<div>■ SETTING GROUPS</div> <div>■</div>	See page 5-103.
	▲▼	<div>■ SELECTOR SWITCH</div> <div>■</div>	See page 5-104.
	▲▼	<div>■ UNDERFREQUENCY</div> <div>■</div>	See page 5-110.
	▲▼	<div>■ OVERFREQUENCY</div> <div>■</div>	See page 5-111.
	▲▼	<div>■ SYNCHROCHECK</div> <div>■</div>	See page 5-112.
	▲▼	<div>■ DIGITAL ELEMENTS</div> <div>■</div>	See page 5-116.
	▲▼	<div>■ DIGITAL COUNTERS</div> <div>■</div>	See page 5-119.
	▲▼	<div>■ MONITORING</div> <div>■ ELEMENTS</div>	See page 5-121.
	▲▼	<div>■ FREQUENCY RATE</div> <div>■ OF CHANGE</div>	See page 5-122.
	▲▼	<div>■ DIGITIZERS</div> <div>■</div>	See page 5-124.

<div>▲</div> <div>■ ■ SETTINGS</div> <div>■ ■ INPUTS/OUTPUTS</div> <div>▼</div>	▲	■ 8-BIT COMPARATORS	See page 5-127.
	▲	■ 8-BIT SWITCHES	See page 5-133.
<div>▲</div> <div>■ ■ SETTINGS</div> <div>■ ■ INPUTS/OUTPUTS</div> <div>▼</div>	◀▶	■ CONTACT INPUTS	See page 5-135.
	▲	■ VIRTUAL INPUTS	See page 5-137.
	▲	■ CONTACT OUTPUTS	See page 5-138.
	▲	■ VIRTUAL OUTPUTS	See page 5-140.
	▲	■ REMOTE DEVICES	See page 5-141.
	▲	■ REMOTE INPUTS	See page 5-142.
	▲	■ REMOTE OUTPUTS	See page 5-143.
	▲	■ DNA BIT PAIRS	See page 5-143.
	▲	■ REMOTE OUTPUTS	See page 5-143.
	▲	■ UserSt BIT PAIRS	See page 5-143.
	▲	■ RESETTING	See page 5-144.
	▲	■ DIRECT INPUTS	See page 5-144.
<div>▲</div> <div>■ ■ SETTINGS</div> <div>■ ■ TRANSDUCER I/O</div> <div>▼</div>	▲	■ DIRECT OUTPUTS	See page 5-144.
	◀▶	■ DCMA INPUTS	See page 5-148.
	▲	■ RTD INPUTS	See page 5-149.
<div>▲</div> <div>■ ■ SETTINGS</div> <div>■ ■ TESTING</div> <div>▼</div>	▲	■ DCMA OUTPUTS	See page 5-149.
	◀▶	TEST MODE FUNCTION: Disabled	See page 5-153.
	▲	TEST MODE INITIATE: On	See page 5-153.
	▲	■ FORCE CONTACT ■ INPUTS	See page 5-153.
	▲	■ FORCE CONTACT ■ OUTPUTS	See page 5-154.
	▲	■ PMU ■ TEST VALUES	See page 5-155.

5.1.2 INTRODUCTION TO ELEMENTS

In the design of UR relays, the term “element” is used to describe a feature that is based around a comparator. The comparator is provided with an input (or set of inputs) that is tested against a programmed setting (or group of settings) to determine if the input is within the defined range that will set the output to logic 1, also referred to as “setting the flag”. A single comparator may make multiple tests and provide multiple outputs; for example, the time overcurrent comparator sets a Pickup flag when the current input is above the setting and sets an Operate flag when the input current has been at a level above the pickup setting for the time specified by the time-current curve settings. All comparators, except the Digital Element which uses a logic state as the input, use analog parameter actual values as the input.

Elements are arranged into two classes, GROUPED and CONTROL. Each element classed as a GROUPED element is provided with six alternate sets of settings, in setting groups numbered 1 through 6. The performance of a GROUPED element is defined by the setting group that is active at a given time. The performance of a CONTROL element is independent of the selected active setting group.

The main characteristics of an element are shown on the element logic diagram. This includes the input(s), settings, fixed logic, and the output operands generated (abbreviations used on scheme logic diagrams are defined in Appendix F).

Some settings for current and voltage elements are specified in per-unit (pu) calculated quantities:

pu quantity = (actual quantity) / (base quantity)

- For current elements, the ‘base quantity’ is the nominal secondary or primary current of the CT. Where the current source is the sum of two CTs with different ratios, the ‘base quantity’ will be the common secondary or primary current to which the sum is scaled (i.e. normalized to the larger of the 2 rated CT inputs). For example, if CT1 = 300 / 5 A and CT2 = 100 / 5 A, then in order to sum these, CT2 is scaled to the CT1 ratio. In this case, the ‘base quantity’ will be 5 A secondary or 300 A primary.
- For voltage elements the ‘base quantity’ is the nominal primary voltage of the protected system which corresponds (based on VT ratio and connection) to secondary VT voltage applied to the relay. For example, on a system with a 13.8 kV nominal primary voltage and with 14400:120 V Delta-connected VTs, the secondary nominal voltage (1 pu) would be:

$$\frac{13800}{14400} \times 120 = 115 \text{ V} \quad (\text{EQ 5.1})$$

For Wye-connected VTs, the secondary nominal voltage (1 pu) would be:

$$\frac{13800}{14400} \times \frac{120}{\sqrt{3}} = 66.4 \text{ V} \quad (\text{EQ 5.2})$$

Many settings are common to most elements and are discussed below:

- FUNCTION setting:** This setting programs the element to be operational when selected as “Enabled”. The factory default is “Disabled”. Once programmed to “Enabled”, any element associated with the Function becomes active and all options become available.
- NAME setting:** This setting is used to uniquely identify the element.
- SOURCE setting:** This setting is used to select the parameter or set of parameters to be monitored.
- PICKUP setting:** For simple elements, this setting is used to program the level of the measured parameter above or below which the pickup state is established. In more complex elements, a set of settings may be provided to define the range of the measured parameters which will cause the element to pickup.
- PICKUP DELAY setting:** This setting sets a time-delay-on-pickup, or on-delay, for the duration between the Pickup and Operate output states.
- RESET DELAY setting:** This setting is used to set a time-delay-on-dropout, or off-delay, for the duration between the Operate output state and the return to logic 0 after the input transits outside the defined pickup range.
- BLOCK setting:** The default output operand state of all comparators is a logic 0 or “flag not set”. The comparator remains in this default state until a logic 1 is asserted at the RUN input, allowing the test to be performed. If the RUN input changes to logic 0 at any time, the comparator returns to the default state. The RUN input is used to supervise the comparator. The BLOCK input is used as one of the inputs to RUN control.

- **TARGET setting:** This setting is used to define the operation of an element target message. When set to Disabled, no target message or illumination of a faceplate LED indicator is issued upon operation of the element. When set to Self-Reset, the target message and LED indication follow the Operate state of the element, and self-resets once the operate element condition clears. When set to Latched, the target message and LED indication will remain visible after the element output returns to logic 0 - until a RESET command is received by the relay.
- **EVENTS setting:** This setting is used to control whether the Pickup, Dropout or Operate states are recorded by the event recorder. When set to Disabled, element pickup, dropout or operate are not recorded as events. When set to Enabled, events are created for:
 - (Element) PKP (pickup)
 - (Element) DPO (dropout)
 - (Element) OP (operate)

The DPO event is created when the measure and decide comparator output transits from the pickup state (logic 1) to the dropout state (logic 0). This could happen when the element is in the operate state if the reset delay time is not '0'.

5.1.3 INTRODUCTION TO AC SOURCES

a) BACKGROUND

The N60 may be used on systems with breaker-and-a-half or ring bus configurations. In these applications, each of the two three-phase sets of individual phase currents (one associated with each breaker) can be used as an input to a breaker failure element. The sum of both breaker phase currents and 3I₀ residual currents may be required for the circuit relaying and metering functions. For a three-winding transformer application, it may be required to calculate watts and vars for each of three windings, using voltage from different sets of VTs. These requirements can be satisfied with a single UR, equipped with sufficient CT and VT input channels, by selecting the parameter to measure. A mechanism is provided to specify the AC parameter (or group of parameters) used as the input to protection/control comparators and some metering elements.

Selection of the parameter(s) to measure is partially performed by the design of a measuring element or protection/control comparator by identifying the type of parameter (fundamental frequency phasor, harmonic phasor, symmetrical component, total waveform RMS magnitude, phase-phase or phase-ground voltage, etc.) to measure. The user completes the process by selecting the instrument transformer input channels to use and some of the parameters calculated from these channels. The input parameters available include the summation of currents from multiple input channels. For the summed currents of phase, 3I₀, and ground current, current from CTs with different ratios are adjusted to a single ratio before summation.

A mechanism called a "Source" configures the routing of CT and VT input channels to measurement sub-systems. Sources, in the context of UR series relays, refer to the logical grouping of current and voltage signals such that one source contains all the signals required to measure the load or fault in a particular power apparatus. A given source may contain all or some of the following signals: three-phase currents, single-phase ground current, three-phase voltages and an auxiliary voltage from a single VT for checking for synchronism.

To illustrate the concept of Sources, as applied to current inputs only, consider the breaker-and-a-half scheme below. In this application, the current flows as shown by the arrows. Some current flows through the upper bus bar to some other location or power equipment, and some current flows into transformer Winding 1. The current into Winding 1 is the phasor sum (or difference) of the currents in CT1 and CT2 (whether the sum or difference is used depends on the relative polarity of the CT connections). The same considerations apply to transformer Winding 2. The protection elements require access to the net current for transformer protection, but some elements may need access to the individual currents from CT1 and CT2.

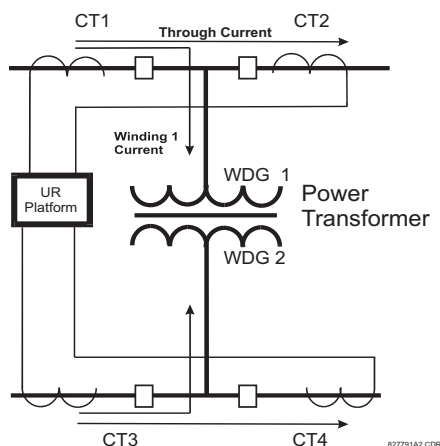


Figure 5–1: BREAKER-AND-A-HALF SCHEME

In conventional analog or electronic relays, the sum of the currents is obtained from an appropriate external connection of all CTs through which any portion of the current for the element being protected could flow. Auxiliary CTs are required to perform ratio matching if the ratios of the primary CTs to be summed are not identical. In the UR series of relays, provisions have been included for all the current signals to be brought to the UR device where grouping, ratio correction and summation are applied internally via configuration settings.

A major advantage of using internal summation is that the individual currents are available to the protection device; for example, as additional information to calculate a restraint current, or to allow the provision of additional protection features that operate on the individual currents such as breaker failure.

Given the flexibility of this approach, it becomes necessary to add configuration settings to the platform to allow the user to select which sets of CT inputs will be added to form the net current into the protected device.

The internal grouping of current and voltage signals forms an internal source. This source can be given a specific name through the settings, and becomes available to protection and metering elements in the UR platform. Individual names can be given to each source to help identify them more clearly for later use. For example, in the scheme shown in the above diagram, the configures one Source to be the sum of CT1 and CT2 and can name this Source as “Wdg 1 Current”.

Once the sources have been configured, the user has them available as selections for the choice of input signal for the protection elements and as metered quantities.

b) CT/VT MODULE CONFIGURATION

CT and VT input channels are contained in CT/VT modules. The type of input channel can be phase/neutral/other voltage, phase/ground current, or sensitive ground current. The CT/VT modules calculate total waveform RMS levels, fundamental frequency phasors, symmetrical components and harmonics for voltage or current, as allowed by the hardware in each channel. These modules may calculate other parameters as directed by the CPU module.

A CT/VT module contains up to eight input channels, numbered 1 through 8. The channel numbering corresponds to the module terminal numbering 1 through 8 and is arranged as follows: Channels 1, 2, 3 and 4 are always provided as a group, hereafter called a “bank,” and all four are either current or voltage, as are Channels 5, 6, 7 and 8. Channels 1, 2, 3 and 5, 6, 7 are arranged as phase A, B and C respectively. Channels 4 and 8 are either another current or voltage.

Banks are ordered sequentially from the block of lower-numbered channels to the block of higher-numbered channels, and from the CT/VT module with the lowest slot position letter to the module with the highest slot position letter, as follows:

The UR platform allows for a maximum of three sets of three-phase voltages and six sets of three-phase currents. The result of these restrictions leads to the maximum number of CT/VT modules in a chassis to three. The maximum number of sources is six. A summary of CT/VT module configurations is shown below.

ITEM	MAXIMUM NUMBER
CT/VT Module	3
CT Bank (3 phase channels, 1 ground channel)	12
VT Bank (3 phase channels, 1 auxiliary channel)	6

c) CT/VT INPUT CHANNEL CONFIGURATION

Upon relay startup, configuration settings for every bank of current or voltage input channels in the relay are automatically generated from the order code. Within each bank, a channel identification label is automatically assigned to each bank of channels in a given product. The 'bank' naming convention is based on the physical location of the channels, required by the user to know how to connect the relay to external circuits. Bank identification consists of the letter designation of the slot in which the CT/VT module is mounted as the first character, followed by numbers indicating the channel, either 1 or 5.

For three-phase channel sets, the number of the lowest numbered channel identifies the set. For example, F1 represents the three-phase channel set of F1/F2/F3, where F is the slot letter and 1 is the first channel of the set of three channels.

Upon startup, the CPU configures the settings required to characterize the current and voltage inputs, and will display them in the appropriate section in the sequence of the banks (as described above) as follows for a maximum configuration: F1, F5, M1, M5, U1, and U5.

The above section explains how the input channels are identified and configured to the specific application instrument transformers and the connections of these transformers. The specific parameters to be used by each measuring element and comparator, and some actual values are controlled by selecting a specific source. The source is a group of current and voltage input channels selected by the user to facilitate this selection. With this mechanism, a user does not have to make multiple selections of voltage and current for those elements that need both parameters, such as a distance element or a watt calculation. It also gathers associated parameters for display purposes.

The basic idea of arranging a source is to select a point on the power system where information is of interest. An application example of the grouping of parameters in a Source is a transformer winding, on which a three phase voltage is measured, and the sum of the currents from CTs on each of two breakers is required to measure the winding current flow.

5.2.1 PASSWORD SECURITY

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ PASSWORD SECURITY

<div> <div>■ PASSWORD</div> <div>■ SECURITY</div> </div>	<div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> </div>	<div> <div>◀▶</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> </div>	ACCESS LEVEL: Restricted	Range: Restricted, Command, Setting, Factory Service (for factory use only)
			CHANGE COMMAND PASSWORD: No	Range: No, Yes
			CHANGE SETTING PASSWORD: No	Range: No, Yes
			ENCRYPTED COMMAND PASSWORD: -----	Range: 0 to 9999999999 Note: ----- indicates no password
			ENCRYPTED SETTING PASSWORD: -----	Range: 0 to 9999999999 Note: ----- indicates no password

Two levels of password security are provided: **Command** and **Setting**. The following command operations are under password supervision:

operating the breakers via faceplate keypad, changing the state of virtual inputs, clearing the event records, clearing the oscillography records, changing the date and time, clearing energy records, clearing the data logger, user-programmable pushbuttons

The following setting operations are under password supervision:

changing any setting, test mode operation

The Command and Setting passwords are defaulted to "Null" when the relay is shipped from the factory. When a password is set to "Null", the password security feature is disabled.

Programming a password code is required to enable each access level. A password consists of one to ten numerical characters. When a **CHANGE ... PASSWORD** setting is set to "Yes", the following message sequence is invoked:

1. ENTER NEW PASSWORD: _____
2. VERIFY NEW PASSWORD: _____
3. NEW PASSWORD HAS BEEN STORED

To gain write access to a "Restricted" setting, set **ACCESS LEVEL** to "Setting" and then change the setting, or attempt to change the setting and follow the prompt to enter the programmed password. If the password is correctly entered, access will be allowed. If no keys are pressed for longer than 30 minutes or control power is cycled, accessibility will automatically revert to the "Restricted" level.

If an entered password is lost (or forgotten), consult the factory with the corresponding **ENCRYPTED PASSWORD**.

In the event that an incorrect Command or Setting password has been entered via the faceplate interface three times within a three-minute time span, the LOCAL ACCESS DENIED FlexLogic™ operand will be set to "On" and the N60 will not allow Settings or Command access via the faceplate interface for the next ten minutes. The **TOO MANY ATTEMPTS – BLOCKED FOR 10 MIN!** flash message will appear upon activation of the ten minute timeout or any other time a user attempts any change to the defined tier during the ten minute timeout. The LOCAL ACCESS DENIED FlexLogic™ operand will be set to "Off" after the expiration of the ten-minute timeout.

In the event that an incorrect Command or Setting password has been entered via the any external communications interface three times within a three-minute time span, the REMOTE ACCESS DENIED FlexLogic™ operand will be set to "On" and the N60 will not allow Settings or Command access via the any external communications interface for the next ten minutes. The REMOTE ACCESS DENIED FlexLogic™ operand will be set to "Off" after the expiration of the ten-minute timeout.

The N60 provides a means to raise an alarm upon failed password entry. Should password verification fail while accessing a password-protected level of the relay (either settings or commands), the UNAUTHORIZED ACCESS FlexLogic™ operand is asserted. The operand can be programmed to raise an alarm via contact outputs or communications. This feature can be used to protect against both unauthorized and accidental access attempts.

The UNAUTHORIZED ACCESS operand is reset with the **COMMANDS** ⇒⇩ **CLEAR RECORDS** ⇒⇩ **RESET UNAUTHORIZED ALARMS** command. Therefore, to apply this feature with security, the command level should be password-protected. The operand does not generate events or targets. If these are required, the operand can be assigned to a digital element programmed with event logs and/or targets enabled.



If the Setting and Command passwords are identical, this one password allows access to both commands and settings.



When EnerVista UR Setup is used to access a particular level, the user will continue to have access to that level as long as there are open windows in the EnerVista UR Setup software. To re-establish the password security feature, all windows must be closed for at least 30 minutes.

5.2.2 DISPLAY PROPERTIES

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒⇩ DISPLAY PROPERTIES

■ DISPLAY	◀▶	LANGUAGE:	English	Range: English; English, French; English, Russian; English, Chinese (range dependent on order code)
■ PROPERTIES		FLASH MESSAGE	TIME: 1.0 s	Range: 0.5 to 10.0 s in steps of 0.1
MESSAGE	▲▼	DEFAULT MESSAGE	TIMEOUT: 300 s	Range: 10 to 900 s in steps of 1
MESSAGE	▲▼	DEFAULT MESSAGE	INTENSITY: 25 %	Range: 25%, 50%, 75%, 100% Visible only if a VFD is installed
MESSAGE	▲▼	SCREEN SAVER	FEATURE: Disabled	Range: Disabled, Enabled Visible only if an LCD is installed
MESSAGE	▲▼	SCREEN SAVER WAIT	TIME: 30 min	Range: 1 to 65535 min. in steps of 1 Visible only if an LCD is installed
MESSAGE	▲▼	CURRENT CUT-OFF	LEVEL: 0.020 pu	Range: 0.002 to 0.020 pu in steps of 0.001
MESSAGE	▲	VOLTAGE CUT-OFF	LEVEL: 1.0 V	Range: 0.1 to 1.0 V secondary in steps of 0.1

Some relay messaging characteristics can be modified to suit different situations using the display properties settings.

- **LANGUAGE:** This setting selects the language used to display settings, actual values, and targets. The range is dependent on the order code of the relay.
- **FLASH MESSAGE TIME:** Flash messages are status, warning, error, or information messages displayed for several seconds in response to certain key presses during setting programming. These messages override any normal messages. The duration of a flash message on the display can be changed to accommodate different reading rates.
- **DEFAULT MESSAGE TIMEOUT:** If the keypad is inactive for a period of time, the relay automatically reverts to a default message. The inactivity time is modified via this setting to ensure messages remain on the screen long enough during programming or reading of actual values.
- **DEFAULT MESSAGE INTENSITY:** To extend phosphor life in the vacuum fluorescent display, the brightness can be attenuated during default message display. During keypad interrogation, the display always operates at full brightness.
- **SCREEN SAVER FEATURE** and **SCREEN SAVER WAIT TIME:** These settings are only visible if the N60 has a liquid crystal display (LCD) and control its backlighting. When the **SCREEN SAVER FEATURE** is "Enabled", the LCD backlighting is turned off after the **DEFAULT MESSAGE TIMEOUT** followed by the **SCREEN SAVER WAIT TIME**, providing that no keys have been pressed and no target messages are active. When a keypress occurs or a target becomes active, the LCD backlighting is turned on.
- **CURRENT CUT-OFF LEVEL:** This setting modifies the current cut-off threshold. Very low currents (1 to 2% of the rated value) are very susceptible to noise. Some customers prefer very low currents to display as zero, while others prefer the current be displayed even when the value reflects noise rather than the actual signal. The N60 applies a cut-off value to the magnitudes and angles of the measured currents. If the magnitude is below the cut-off level, it is substi-

tuted with zero. This applies to phase and ground current phasors as well as true RMS values and symmetrical components. The cut-off operation applies to quantities used for metering, protection, and control, as well as those used by communications protocols. Note that the cut-off level for the sensitive ground input is 10 times lower than the **CURRENT CUT-OFF LEVEL** setting value. Raw current samples available via oscillography are not subject to cut-off.

- **VOLTAGE CUT-OFF LEVEL:** This setting modifies the voltage cut-off threshold. Very low secondary voltage measurements (at the fractional volt level) can be affected by noise. Some customers prefer these low voltages to be displayed as zero, while others prefer the voltage to be displayed even when the value reflects noise rather than the actual signal. The N60 applies a cut-off value to the magnitudes and angles of the measured voltages. If the magnitude is below the cut-off level, it is substituted with zero. This operation applies to phase and auxiliary voltages, and symmetrical components. The cut-off operation applies to quantities used for metering, protection, and control, as well as those used by communications protocols. Raw samples of the voltages available via oscillography are not subject cut-off.

The **CURRENT CUT-OFF LEVEL** and the **VOLTAGE CUT-OFF LEVEL** are used to determine the metered power cut-off levels. The power cut-off level is calculated as shown below. For Delta connections:

$$\text{3-phase power cut-off} = \frac{\sqrt{3} \times \text{CURRENT CUT-OFF LEVEL} \times \text{VOLTAGE CUT-OFF LEVEL} \times \text{VT primary} \times \text{CT primary}}{\text{VT secondary}} \quad (\text{EQ 5.3})$$

For Wye connections:

$$\text{3-phase power cut-off} = \frac{3 \times \text{CURRENT CUT-OFF LEVEL} \times \text{VOLTAGE CUT-OFF LEVEL} \times \text{VT primary} \times \text{CT primary}}{\text{VT secondary}} \quad (\text{EQ 5.4})$$

$$\text{per-phase power cut-off} = \frac{\text{CURRENT CUT-OFF LEVEL} \times \text{VOLTAGE CUT-OFF LEVEL} \times \text{VT primary} \times \text{CT primary}}{\text{VT secondary}} \quad (\text{EQ 5.5})$$

where VT primary = VT secondary × VT ratio and CT primary = CT secondary × CT ratio.

For example, given the following settings:

CURRENT CUT-OFF LEVEL: "0.02 pu"

VOLTAGE CUT-OFF LEVEL: "1.0 V"

PHASE CT PRIMARY: "100 A"

PHASE VT SECONDARY: "66.4 V"

PHASE VT RATIO: "208.00 : 1"

PHASE VT CONNECTION: "Delta".

We have:

CT primary = "100 A", and

VT primary = PHASE VT SECONDARY × PHASE VT RATIO = 66.4 V × 208 = 13811.2 V

The power cut-off is therefore:

$$\begin{aligned} \text{power cut-off} &= (\text{CURRENT CUT-OFF LEVEL} \times \text{VOLTAGE CUT-OFF LEVEL} \times \text{CT primary} \times \text{VT primary}) / \text{VT secondary} \\ &= (\sqrt{3} \times 0.02 \text{ pu} \times 1.0 \text{ V} \times 100 \text{ A} \times 13811.2 \text{ V}) / 66.4 \text{ V} \\ &= 720.5 \text{ watts} \end{aligned}$$

Any calculated power value below this cut-off will not be displayed. As well, the three-phase energy data will not accumulate if the total power from all three phases does not exceed the power cut-off.



Lower the VOLTAGE CUT-OFF LEVEL and CURRENT CUT-OFF LEVEL with care as the relay accepts lower signals as valid measurements. Unless dictated otherwise by a specific application, the default settings of "0.02 pu" for CURRENT CUT-OFF LEVEL and "1.0 V" for VOLTAGE CUT-OFF LEVEL are recommended.

5.2.3 CLEAR RELAY RECORDS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ CLEAR RELAY RECORDS

<div> <div>■ CLEAR RELAY RECORDS</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> </div>	<div> <div>◀▶</div> <div>CLEAR EVENT RECORDS: Off</div> </div>	Range: FlexLogic™ operand
	<div> <div>▲▼</div> <div>CLEAR OSCILLOGRAPHY? No</div> </div>	Range: FlexLogic™ operand
	<div> <div>▲▼</div> <div>CLEAR DATA LOGGER: Off</div> </div>	Range: FlexLogic™ operand
	<div> <div>▲▼</div> <div>CLEAR DEMAND: Off</div> </div>	Range: FlexLogic™ operand
	<div> <div>▲▼</div> <div>CLEAR ENERGY: Off</div> </div>	Range: FlexLogic™ operand
	<div> <div>▲▼</div> <div>RESET UNAUTH ACCESS: Off</div> </div>	Range: FlexLogic™ operand
<div> <div>▲</div> <div>CLEAR DIR I/O STATS: Off</div> </div>	<div> <div>Range: FlexLogic™ operand.</div> <div>Valid only for units with Direct I/O module.</div> </div>	

Selected records can be cleared from user-programmable conditions with FlexLogic™ operands. Assigning user-programmable pushbuttons to clear specific records are typical applications for these commands. Since the N60 responds to rising edges of the configured FlexLogic™ operands, they must be asserted for at least 50 ms to take effect.

Clearing records with user-programmable operands is not protected by the command password. However, user-programmable pushbuttons are protected by the command password. Thus, if they are used to clear records, the user-programmable pushbuttons can provide extra security if required.

For example, to assign User-Programmable Pushbutton 1 to clear demand records, the following settings should be applied.

- Assign the clear demand function to Pushbutton 1 by making the following change in the **SETTINGS ⇒ PRODUCT SETUP ⇒ CLEAR RELAY RECORDS** menu:
CLEAR DEMAND: "PUSHBUTTON 1 ON"
- Set the properties for User-Programmable Pushbutton 1 by making the following changes in the **SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE PUSHBUTTONS ⇒ USER PUSHBUTTON 1** menu:
PUSHBUTTON 1 FUNCTION: "Self-reset"
PUSHBTN 1 DROP-OUT TIME: "0.20 s"

5.2.4 COMMUNICATIONS

a) MAIN MENU

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS

■ COMMUNICATIONS	◀▶	■ SERIAL PORTS	See below.
MESSAGE	▲▼	■ NETWORK	See page 5-13.
MESSAGE	▲▼	■ MODBUS PROTOCOL	See page 5-13.
MESSAGE	▲▼	■ DNP PROTOCOL	See page 5-14.
MESSAGE	▲▼	■ DNP / IEC104 ■ POINT LISTS	See page 5-17.
MESSAGE	▲▼	■ IEC 61850 PROTOCOL	See page 5-18.
MESSAGE	▲▼	■ WEB SERVER ■ HTTP PROTOCOL	See page 5-20.
MESSAGE	▲▼	■ TFTP PROTOCOL	See page 5-20.
MESSAGE	▲▼	■ IEC 60870-5-104 ■ PROTOCOL	See page 5-21.
MESSAGE	▲	■ SNTP PROTOCOL	See page 5-21.

b) SERIAL PORTS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ SERIAL PORTS

■ SERIAL PORTS	◀▶	RS485 COM1 BAUD RATE: 19200	Range: 300, 1200, 2400, 4800, 9600, 14400, 19200, 28800, 33600, 38400, 57600, 115200. Only active if CPU Type E is ordered.
MESSAGE	▲▼	RS485 COM1 PARITY: None	Range: None, Odd, Even Only active if CPU Type E is ordered
MESSAGE	▲▼	RS485 COM1 RESPONSE MIN TIME: 0 ms	Range: 0 to 1000 ms in steps of 10 Only active if CPU Type E is ordered
MESSAGE	▲▼	RS485 COM2 BAUD RATE: 19200	Range: 300, 1200, 2400, 4800, 9600, 14400, 19200, 28800, 33600, 38400, 57600, 115200
MESSAGE	▲▼	RS485 COM2 PARITY: None	Range: None, Odd, Even
MESSAGE	▲	RS485 COM2 RESPONSE MIN TIME: 0 ms	Range: 0 to 1000 ms in steps of 10

The N60 is equipped with up to 3 independent serial communication ports. The faceplate RS232 port is intended for local use and is fixed at 19200 baud and no parity. The rear COM1 port type is selected when ordering: either an Ethernet or RS485 port. The rear COM2 port is RS485. The RS485 ports have settings for baud rate and parity. It is important that these parameters agree with the settings used on the computer or other equipment that is connected to these ports. Any of these ports may be connected to a computer running EnerVista UR Setup. This software can download and upload setting files, view measured parameters, and upgrade the relay firmware. A maximum of 32 relays can be daisy-chained and connected to a DCS, PLC or PC using the RS485 ports.



For each RS485 port, the minimum time before the port will transmit after receiving data from a host can be set. This feature allows operation with hosts which hold the RS485 transmitter active for some time after each transmission.

c) NETWORK

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ NETWORK

<div> <div>■ NETWORK</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> </div>	<div> <div>◀▶</div> <div>IP ADDRESS: 0.0.0.0</div> </div>	Range: Standard IP address format Not shown if CPU Type E is ordered.
	<div> <div>▲▼</div> <div>SUBNET IP MASK: 0.0.0.0</div> </div>	Range: Standard IP address format Not shown if CPU Type E is ordered.
	<div> <div>▲▼</div> <div>GATEWAY IP ADDRESS: 0.0.0.0</div> </div>	Range: Standard IP address format Not shown if CPU Type E is ordered.
	<div> <div>▲▼</div> <div> <div>■ OSI NETWORK</div> <div>■ ADDRESS (NSAP)</div> </div> </div>	Range: Select to enter the OSI NETWORK ADDRESS. Not shown if CPU Type E is ordered.
	<div> <div>▲</div> <div>ETHERNET OPERATION MODE: Full-Duplex</div> </div>	Range: Half-Duplex, Full-Duplex Not shown if CPU Type E or N is ordered.

These messages appear only if the N60 is ordered with an Ethernet card.

The IP addresses are used with the DNP, Modbus/TCP, IEC 61580, IEC 60870-5-104, TFTP, and HTTP protocols. The NSAP address is used with the IEC 61850 protocol over the OSI (CLNP/TP4) stack only. Each network protocol has a setting for the **TCP/UDP PORT NUMBER**. These settings are used only in advanced network configurations and should normally be left at their default values, but may be changed if required (for example, to allow access to multiple UR-series relays behind a router). By setting a different **TCP/UDP PORT NUMBER** for a given protocol on each UR-series relay, the router can map the relays to the same external IP address. The client software (EnerVista UR Setup, for example) must be configured to use the correct port number if these settings are used.



When the NSAP address, any TCP/UDP Port Number, or any User Map setting (when used with DNP) is changed, it will not become active until power to the relay has been cycled (OFF/ON).



Do not set more than one protocol to use the same TCP/UDP PORT NUMBER, as this will result in unreliable operation of those protocols.

d) MODBUS PROTOCOL

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ MODBUS PROTOCOL

<div> <div>■ MODBUS PROTOCOL</div> <div>MESSAGE</div> </div>	<div> <div>◀▶</div> <div>MODBUS SLAVE ADDRESS: 254</div> </div>	Range: 1 to 254 in steps of 1
	<div> <div>▲</div> <div>MODBUS TCP PORT NUMBER: 502</div> </div>	Range: 1 to 65535 in steps of 1

The serial communication ports utilize the Modbus protocol, unless configured for DNP or IEC 60870-5-104 operation (see descriptions below). This allows the EnerVista UR Setup software to be used. The UR operates as a Modbus slave device only. When using Modbus protocol on the RS232 port, the N60 will respond regardless of the **MODBUS SLAVE ADDRESS** programmed. For the RS485 ports each N60 must have a unique address from 1 to 254. Address 0 is the broadcast address which all Modbus slave devices listen to. Addresses do not have to be sequential, but no two devices can have the same address or conflicts resulting in errors will occur. Generally, each device added to the link should use the next higher address starting at 1. Refer to Appendix B for more information on the Modbus protocol.



Changes to the MODBUS TCP PORT NUMBER setting will not take effect until the N60 is restarted.

e) DNP PROTOCOL

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ DNP PROTOCOL

■ DNP PROTOCOL		■ DNP CHANNELS	Range: see sub-menu below
		■	
MESSAGE		DNP ADDRESS:	Range: 0 to 65519 in steps of 1
		65519	
MESSAGE		■ DNP NETWORK	Range: see sub-menu below
		■ CLIENT ADDRESSES	
MESSAGE		DNP TCP/UDP PORT	Range: 1 to 65535 in steps of 1
		NUMBER: 20000	
MESSAGE		DNP UNSOL RESPONSE	Range: Enabled, Disabled
		FUNCTION: Disabled	
MESSAGE		DNP UNSOL RESPONSE	Range: 0 to 60 s in steps of 1
		TIMEOUT: 5 s	
MESSAGE		DNP UNSOL RESPONSE	Range: 1 to 255 in steps of 1
		MAX RETRIES: 10	
MESSAGE		DNP UNSOL RESPONSE	Range: 0 to 65519 in steps of 1
		DEST ADDRESS: 1	
MESSAGE		DNP CURRENT SCALE	Range: 0.001, 0.01, 0.1, 1, 10, 100, 1000, 10000, 100000
		FACTOR: 1	
MESSAGE		DNP VOLTAGE SCALE	Range: 0.001, 0.01, 0.1, 1, 10, 100, 1000, 10000, 100000
		FACTOR: 1	
MESSAGE		DNP POWER SCALE	Range: 0.001, 0.01, 0.1, 1, 10, 100, 1000, 10000, 100000
		FACTOR: 1	
MESSAGE		DNP ENERGY SCALE	Range: 0.001, 0.01, 0.1, 1, 10, 100, 1000, 10000, 100000
		FACTOR: 1	
MESSAGE		DNP OTHER SCALE	Range: 0.001, 0.01, 0.1, 1, 10, 100, 1000, 10000, 100000
		FACTOR: 1	
MESSAGE		DNP CURRENT DEFAULT	Range: 0 to 65535 in steps of 1
		DEADBAND: 30000	
MESSAGE		DNP VOLTAGE DEFAULT	Range: 0 to 65535 in steps of 1
		DEADBAND: 30000	
MESSAGE		DNP POWER DEFAULT	Range: 0 to 65535 in steps of 1
		DEADBAND: 30000	
MESSAGE		DNP ENERGY DEFAULT	Range: 0 to 65535 in steps of 1
		DEADBAND: 30000	
MESSAGE		DNP OTHER DEFAULT	Range: 0 to 65535 in steps of 1
		DEADBAND: 30000	
MESSAGE		DNP TIME SYNC IIN	Range: 1 to 10080 min. in steps of 1
		PERIOD: 1440 min	
MESSAGE		DNP MESSAGE FRAGMENT	Range: 30 to 2048 in steps of 1
		SIZE: 240	
MESSAGE		DNP OBJECT 1	Range: 1, 2
		DEFAULT VARIATION: 2	

MESSAGE		DNP OBJECT 2 DEFAULT VARIATION: 2	Range: 1, 2
MESSAGE		DNP OBJECT 20 DEFAULT VARIATION: 1	Range: 1, 2, 5, 6
MESSAGE		DNP OBJECT 21 DEFAULT VARIATION: 1	Range: 1, 2, 9, 10
MESSAGE		DNP OBJECT 22 DEFAULT VARIATION: 1	Range: 1, 2, 5, 6
MESSAGE		DNP OBJECT 23 DEFAULT VARIATION: 2	Range: 1, 2, 5, 6
MESSAGE		DNP OBJECT 30 DEFAULT VARIATION: 1	Range: 1, 2, 3, 4, 5
MESSAGE		DNP OBJECT 32 DEFAULT VARIATION: 1	Range: 1, 2, 3, 4, 5, 7
MESSAGE		DNP NUMBER OF PAIRED CONTROL POINTS: 0	Range: 0 to 32 in steps of 1

The N60 supports the Distributed Network Protocol (DNP) version 3.0. The N60 can be used as a DNP slave device connected to multiple DNP masters (usually an RTU or a SCADA master station). Since the N60 maintains two sets of DNP data change buffers and connection information, two DNP masters can actively communicate with the N60 at one time.

The DNP Channels sub-menu is shown below.

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ DNP PROTOCOL ⇒ DNP CHANNELS

<input checked="" type="checkbox"/> DNP CHANNELS		DNP CHANNEL 1 PORT: NETWORK	Range: NONE, COM1 - RS485, COM2 - RS485, FRONT PANEL - RS232, NETWORK - TCP, NETWORK - UDP
MESSAGE		DNP CHANNEL 2 PORT: COM2 - RS485	Range: NONE, COM1 - RS485, COM2 - RS485, FRONT PANEL - RS232, NETWORK - TCP, NETWORK - UDP

The **DNP CHANNEL 1(2) PORT** settings select the communications port assigned to the DNP protocol for each channel. Once DNP is assigned to a serial port, the Modbus protocol is disabled on that port. Note that COM1 can be used only in non-Ethernet UR relays. When this setting is set to "Network - TCP", the DNP protocol can be used over TCP/IP on channels 1 or 2. When this value is set to "Network - UDP", the DNP protocol can be used over UDP/IP on channel 1 only. Refer to *Appendix E* for additional information on the DNP protocol.



Changes to the DNP CHANNEL 1(2) PORT settings will take effect only after power has been cycled to the relay.

The **DNP NETWORK CLIENT ADDRESS** settings can force the N60 to respond to a maximum of five specific DNP masters. The settings in this sub-menu are shown below.

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ DNP PROTOCOL ⇒ DNP NETWORK CLIENT ADDRESSES

<input checked="" type="checkbox"/> DNP NETWORK <input checked="" type="checkbox"/> CLIENT ADDRESSES			CLIENT ADDRESS 1: 0.0.0.0	Range: standard IP address
	MESSAGE	▲▼	CLIENT ADDRESS 2: 0.0.0.0	Range: standard IP address
	MESSAGE	▲▼	CLIENT ADDRESS 3: 0.0.0.0	Range: standard IP address
	MESSAGE	▲▼	CLIENT ADDRESS 4: 0.0.0.0	Range: standard IP address
	MESSAGE	▲▼	CLIENT ADDRESS 5: 0.0.0.0	Range: standard IP address

The **DNP UNSOL RESPONSE FUNCTION** should be “Disabled” for RS485 applications since there is no collision avoidance mechanism. The **DNP UNSOL RESPONSE TIMEOUT** sets the time the N60 waits for a DNP master to confirm an unsolicited response. The **DNP UNSOL RESPONSE MAX RETRIES** setting determines the number of times the N60 retransmits an unsolicited response without receiving confirmation from the master; a value of “255” allows infinite re-tries. The **DNP UNSOL RESPONSE DEST ADDRESS** is the DNP address to which all unsolicited responses are sent. The IP address to which unsolicited responses are sent is determined by the N60 from the current TCP connection or the most recent UDP message.

The **DNP SCALE FACTOR** settings are numbers used to scale Analog Input point values. These settings group the N60 Analog Input data into types: current, voltage, power, energy, and other. Each setting represents the scale factor for all Analog Input points of that type. For example, if the **DNP VOLTAGE SCALE FACTOR** setting is set to a value of 1000, all DNP Analog Input points that are voltages will be returned with values 1000 times smaller (e.g. a value of 72000 V on the N60 will be returned as 72). These settings are useful when analog input values must be adjusted to fit within certain ranges in DNP masters. Note that a scale factor of 0.1 is equivalent to a multiplier of 10 (i.e. the value will be 10 times larger).

The **DNP DEFAULT DEADBAND** settings determine when to trigger unsolicited responses containing Analog Input data. These settings group the N60 Analog Input data into types: current, voltage, power, energy, and other. Each setting represents the default deadband value for all Analog Input points of that type. For example, to trigger unsolicited responses from the N60 when any current values change by 15 A, the **DNP CURRENT DEFAULT DEADBAND** setting should be set to “15”. Note that these settings are the deadband default values. DNP Object 34 points can be used to change deadband values, from the default, for each individual DNP Analog Input point. Whenever power is removed and re-applied to the N60, the default deadbands will be in effect.

The **DNP TIME SYNC IIN PERIOD** setting determines how often the Need Time Internal Indication (IIN) bit is set by the N60. Changing this time allows the DNP master to send time synchronization commands more or less often, as required.

The **DNP MESSAGE FRAGMENT SIZE** setting determines the size, in bytes, at which message fragmentation occurs. Large fragment sizes allow for more efficient throughput; smaller fragment sizes cause more application layer confirmations to be necessary which can provide for more robust data transfer over noisy communication channels.



NOTE

When the DNP data points (analog inputs and/or binary inputs) are configured for Ethernet-enabled relays, check the “DNP Points Lists” N60 web page to view the points lists. This page can be viewed with a web browser by entering the N60 IP address to access the N60 “Main Menu”, then by selecting the “Device Information Menu” > “DNP Points Lists” menu item.

The **DNP OBJECT N DEFAULT VARIATION** settings allow the user to select the DNP default variation number for object types 1, 2, 20, 21, 22, 23, 30, and 32. The default variation refers to the variation response when variation 0 is requested and/or in class 0, 1, 2, or 3 scans. Refer to the *DNP Implementation* section in Appendix E for additional details.

The DNP binary outputs typically map one-to-one to IED data points. That is, each DNP binary output controls a single physical or virtual control point in an IED. In the N60 relay, DNP binary outputs are mapped to virtual inputs. However, some legacy DNP implementations use a mapping of one DNP binary output to two physical or virtual control points to support the concept of trip/close (for circuit breakers) or raise/lower (for tap changers) using a single control point. That is, the DNP master can operate a single point for both trip and close, or raise and lower, operations. The N60 can be configured to support paired control points, with each paired control point operating two virtual inputs. The **DNP NUMBER OF PAIRED CONTROL POINTS** setting allows configuration of from 0 to 32 binary output paired controls. Points not configured as paired operate on a one-to-one basis.

The **DNP ADDRESS** setting is the DNP slave address. This number identifies the N60 on a DNP communications link. Each DNP slave should be assigned a unique address.

f) DNP / IEC 60870-5-104 POINT LISTS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ DNP / IEC104 POINT LISTS

<div> <div>■ DNP / IEC104</div> <div>■ POINT LISTS</div> </div> <div>MESSAGE ▲</div>	<div>◀▶</div> <div> <div>■ BINARY INPUT / MSP</div> <div>■ POINTS</div> </div> <div>Range: see sub-menu below</div>
	<div> <div>■ ANALOG INPUT / MME</div> <div>■ POINTS</div> </div> <div>Range: see sub-menu below</div>

The binary and analog inputs points for the DNP protocol, or the MSP and MME points for IEC 60870-5-104 protocol, can be configured to a maximum of 256 points. The value for each point is user-programmable and can be configured by assigning FlexLogic™ operands for binary inputs / MSP points or FlexAnalog parameters for analog inputs / MME points.

The menu for the binary input points (DNP) or MSP points (IEC 60870-5-104) is shown below.

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ DNP / IEC104 POINT LISTS ⇒ BINARY INPUT / MSP POINTS

<div> <div>■ BINARY INPUT / MSP</div> <div>■ POINTS</div> </div> <div>MESSAGE ▲</div>	<div>◀▶</div> <div> <div>Point: 0</div> <div>Off</div> </div> <div>Range: FlexLogic™ operand</div>
	<div> <div>Point: 1</div> <div>Off</div> </div> <div>Range: FlexLogic™ operand</div>
	↓
MESSAGE ▲	<div> <div>Point: 255</div> <div>Off</div> </div> <div>Range: FlexLogic™ operand</div>

Up to 256 binary input points can be configured for the DNP or IEC 60870-5-104 protocols. The points are configured by assigning an appropriate FlexLogic™ operand. Refer to the *Introduction to FlexLogic™* section in this chapter for the full range of assignable operands.

The menu for the analog input points (DNP) or MME points (IEC 60870-5-104) is shown below.

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ DNP / IEC104 POINT LISTS ⇒ ANALOG INPUT / MME POINTS

<div> <div>■ ANALOG INPUT / MME</div> <div>■ POINTS</div> </div> <div>MESSAGE ▲</div>	<div>◀▶</div> <div> <div>Point: 0</div> <div>Off</div> </div> <div>Range: any FlexAnalog parameter</div>
	<div> <div>Point: 1</div> <div>Off</div> </div> <div>Range: any FlexAnalog parameter</div>
	↓
MESSAGE ▲	<div> <div>Point: 255</div> <div>Off</div> </div> <div>Range: any FlexAnalog parameter</div>

Up to 256 analog input points can be configured for the DNP or IEC 60870-5-104 protocols. The analog point list is configured by assigning an appropriate FlexAnalog parameter to each point. Refer to Appendix A: *FlexAnalog Parameters* for the full range of assignable parameters.



NOTE

The DNP / IEC 60870-5-104 point lists always begin with point 0 and end at the first “Off” value. Since DNP / IEC 60870-5-104 point lists must be in one continuous block, any points assigned after the first “Off” point are ignored.



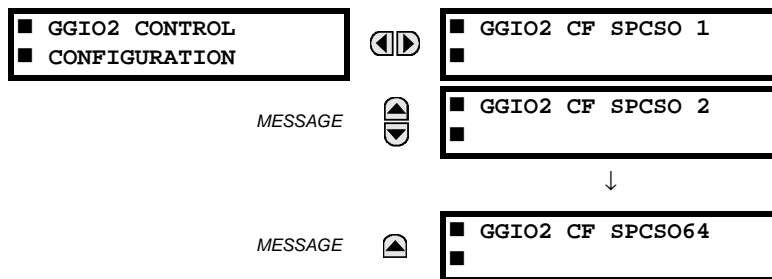
NOTE

Changes to the DNP / IEC 60870-5-104 point lists will not take effect until the N60 is restarted.

g) IEC 61850 PROTOCOL

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ IEC 61850 PROTOCOL ⇒ GSSE / GOOSE CONFIGURATION

<input checked="" type="checkbox"/> GSSE / GOOSE <input checked="" type="checkbox"/> CONFIGURATION		<div> <div>◀▶</div> <div>REMOTE I/O TRANSFER METHOD: GSSE</div> </div>	Range: None, GSSE, GOOSE
	MESSAGE	<div> <div>▲▼</div> <div>DEFAULT GSSE/GOOSE: UPDATE TIME: 60 s</div> </div>	Range: 1 to 60 s in steps of 1
	MESSAGE	<div> <div>▲▼</div> <div>GOOSE TRANSMIT VLAN PRIORITY: 4</div> </div>	Range: 0 to 7 in steps of 1
	MESSAGE	<div> <div>▲▼</div> <div>GOOSE TRANSMIT VLAN ID: 0</div> </div>	Range: 0 to 4095 in steps of 1
	MESSAGE	<div> <div>▲</div> <div>GOOSE TRANSMIT ETYPE APPID: 0</div> </div>	Range: 0 to 16383 in steps of 1
<input checked="" type="checkbox"/> SERVER <input checked="" type="checkbox"/> CONFIGURATION		<div> <div>◀▶</div> <div>LD NAME: IECDevice</div> </div>	Range: up to 32 alphanumeric characters
	MESSAGE	<div> <div>▲▼</div> <div>IEC/MMS TCP PORT NUMBER: 102</div> </div>	Range: 1 to 65535 in steps of 1
	MESSAGE	<div> <div>▲▼</div> <div>INCLUDE NON-IEC DATA: Enabled</div> </div>	Range: Disabled, Enabled
	MESSAGE	<div> <div>▲▼</div> <div>NUMBER OF STATUS POINTS IN GGIO1: 8</div> </div>	Range: 8 to 128 in steps of 8
	MESSAGE	<div> <div>▲▼</div> <div>SERVER SCANNING: Disabled</div> </div>	Range: Disabled, Enabled
	MESSAGE	<div> <div>▲▼</div> <div>CLEAR XCBR1 OpCnt: No</div> </div>	Range: No, Yes
	MESSAGE	<div> <div>▲</div> <div>CLEAR XCBR2 OpCnt: No</div> </div>	Range: No, Yes
<input checked="" type="checkbox"/> IEC 61850 LOGICAL <input checked="" type="checkbox"/> NODE NAME PREFIXES		<div> <div>◀▶</div> <div> <input checked="" type="checkbox"/> PIOC LOGICAL NODE <input checked="" type="checkbox"/> NAME PREFIXES </div> </div>	
	MESSAGE	<div> <div>▲▼</div> <div> <input checked="" type="checkbox"/> PTOC LOGICAL NODE <input checked="" type="checkbox"/> NAME PREFIXES </div> </div>	
		↓	
	MESSAGE	<div> <div>▲</div> <div> <input checked="" type="checkbox"/> PTRC LOGICAL NODE <input checked="" type="checkbox"/> NAME PREFIXES </div> </div>	
<input checked="" type="checkbox"/> MMXU DEADBANDS <input checked="" type="checkbox"/>		<div> <div>◀▶</div> <div> <input checked="" type="checkbox"/> MMXU1 DEADBANDS <input checked="" type="checkbox"/> </div> </div>	
	MESSAGE	<div> <div>▲▼</div> <div> <input checked="" type="checkbox"/> MMXU2 DEADBANDS <input checked="" type="checkbox"/> </div> </div>	
	MESSAGE	<div> <div>▲▼</div> <div> <input checked="" type="checkbox"/> MMXU3 DEADBANDS <input checked="" type="checkbox"/> </div> </div>	
	MESSAGE	<div> <div>▲</div> <div> <input checked="" type="checkbox"/> MMXU4 DEADBANDS <input checked="" type="checkbox"/> </div> </div>	



The N60 Network Stability and Synchrophasor Measurement System is provided with optional IEC 61850 communications capability. This feature is specified as a software option at the time of ordering. Refer to the *Ordering* section of chapter 2 for additional details. The IEC 61850 protocol feature are not available if CPU Type E is ordered.

The N60 supports the Manufacturing Message Specification (MMS) protocol as specified by IEC 61850. MMS is supported over two protocol stacks: TCP/IP over ethernet and TP4/CLNP (OSI) over ethernet. The N60 operates as an IEC 61850 server. The *Remote Inputs/Outputs* section in this chapter describe the peer-to-peer GSSE/GOOSE message scheme.

The **REMOTE I/O TRANSFER METHOD** selects the method used to transfer remote input/output data. This can be either IEC 61850 GSSE, IEC 61850 GOOSE, or none (remote inputs/outputs disabled). GOOSE messages are more efficient and can make use of Ethernet priority tagging and virtual LAN functionality. All relays exchanging remote input/output data must be set to the same transfer method.

The **DEFAULT GSSE/GOOSE UPDATE TIME** sets the time between GSSE or GOOSE messages when there are no remote output state changes to be sent. When remote output data changes, GSSE or GOOSE messages are sent immediately. This setting controls the steady-state 'heartbeat' time interval.

The **GOOSE TRANSMIT VLAN PRIORITY** setting indicates the Ethernet priority of GOOSE messages. This allows GOOSE messages to have higher priority than other Ethernet data. The **GOOSE TRANSMIT ETYPE APPID** setting allows the selection of a specific application ID for each GOOSE sending device. This value can be left at its default if the feature is not required. Both the **GOOSE TRANSMIT VLAN PRIORITY** and **GOOSE TRANSMIT ETYPE APPID** settings are required by IEC 61850.

The **LD NAME** setting represents the MMS domain name (IEC 61850 logical device) where all IEC/MMS logical nodes are located. The **IEC/MMS TCP PORT NUMBER** setting allows the user to change the TCP port number for MMS connections. The **INCLUDE NON-IEC DATA** setting determines whether or not the "UR" MMS domain will be available. This domain contains a large number of UR-series specific data items that are not available in the IEC 61850 logical nodes. This data does not follow the IEC 61850 naming conventions. For communications schemes that strictly follow the IEC 61850 standard, this setting should be "Disabled".

The **NUMBER OF STATUS POINTS IN GGIO1** setting determines the number of "Ind" (single point status indications) that are instantiated in the GGIO1 logical node. The indication points in GGIO1 are mapped to FlexStates in the N60. These FlexStates allow user-customized access to the FlexLogic™ operand states in the relay.

The **SERVER SCANNING** feature should be set to "Disabled" when IEC 61850 client/server functionality is not required. IEC 61850 has two modes of functionality: GOOSE/GSSE inter-device communication and client/server communication. If the GOOSE/GSSE functionality is required without the IEC 61850 client server feature, then server scanning can be disabled to increase CPU resources. When server scanning is disabled, there will be not updated to the IEC 61850 logical node status values in the N60. Clients will still be able to connect to the server (N60 relay), but most data values will not be updated. This setting does not affect GOOSE/GSSE operation.



Changes to the **LD NAME**, **NUMBER OF STATUS POINTS IN GGIO1**, and **SERVER SCANNING** settings will not take effect until the N60 is restarted.

The **CLEAR XCBR1(2) OpCnt** settings represent the breaker operating counters. As breakers operate by opening and closing, the XCBR operating counter status attribute (OpCnt) increments with every operation. Frequent breaker operation may result in very large OpCnt values over time. This setting allows the OpCnt to be reset to "0" for XCBR1 and XCBR2.

The IEC 61850 logical node name prefix settings are used to create name prefixes to uniquely identify each logical node. For example, the logical node “PTOC1” may have the name prefix “abc”. The full logical node name will then be “abcMMXU1”. Valid characters for the logical node name prefixes are upper and lowercase letters, numbers, and the underscore () character, and the first character in the prefix must be a letter. This conforms to the IEC 61850 standard.

The MMXU deadband settings represent the deadband values used to determine when the update the MMXU “mag” and “cVal” values from the associated “instmag” and “instcVal” values. The “mag” and “cVal” values are used for the IEC 61850 buffered and unbuffered reports. These settings correspond to the associated “db” data items in the CF functional constraint of the MMXU logical node, as per the IEC 61850 standard. According to IEC 61850-7-3, the db value “shall represent the percentage of difference between the maximum and minimum in units of 0.00%”. Thus, it is important to know the maximum value for each MMXU measured quantity, since this represents the 100.00% value for the deadband.

The minimum value for all quantities is 0; the maximum values are as follows:

phase current: $46 \times$ phase CT primary setting
 neutral current: $46 \times$ ground CT primary setting
 voltage: $275 \times$ VT ratio setting
 power (real, reactive, and apparent): $46 \times$ phase CT primary setting \times $275 \times$ VT ratio setting
 frequency: 90 Hz
 power factor: 2

The GGIO2 control configuration settings are used to set the control model for each input. The available choices are “0” (status only), “1” (direct control), and “2” (SBO with normal security). The GGIO2 control points are used to control the N60 virtual inputs.



Since GSSE/GOOSE messages are multicast ethernet by specification, they will not usually be forwarded by network routers. However, GOOSE messages may be forwarded by routers if the router has been configured for VLAN functionality.

5

h) WEB SERVER HTTP PROTOCOL

PATH: SETTINGS \Rightarrow PRODUCT SETUP \Rightarrow COMMUNICATIONS \Rightarrow WEB SERVER HTTP PROTOCOL

<input checked="" type="checkbox"/> WEB SERVER <input checked="" type="checkbox"/> HTTP PROTOCOL		HTTP TCP PORT NUMBER: 80	Range: 1 to 65535 in steps of 1
---	--	---	---------------------------------

The N60 contains an embedded web server and is capable of transferring web pages to a web browser such as Microsoft Internet Explorer or Netscape Navigator. This feature is available only if the N60 has the ethernet option installed. The web pages are organized as a series of menus that can be accessed starting at the N60 “Main Menu”. Web pages are available showing DNP and IEC 60870-5-104 points lists, Modbus registers, Event Records, Fault Reports, etc. The web pages can be accessed by connecting the UR and a computer to an ethernet network. The Main Menu will be displayed in the web browser on the computer simply by entering the IP address of the N60 into the “Address” box on the web browser.

i) TFTP PROTOCOL

PATH: SETTINGS \Rightarrow PRODUCT SETUP \Rightarrow COMMUNICATIONS \Rightarrow TFTP PROTOCOL

<input checked="" type="checkbox"/> TFTP PROTOCOL		TFTP MAIN UDP PORT NUMBER: 69	Range: 1 to 65535 in steps of 1
MESSAGE		TFTP DATA UDP PORT 1 NUMBER: 0	Range: 0 to 65535 in steps of 1
MESSAGE		TFTP DATA UDP PORT 2 NUMBER: 0	Range: 0 to 65535 in steps of 1

The Trivial File Transfer Protocol (TFTP) can be used to transfer files from the N60 over a network. The N60 operates as a TFTP server. TFTP client software is available from various sources, including Microsoft Windows NT. The `dir.txt` file obtained from the N60 contains a list and description of all available files (event records, oscillography, etc.).

j) IEC 60870-5-104 PROTOCOL

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ IEC 60870-5-104 PROTOCOL

<input checked="" type="checkbox"/> IEC 60870-5-104 <input checked="" type="checkbox"/> PROTOCOL		IEC 60870-5-104 FUNCTION: Disabled	Range: Enabled, Disabled
MESSAGE		IEC TCP PORT NUMBER: 2404	Range: 1 to 65535 in steps of 1
MESSAGE		<input checked="" type="checkbox"/> IEC NETWORK <input checked="" type="checkbox"/> CLIENT ADDRESSES	
MESSAGE		IEC COMMON ADDRESS OF ASDU: 0	Range: 0 to 65535 in steps of 1
MESSAGE		IEC CYCLIC DATA PERIOD: 60 s	Range: 1 to 65535 s in steps of 1
MESSAGE		IEC CURRENT DEFAULT THRESHOLD: 30000	Range: 0 to 65535 in steps of 1
MESSAGE		IEC VOLTAGE DEFAULT THRESHOLD: 30000	Range: 0 to 65535 in steps of 1
MESSAGE		IEC POWER DEFAULT THRESHOLD: 30000	Range: 0 to 65535 in steps of 1
MESSAGE		IEC ENERGY DEFAULT THRESHOLD: 30000	Range: 0 to 65535 in steps of 1
MESSAGE		IEC OTHER DEFAULT THRESHOLD: 30000	Range: 0 to 65535 in steps of 1

The N60 supports the IEC 60870-5-104 protocol. The N60 can be used as an IEC 60870-5-104 slave device connected to a maximum of two masters (usually either an RTU or a SCADA master station). Since the N60 maintains two sets of IEC 60870-5-104 data change buffers, no more than two masters should actively communicate with the N60 at one time.

The IEC ----- DEFAULT THRESHOLD settings are used to determine when to trigger spontaneous responses containing M_ME_NC_1 analog data. These settings group the N60 analog data into types: current, voltage, power, energy, and other. Each setting represents the default threshold value for all M_ME_NC_1 analog points of that type. For example, to trigger spontaneous responses from the N60 when any current values change by 15 A, the IEC CURRENT DEFAULT THRESHOLD setting should be set to 15. Note that these settings are the default values of the deadbands. P_ME_NC_1 (parameter of measured value, short floating point value) points can be used to change threshold values, from the default, for each individual M_ME_NC_1 analog point. Whenever power is removed and re-applied to the N60, the default thresholds will be in effect.



The IEC 60870-5-104 and DNP protocols can not be used at the same time. When the IEC 60870-5-104 FUNCTION setting is set to “Enabled”, the DNP protocol will not be operational. When this setting is changed it will not become active until power to the relay has been cycled (Off/On).

k) SNTP PROTOCOL

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ SNTP PROTOCOL

<input checked="" type="checkbox"/> SNTP PROTOCOL <input checked="" type="checkbox"/>		SNTP FUNCTION: Disabled	Range: Enabled, Disabled
MESSAGE		SNTP SERVER IP ADDR: 0.0.0.0	Range: Standard IP address format
MESSAGE		SNTP UDP PORT NUMBER: 123	Range: 0 to 65535 in steps of 1

The N60 supports the Simple Network Time Protocol specified in RFC-2030. With SNTP, the N60 can obtain clock time over an Ethernet network. The N60 acts as an SNTP client to receive time values from an SNTP/NTP server, usually a dedicated product using a GPS receiver to provide an accurate time. Both unicast and broadcast SNTP are supported.

If SNTP functionality is enabled at the same time as IRIG-B, the IRIG-B signal provides the time value to the N60 clock for as long as a valid signal is present. If the IRIG-B signal is removed, the time obtained from the SNTP server is used. If either SNTP or IRIG-B is enabled, the N60 clock value cannot be changed using the front panel keypad.

To use SNTP in unicast mode, **SNTP SERVER IP ADDR** must be set to the SNTP/NTP server IP address. Once this address is set and **SNTP FUNCTION** is “Enabled”, the N60 attempts to obtain time values from the SNTP/NTP server. Since many time values are obtained and averaged, it generally takes three to four minutes until the N60 clock is closely synchronized with the SNTP/NTP server. It may take up to two minutes for the N60 to signal an SNTP self-test error if the server is offline.

To use SNTP in broadcast mode, set the **SNTP SERVER IP ADDR** setting to “0.0.0.0” and **SNTP FUNCTION** to “Enabled”. The N60 then listens to SNTP messages sent to the “all ones” broadcast address for the subnet. The N60 waits up to eighteen minutes (>1024 seconds) without receiving an SNTP broadcast message before signaling an SNTP self-test error.

The UR-series relays do not support the multicast or anycast SNTP functionality.

5.2.5 MODBUS USER MAP

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ MODBUS USER MAP

<div> <div>MODBUS USER MAP</div> <div>MESSAGE</div> </div>	<div> <div>◀▶</div> <div>▲</div> </div>	<div>ADDRESS 1: 0</div> <div>VALUE: 0</div>	Range: 0 to 65535 in steps of 1
		<div>ADDRESS 256: 0</div> <div>VALUE: 0</div>	Range: 0 to 65535 in steps of 1

The Modbus User Map provides read-only access for up to 256 registers. To obtain a memory map value, enter the desired address in the **ADDRESS** line (this value must be converted from hex to decimal format). The corresponding value is displayed in the **VALUE** line. A value of “0” in subsequent register **ADDRESS** lines automatically returns values for the previous **ADDRESS** lines incremented by “1”. An address value of “0” in the initial register means “none” and values of “0” will be displayed for all registers. Different **ADDRESS** values can be entered as required in any of the register positions.

5.2.6 REAL TIME CLOCK

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ REAL TIME CLOCK

<div> <div>REAL TIME</div> <div>CLOCK</div> </div>	<div> <div>◀▶</div> <div>▲</div> </div>	<div>IRIG-B SIGNAL TYPE:</div> <div>None</div>	Range: None, DC Shift, Amplitude Modulated
		<div>REAL TIME CLOCK</div> <div>EVENTS: Disabled</div>	Range: Disabled, Enabled

The date and time can be synchronized a known time base and to other relays using an IRIG-B signal. It has the same accuracy as an electronic watch, approximately ± 1 minute per month. If an IRIG-B signal is connected to the relay, only the current year needs to be entered. See the **COMMANDS ⇒ SET DATE AND TIME** menu to manually set the relay clock.

The **REAL TIME CLOCK EVENTS** setting allows changes to the date and/or time to be captured in the event record.

5.2.7 OSCILLOGRAPHY

a) MAIN MENU

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ OSCILLOGRAPHY

■ OSCILLOGRAPHY	◀▶	NUMBER OF RECORDS: 15	Range: 1 to 64 in steps of 1
MESSAGE	▲▼	TRIGGER MODE: Automatic Overwrite	Range: Automatic Overwrite, Protected
MESSAGE	▲▼	TRIGGER POSITION: 50%	Range: 0 to 100% in steps of 1
MESSAGE	▲▼	TRIGGER SOURCE: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	AC INPUT WAVEFORMS: 16 samples/cycle	Range: Off; 8, 16, 32, 64 samples/cycle
MESSAGE	▲▼	■ DIGITAL CHANNELS ■	
MESSAGE	▲	■ ANALOG CHANNELS ■	

Oscillography records contain waveforms captured at the sampling rate as well as other relay data at the point of trigger. Oscillography records are triggered by a programmable FlexLogic™ operand. Multiple oscillography records may be captured simultaneously.

The **NUMBER OF RECORDS** is selectable, but the number of cycles captured in a single record varies considerably based on other factors such as sample rate and the number of operational CT/VT modules. There is a fixed amount of data storage for oscillography; the more data captured, the less the number of cycles captured per record. See the **ACTUAL VALUES** ⇒ **RECORDS** ⇒ **OSCILLOGRAPHY** menu to view the number of cycles captured per record. The following table provides sample configurations with corresponding cycles/record.

Table 5–1: OSCILLOGRAPHY CYCLES/RECORD EXAMPLE

# RECORDS	# CT/VTs	SAMPLE RATE	# DIGITALS	# ANALOGS	CYCLES/RECORD
1	1	8	0	0	1872.0
1	1	16	16	0	1685.0
8	1	16	16	0	276.0
8	1	16	16	4	219.5
8	2	16	16	4	93.5
8	2	16	64	16	93.5
8	2	32	64	16	57.6
8	2	64	64	16	32.3
32	2	64	64	16	9.5

A new record may automatically overwrite an older record if **TRIGGER MODE** is set to “Automatic Overwrite”.

Set the **TRIGGER POSITION** to a percentage of the total buffer size (e.g. 10%, 50%, 75%, etc.). A trigger position of 25% consists of 25% pre- and 75% post-trigger data. The **TRIGGER SOURCE** is always captured in oscillography and may be any FlexLogic™ parameter (element state, contact input, virtual output, etc.). The relay sampling rate is 64 samples per cycle.

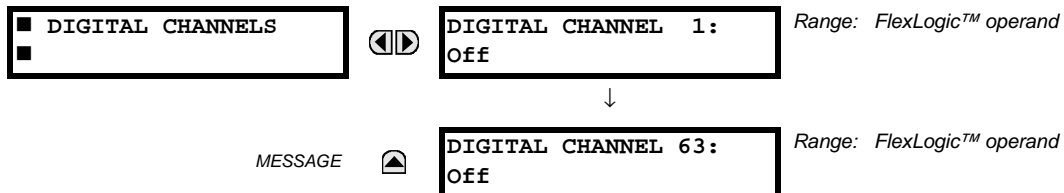
The **AC INPUT WAVEFORMS** setting determines the sampling rate at which AC input signals (i.e. current and voltage) are stored. Reducing the sampling rate allows longer records to be stored. This setting has no effect on the internal sampling rate of the relay which is always 64 samples per cycle, i.e. it has no effect on the fundamental calculations of the device.



When changes are made to the oscillography settings, all existing oscillography records will be CLEARED.

b) DIGITAL CHANNELS

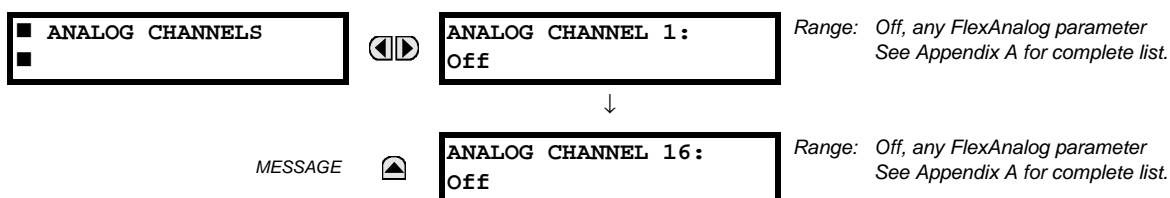
PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ OSCILLOGRAPHY ⇒ DIGITAL CHANNELS



A **DIGITAL CHANNEL** setting selects the FlexLogic™ operand state recorded in an oscillography trace. The length of each oscillography trace depends in part on the number of parameters selected here. Parameters set to “Off” are ignored. Upon startup, the relay will automatically prepare the parameter list.

c) ANALOG CHANNELS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ OSCILLOGRAPHY ⇒ ANALOG CHANNELS



An **ANALOG CHANNEL** setting selects the metering actual value recorded in an oscillography trace. The length of each oscillography trace depends in part on the number of parameters selected here. Parameters set to “Off” are ignored. The parameters available in a given relay are dependent on: (a) the type of relay, (b) the type and number of CT/VT hardware modules installed, and (c) the type and number of Analog Input hardware modules installed. Upon startup, the relay will automatically prepare the parameter list. A list of all possible analog metering actual value parameters is presented in Appendix A: *FlexAnalog Parameters*. The parameter index number shown in any of the tables is used to expedite the selection of the parameter on the relay display. It can be quite time-consuming to scan through the list of parameters via the relay keypad/display - entering this number via the relay keypad will cause the corresponding parameter to be displayed.

All eight CT/VT module channels are stored in the oscillography file. The CT/VT module channels are named as follows:

<slot_letter><terminal_number>—<I or V><phase A, B, or C, or 4th input>

The fourth current input in a bank is called IG, and the fourth voltage input in a bank is called VX. For example, F2-IB designates the IB signal on Terminal 2 of the CT/VT module in slot F. If there are no CT/VT modules and Analog Input modules, no analog traces will appear in the file; only the digital traces will appear.

5.2.8 DATA LOGGER

PATH: SETTINGS ⇒ ↓ PRODUCT SETUP ⇒ ↓ DATA LOGGER

■ DATA LOGGER		DATA LOGGER MODE: Continuous	Range: Continuous, Trigger
MESSAGE	▲▼	DATA LOGGER TRIGGER: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	DATA LOGGER RATE: 60000 ms	Range: 15 to 3600000 ms in steps of 1
MESSAGE	▲▼	DATA LOGGER CHNL 1: Off	Range: Off, any FlexAnalog parameter. See Appendix A: FlexAnalog Parameters for complete list.
MESSAGE	▲▼	DATA LOGGER CHNL 2: Off	Range: Off, any FlexAnalog parameter. See Appendix A: FlexAnalog Parameters for complete list.
↓			
MESSAGE	▲▼	DATA LOGGER CHNL 16: Off	Range: Off, any FlexAnalog parameter. See Appendix A: FlexAnalog Parameters for complete list.
MESSAGE	▲	DATA LOGGER CONFIG: 0 CHNL x 0.0 DAYS	Range: Not applicable - shows computed data only

The data logger samples and records up to 16 analog parameters at a user-defined sampling rate. This recorded data may be downloaded to EnerVista UR Setup and displayed with *parameters* on the vertical axis and *time* on the horizontal axis. All data is stored in non-volatile memory, meaning that the information is retained when power to the relay is lost.

For a fixed sampling rate, the data logger can be configured with a few channels over a long period or a larger number of channels for a shorter period. The relay automatically partitions the available memory between the channels in use. Example storage capacities for a system frequency of 60 Hz are shown in the following table.

Table 5–2: DATA LOGGER STORAGE CAPACITY EXAMPLE

SAMPLING RATE	CHANNELS	DAYS	STORAGE CAPACITY
15 ms	1	0.1	954 s
	8	0.1	120 s
	9	0.1	107 s
	16	0.1	60 s
1000 ms	1	0.7	65457 s
	8	0.1	8182 s
	9	0.1	7273 s
	16	0.1	4091 s
60000 ms	1	45.4	3927420 s
	8	5.6	490920 s
	9	5	436380 s
	16	2.8	254460 s
3600000 ms	1	2727.5	235645200 s
	8	340.9	29455200 s
	9	303	26182800 s



Changing any setting affecting Data Logger operation will clear any data that is currently in the log.

NOTE

- **DATA LOGGER MODE:** This setting configures the mode in which the data logger will operate. When set to “Continuous”, the data logger will actively record any configured channels at the rate as defined by the **DATA LOGGER RATE**. The

data logger will be idle in this mode if no channels are configured. When set to "Trigger", the data logger will begin to record any configured channels at the instance of the rising edge of the **DATA LOGGER TRIGGER** source FlexLogic™ operand. The **Data Logger will ignore** all subsequent triggers and will continue to record data until the active record is full. Once the data logger is full a **CLEAR DATA LOGGER** command is required to clear the data logger record before a new record can be started. Performing the **CLEAR DATA LOGGER** command will also stop the current record and reset the data logger to be ready for the next trigger.

- **DATA LOGGER TRIGGER:** This setting selects the signal used to trigger the start of a new data logger record. Any FlexLogic™ operand can be used as the trigger source. The **DATA LOGGER TRIGGER** setting only applies when the mode is set to "Trigger".
- **DATA LOGGER RATE:** This setting selects the time interval at which the actual value data will be recorded.
- **DATA LOGGER CHNL 1(16):** This setting selects the metering actual value that is to be recorded in Channel 1(16) of the data log. The parameters available in a given relay are dependent on: the type of relay, the type and number of CT/VT hardware modules installed, and the type and number of Analog Input hardware modules installed. Upon startup, the relay will automatically prepare the parameter list. A list of all possible analog metering actual value parameters is shown in Appendix A: *FlexAnalog Parameters*. The parameter index number shown in any of the tables is used to expedite the selection of the parameter on the relay display. It can be quite time-consuming to scan through the list of parameters via the relay keypad/display – entering this number via the relay keypad will cause the corresponding parameter to be displayed.
- **DATA LOGGER CONFIG:** This display presents the total amount of time the Data Logger can record the channels not selected to "Off" without over-writing old data.

5.2.9 DEMAND

5

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ DEMAND

■ DEMAND	◀ ▶	CRNT DEMAND METHOD: Thermal Exponential	Range: Thermal Exponential, Block Interval, Rolling Demand
MESSAGE	▲ ▼	POWER DEMAND METHOD: Thermal Exponential	Range: Thermal Exponential, Block Interval, Rolling Demand
MESSAGE	▲ ▼	DEMAND INTERVAL: 15 MIN	Range: 5, 10, 15, 20, 30, 60 minutes
MESSAGE	▲	DEMAND TRIGGER: Off	Range: FlexLogic™ operand Note: for calculation using Method 2a

The relay measures current demand on each phase, and three-phase demand for real, reactive, and apparent power. Current and Power methods can be chosen separately for the convenience of the user. Settings are provided to allow the user to emulate some common electrical utility demand measuring techniques, for statistical or control purposes. If the **CRNT DEMAND METHOD** is set to "Block Interval" and the **DEMAND TRIGGER** is set to "Off", Method 2 is used (see below). If **DEMAND TRIGGER** is assigned to any other FlexLogic™ operand, Method 2a is used (see below).

The relay can be set to calculate demand by any of three methods as described below:

CALCULATION METHOD 1: THERMAL EXPONENTIAL

This method emulates the action of an analog peak recording thermal demand meter. The relay measures the quantity (RMS current, real power, reactive power, or apparent power) on each phase every second, and assumes the circuit quantity remains at this value until updated by the next measurement. It calculates the 'thermal demand equivalent' based on the following equation:

$$d(t) = D(1 - e^{-kt}) \quad (\text{EQ 5.6})$$

where: d = demand value after applying input quantity for time t (in minutes)
 D = input quantity (constant), and $k = 2.3 / \text{thermal 90\% response time}$.

The 90% thermal response time characteristic of 15 minutes is illustrated below. A setpoint establishes the time to reach 90% of a steady-state value, just as the response time of an analog instrument. A steady state value applied for twice the response time will indicate 99% of the value.

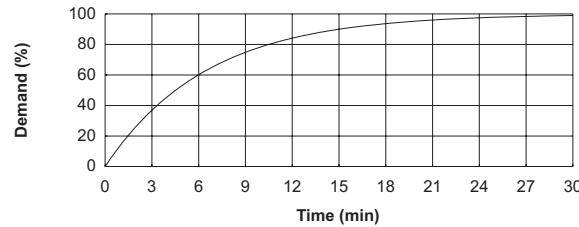


Figure 5-2: THERMAL DEMAND CHARACTERISTIC

CALCULATION METHOD 2: BLOCK INTERVAL

This method calculates a linear average of the quantity (RMS current, real power, reactive power, or apparent power) over the programmed demand time interval, starting daily at 00:00:00 (i.e. 12:00 am). The 1440 minutes per day is divided into the number of blocks as set by the programmed time interval. Each new value of demand becomes available at the end of each time interval.

CALCULATION METHOD 2a: BLOCK INTERVAL (with Start Demand Interval Logic Trigger)

This method calculates a linear average of the quantity (RMS current, real power, reactive power, or apparent power) over the interval between successive Start Demand Interval logic input pulses. Each new value of demand becomes available at the end of each pulse. Assign a FlexLogic™ operand to the **DEMAND TRIGGER** setting to program the input for the new demand interval pulses.



If no trigger is assigned in the **DEMAND TRIGGER** setting and the **CRNT DEMAND METHOD** is "Block Interval", use calculating method #2. If a trigger is assigned, the maximum allowed time between 2 trigger signals is 60 minutes. If no trigger signal appears within 60 minutes, demand calculations are performed and available and the algorithm resets and starts the new cycle of calculations. The minimum required time for trigger contact closure is 20 μs.

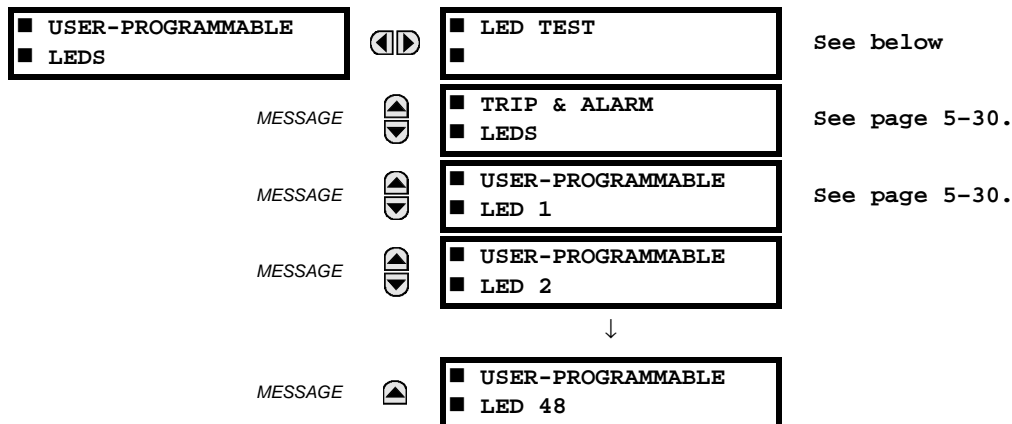
CALCULATION METHOD 3: ROLLING DEMAND

This method calculates a linear average of the quantity (RMS current, real power, reactive power, or apparent power) over the programmed demand time interval, in the same way as Block Interval. The value is updated every minute and indicates the demand over the time interval just preceding the time of update.

5.2.10 USER-PROGRAMMABLE LEDS

a) MAIN MENU

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE LEDS



The 48 amber LEDs on relay panels 2 and 3 can be customized to illuminate when a selected FlexLogic™ operand is in the Logic 1 state. The trip and alarm LEDs on panel 1 can also be customized in a similar manner. To ensure correct functionality of all LEDs, an LED Test feature is also provided.

b) LED TEST

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE LEDS ⇒ LED TEST

<div> <div>■ LED TEST</div> <div>■</div> </div>	<div> <div>◀▶</div> <div>▲</div> </div>	LED TEST FUNCTION:	Disabled	Range: Disabled, Enabled.
		LED TEST CONTROL:	Off	Range: FlexLogic™ operand

MESSAGE

When enabled, the LED Test can be initiated from any digital input or user-programmable condition such as user-programmable pushbutton. The control operand is configured under the **LED TEST CONTROL** setting. The test covers all LEDs, including the LEDs of the optional user-programmable pushbuttons.

The test consists of three stages.

Stage 1: All 62 LEDs on the relay are illuminated. This is a quick test to verify if any of the LEDs is “burned”. This stage lasts as long as the control input is on, up to a maximum of 1 minute. After 1 minute, the test will end.

Stage 2: All the LEDs are turned off, and then one LED at a time turns on for 1 second, then back off. The test routine starts at the top left panel, moving from the top to bottom of each LED column. This test checks for hardware failures that lead to more than one LED being turned on from a single logic point. This stage can be interrupted at any time.

Stage 3: All the LEDs are turned on. One LED at a time turns off for 1 second, then back on. The test routine starts at the top left panel moving from top to bottom of each column of the LEDs. This test checks for hardware failures that lead to more than one LED being turned off from a single logic point. This stage can be interrupted at any time.

When testing is in progress, the LEDs are controlled by the test sequence, rather than the protection, control, and monitoring features. However, the LED control mechanism accepts all the changes to LED states generated by the relay and stores the actual LED states (On or Off) in memory. When the test completes, the LEDs reflect the actual state resulting from relay response during testing. The Reset pushbutton will not clear any targets when the LED Test is in progress.

A dedicated FlexLogic™ operand, LED TEST IN PROGRESS, is set for the duration of the test. When the test sequence is initiated, the LED Test Initiated event is stored in the Event Recorder.

The entire test procedure is user-controlled. In particular, Stage 1 can last as long as necessary, and Stages 2 and 3 can be interrupted. The test responds to the position and rising edges of the control input defined by the **LED TEST CONTROL** setting. The control pulses must last at least 250 ms to take effect. The following diagram explains how the test is executed.

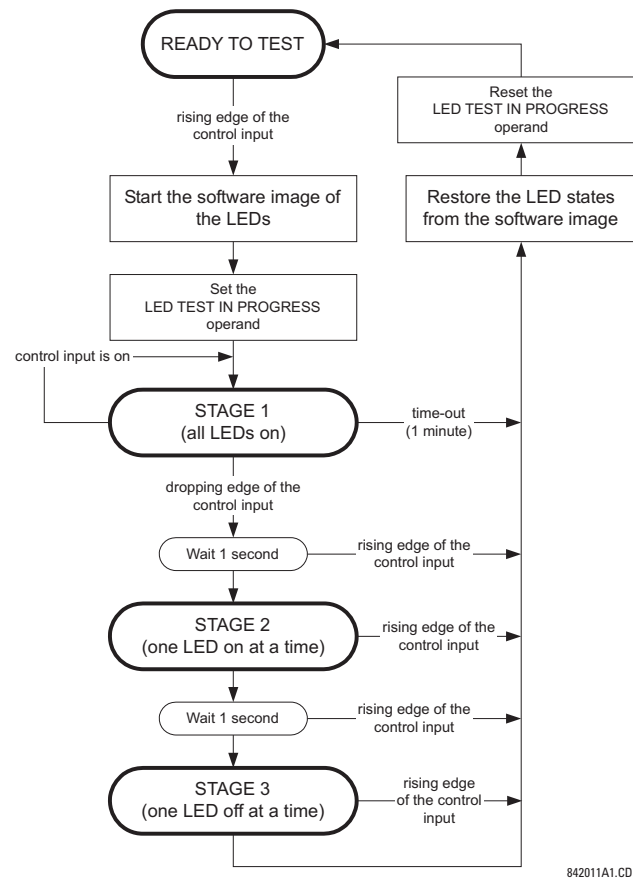


Figure 5–3: LED TEST SEQUENCE

APPLICATION EXAMPLE 1:

Assume one needs to check if any of the LEDs is “burned” through User-Programmable Pushbutton 1. The following settings should be applied. Configure User-Programmable Pushbutton 1 by making the following entries in the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **USER-PROGRAMMABLE PUSHBUTTONS** ⇒ **USER PUSHBUTTON 1** menu:

PUSHBUTTON 1 FUNCTION: “Self-reset”

PUSHBTN 1 DROP-OUT TIME: “0.10 s”

Configure the LED test to recognize User-Programmable Pushbutton 1 by making the following entries in the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **USER-PROGRAMMABLE LEDS** ⇒ **LED TEST** menu:

LED TEST FUNCTION: “Enabled”

LED TEST CONTROL: “PUSHBUTTON 1 ON”

The test will be initiated when the User-Programmable Pushbutton 1 is pressed. The pushbutton should remain pressed for as long as the LEDs are being visually inspected. When finished, the pushbutton should be released. The relay will then automatically start Stage 2. At this point forward, test may be aborted by pressing the pushbutton.

APPLICATION EXAMPLE 2:

Assume one needs to check if any LEDs are “burned” as well as exercise one LED at a time to check for other failures. This is to be performed via User-Programmable Pushbutton 1.

After applying the settings in Application Example 1, hold down the pushbutton as long as necessary to test all LEDs. Next, release the pushbutton to automatically start Stage 2. Once Stage 2 has started, the pushbutton can be released. When Stage 2 is completed, Stage 3 will automatically start. The test may be aborted at any time by pressing the pushbutton.

c) TRIP AND ALARM LEDS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE LEDS ⇒ TRIP & ALARM LEDS

<div> <div>TRIP & ALARM LEDS</div> <div>MESSAGE</div> </div>	<div> <div>TRIP LED INPUT:</div> <div>Off</div> </div>	Range: FlexLogic™ operand
	<div> <div>ALARM LED INPUT:</div> <div>Off</div> </div>	Range: FlexLogic™ operand

The Trip and Alarm LEDs are on LED Panel 1. Each indicator can be programmed to become illuminated when the selected FlexLogic™ operand is in the Logic 1 state.

d) USER-PROGRAMMABLE LED 1(48)

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE LEDS ⇒ USER-PROGRAMMABLE LED 1(48)

<div> <div>USER-PROGRAMMABLE</div> <div>LED 1</div> </div>	<div> <div>LED 1 OPERAND:</div> <div>Off</div> </div>	Range: FlexLogic™ operand
	<div> <div>LED 1 TYPE:</div> <div>Self-Reset</div> </div>	Range: Self-Reset, Latched

There are 48 amber LEDs across the relay faceplate LED panels. Each of these indicators can be programmed to illuminate when the selected FlexLogic™ operand is in the Logic 1 state.

- LEDs 1 through 24 inclusive are on LED Panel 2; LEDs 25 through 48 inclusive are on LED Panel 3.

Refer to the LED Indicators section in Chapter 4 for the locations of these indexed LEDs. This menu selects the operands to control these LEDs. Support for applying user-customized labels to these LEDs is provided. If the **LED X TYPE** setting is “Self-Reset” (default setting), the LED illumination will track the state of the selected LED operand. If the **LED X TYPE** setting is ‘Latched’, the LED, once lit, remains so until reset by the faceplate RESET button, from a remote device via a communications channel, or from any programmed operand, even if the LED operand state de-asserts.

Table 5–3: RECOMMENDED SETTINGS FOR LED PANEL 2 LABELS

SETTING	PARAMETER	SETTING	PARAMETER
LED 1 Operand	SETTING GROUP ACT 1	LED 13 Operand	Off
LED 2 Operand	SETTING GROUP ACT 2	LED 14 Operand	BREAKER 2 OPEN
LED 3 Operand	SETTING GROUP ACT 3	LED 15 Operand	BREAKER 2 CLOSED
LED 4 Operand	SETTING GROUP ACT 4	LED 16 Operand	BREAKER 2 TROUBLE
LED 5 Operand	SETTING GROUP ACT 5	LED 17 Operand	SYNC 1 SYNC OP
LED 6 Operand	SETTING GROUP ACT 6	LED 18 Operand	SYNC 2 SYNC OP
LED 7 Operand	Off	LED 19 Operand	Off
LED 8 Operand	Off	LED 20 Operand	Off
LED 9 Operand	BREAKER 1 OPEN	LED 21 Operand	Off
LED 10 Operand	BREAKER 1 CLOSED	LED 22 Operand	Off
LED 11 Operand	BREAKER 1 TROUBLE	LED 23 Operand	Off
LED 12 Operand	Off	LED 24 Operand	Off

Refer to the *Control of Setting Groups* example in the *Control Elements* section of this chapter for group activation.

5.2.11 USER-PROGRAMMABLE SELF TESTS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE SELF TESTS

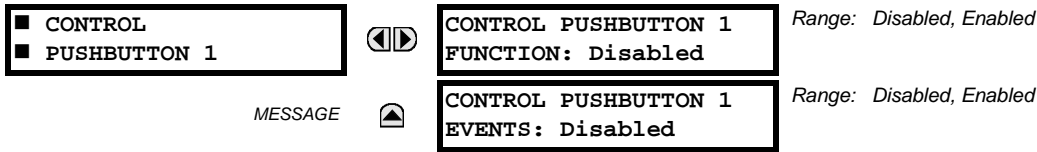
<div> <div>■ USER-PROGRAMMABLE</div> <div>■ SELF TESTS</div> </div>	<div> <div>◀▶</div> <div>DIRECT RING BREAK FUNCTION: Enabled</div> </div>		Range: Disabled, Enabled. Valid for units equipped with Direct Input/Output module.
	MESSAGE	<div> <div>▲▼</div> <div>DIRECT DEVICE OFF FUNCTION: Enabled</div> </div>	Range: Disabled, Enabled. Valid for units equipped with Direct Input/Output module.
	MESSAGE	<div> <div>▲▼</div> <div>REMOTE DEVICE OFF FUNCTION: Enabled</div> </div>	Range: Disabled, Enabled. Valid for units that contain a CPU with Ethernet capability.
	MESSAGE	<div> <div>▲▼</div> <div>PRI. ETHERNET FAIL FUNCTION: Disabled</div> </div>	Range: Disabled, Enabled. Valid for units that contain a CPU with a primary fiber port.
	MESSAGE	<div> <div>▲▼</div> <div>SEC. ETHERNET FAIL FUNCTION: Disabled</div> </div>	Range: Disabled, Enabled. Valid for units that contain a CPU with a redundant fiber port.
	MESSAGE	<div> <div>▲▼</div> <div>BATTERY FAIL FUNCTION: Enabled</div> </div>	Range: Disabled, Enabled.
	MESSAGE	<div> <div>▲▼</div> <div>SNTP FAIL FUNCTION: Enabled</div> </div>	Range: Disabled, Enabled. Valid for units that contain a CPU with Ethernet capability.
	MESSAGE	<div> <div>▲</div> <div>IRIG-B FAIL FUNCTION: Enabled</div> </div>	Range: Disabled, Enabled.

All major self-test alarms are reported automatically with their corresponding FlexLogic™ operands, events, and targets. Most of the Minor Alarms can be disabled if desired.

When in the “Disabled” mode, minor alarms will not assert a FlexLogic™ operand, write to the event recorder, display target messages. Moreover, they will not trigger the **ANY MINOR ALARM** or **ANY SELF-TEST** messages. When in the “Enabled” mode, minor alarms continue to function along with other major and minor alarms. Refer to the *Relay Self-Tests* section in Chapter 7 for additional information on major and minor self-test alarms.

5.2.12 CONTROL PUSHBUTTONS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ CONTROL PUSHBUTTONS ⇒ CONTROL PUSHBUTTON 1(7)



The three standard pushbuttons located on the top left panel of the faceplate are user-programmable and can be used for various applications such as performing an LED test, switching setting groups, and invoking and scrolling through user-programmable displays, etc. Firmware revisions 3.2x and older use these three pushbuttons for manual breaker control. This functionality has been retained – if the Breaker Control feature is configured to use the three pushbuttons, they cannot be used as user-programmable control pushbuttons. The location of the control pushbuttons in the following figure.

An additional four control pushbuttons are included when the N60 is ordered with twelve user programmable pushbuttons.

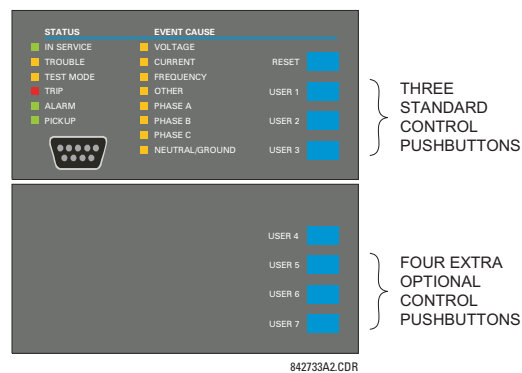


Figure 5-4: CONTROL PUSHBUTTONS

Control pushbuttons are not typically used for critical operations and are not protected by the control password. However, by supervising their output operands, the user can dynamically enable or disable control pushbuttons for security reasons.

Each control pushbutton asserts its own FlexLogic™ operand, CONTROL PUSHBTN 1(7) ON. These operands should be configured appropriately to perform the desired function. The operand remains asserted as long as the pushbutton is pressed and resets when the pushbutton is released. A dropout delay of 100 ms is incorporated to ensure fast pushbutton manipulation will be recognized by various features that may use control pushbuttons as inputs.

An event is logged in the Event Record (as per user setting) when a control pushbutton is pressed; no event is logged when the pushbutton is released. The faceplate keys (including control keys) cannot be operated simultaneously – a given key must be released before the next one can be pressed.

The control pushbuttons become user-programmable only if the Breaker Control feature is not configured for manual control via the User 1 through 3 pushbuttons as shown below. If configured for manual control, Breaker Control typically uses the larger, optional user-programmable pushbuttons, making the control pushbuttons available for other user applications.

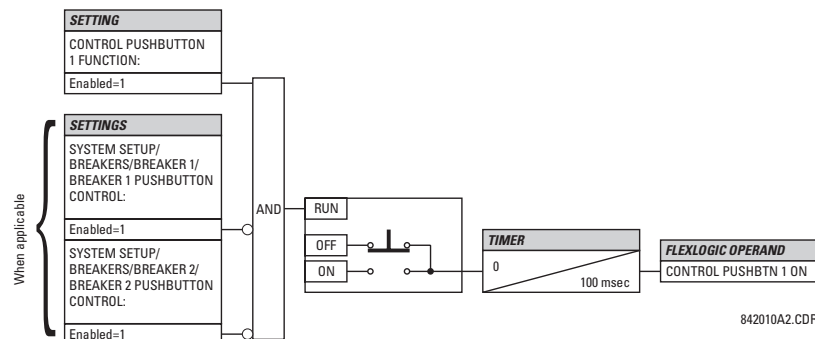


Figure 5-5: CONTROL PUSHBUTTON LOGIC

5.2.13 USER-PROGRAMMABLE PUSHBUTTONS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE PUSHBUTTONS ⇒ USER PUSHBUTTON 1(12)

■ USER PUSHBUTTON 1	◀▶	PUSHBUTTON 1 FUNCTION: Disabled	Range: Self-Reset, Latched, Disabled
MESSAGE	▲▼	PUSHBTN 1 ID TEXT:	Range: Up to 20 alphanumeric characters
MESSAGE	▲▼	PUSHBTN 1 ON TEXT:	Range: Up to 20 alphanumeric characters
MESSAGE	▲▼	PUSHBTN 1 OFF TEXT:	Range: Up to 20 alphanumeric characters
MESSAGE	▲▼	PUSHBTN 1 DROP-OUT TIME: 0.00 s	Range: 0 to 60.00 s in steps of 0.01
MESSAGE	▲▼	PUSHBUTTON 1 TARGETS: Disabled	Range: Self-Reset, Latched, Disabled
MESSAGE	▲	PUSHBUTTON 1 EVENTS: Disabled	Range: Disabled, Enabled

The N60 has 12 optional user-programmable pushbuttons available, each configured via 12 identical menus. The pushbuttons provide an easy and error-free method of manually entering digital information (On, Off) into FlexLogic™ equations as well as protection and control elements. Typical applications include breaker control, autorecloser blocking, ground protection blocking, and setting groups changes.

The user-configurable pushbuttons are shown below. They can be custom labeled with a factory-provided template, available online at <http://www.GEmultilin.com>.



Figure 5-6: USER-PROGRAMMABLE PUSHBUTTONS

Each pushbutton asserts its own On and Off FlexLogic™ operands, respectively. FlexLogic™ operands should be used to program desired pushbutton actions. The operand names are PUSHBUTTON 1 ON and PUSHBUTTON 1 OFF.

A pushbutton may be programmed to latch or self-reset. An indicating LED next to each pushbutton signals the present status of the corresponding "On" FlexLogic™ operand. When set to "Latched", the state of each pushbutton is stored in non-volatile memory which is maintained during any supply power loss.

Pushbuttons states can be logged by the Event Recorder and displayed as target messages. User-defined messages can also be associated with each pushbutton and displayed when the pushbutton is ON.

- **PUSHBUTTON 1 FUNCTION:** This setting selects the characteristic of the pushbutton. If set to "Disabled", the pushbutton is deactivated and the corresponding FlexLogic™ operands (both "On" and "Off") are de-asserted. If set to "Self-reset", the control logic of the pushbutton asserts the "On" corresponding FlexLogic™ operand as long as the pushbutton is being pressed. As soon as the pushbutton is released, the FlexLogic™ operand is de-asserted. The "Off" operand is asserted/de-asserted accordingly.

If set to "Latched", the control logic alternates the state of the corresponding FlexLogic™ operand between "On" and "Off" on each push of the button. When operating in "Latched" mode, FlexLogic™ operand states are stored in non-volatile memory. Should power be lost, the correct pushbutton state is retained upon subsequent power up of the relay.

- **PUSHBTN 1 ID TEXT:** This setting specifies the top 20-character line of the user-programmable message and is intended to provide ID information of the pushbutton. Refer to the User-Definable Displays section for instructions on how to enter alphanumeric characters from the keypad.
- **PUSHBTN 1 ON TEXT:** This setting specifies the bottom 20-character line of the user-programmable message and is displayed when the pushbutton is in the “on” position. Refer to the User-Definable Displays section for instructions on entering alphanumeric characters from the keypad.
- **PUSHBTN 1 OFF TEXT:** This setting specifies the bottom 20-character line of the user-programmable message and is displayed when the pushbutton is activated from the On to the Off position and the **PUSHBUTTON 1 FUNCTION** is “Latched”. This message is not displayed when the **PUSHBUTTON 1 FUNCTION** is “Self-reset” as the pushbutton operand status is implied to be “Off” upon its release. All user text messaging durations for the pushbuttons are configured with the **PRODUCT SETUP** ⇒ **DISPLAY PROPERTIES** ⇒ **FLASH MESSAGE TIME** setting.
- **PUSHBTN 1 DROP-OUT TIME:** This setting specifies a drop-out time delay for a pushbutton in the self-reset mode. A typical applications for this setting is providing a select-before-operate functionality. The selecting pushbutton should have the drop-out time set to a desired value. The operating pushbutton should be logically ANDed with the selecting pushbutton in FlexLogic™. The selecting pushbutton LED remains on for the duration of the drop-out time, signaling the time window for the intended operation.

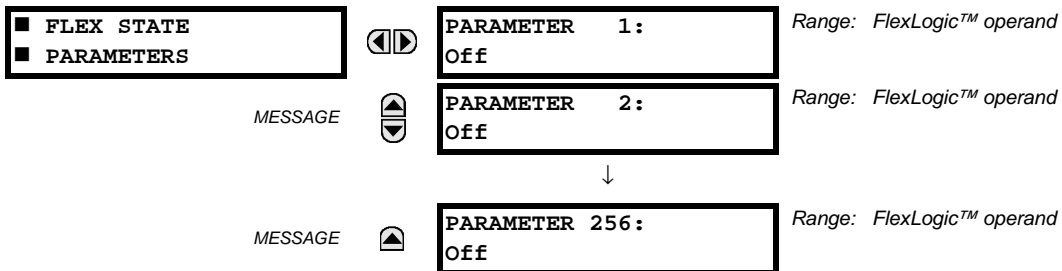
For example, consider a relay with the following settings: **PUSHBTN 1 ID TEXT:** “AUTORECLOSER”, **PUSHBTN 1 ON TEXT:** “DISABLED - CALL 2199”, and **PUSHBTN 1 OFF TEXT:** “ENABLED”. When Pushbutton 1 changes its state to the “On” position, the following **AUTOCLOSER DISABLED – Call 2199** message is displayed: When Pushbutton 1 changes its state to the “Off” position, the message will change to **AUTORECLOSER ENABLED**.



User-programmable pushbuttons require a type HP relay faceplate. If an HP-type faceplate was ordered separately, the relay order code must be changed to indicate the HP faceplate option. This can be done via EnerVista UR Setup with the **Maintenance > Enable Pushbutton** command.

5.2.14 FLEX STATE PARAMETERS

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ FLEX STATE PARAMETERS



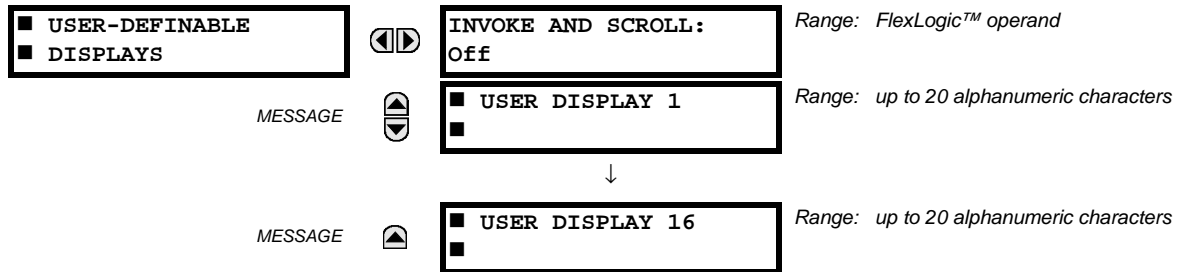
This feature provides a mechanism where any of 256 selected FlexLogic™ operand states can be used for efficient monitoring. The feature allows user-customized access to the FlexLogic™ operand states in the relay. The state bits are packed so that 16 states may be read out in a single Modbus register. The state bits can be configured so that all of the states which are of interest to the user are available in a minimum number of Modbus registers.

The state bits may be read out in the “Flex States” register array beginning at Modbus address 900 hex. 16 states are packed into each register, with the lowest-numbered state in the lowest-order bit. There are 16 registers in total to accommodate the 256 state bits.

5.2.15 USER-DEFINABLE DISPLAYS

a) MAIN MENU

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-DEFINABLE DISPLAYS



This menu provides a mechanism for manually creating up to 16 user-defined information displays in a convenient viewing sequence in the **USER DISPLAYS** menu (between the **TARGETS** and **ACTUAL VALUES** top-level menus). The sub-menus facilitate text entry and Modbus Register data pointer options for defining the User Display content.

Once programmed, the user-definable displays can be viewed in two ways.

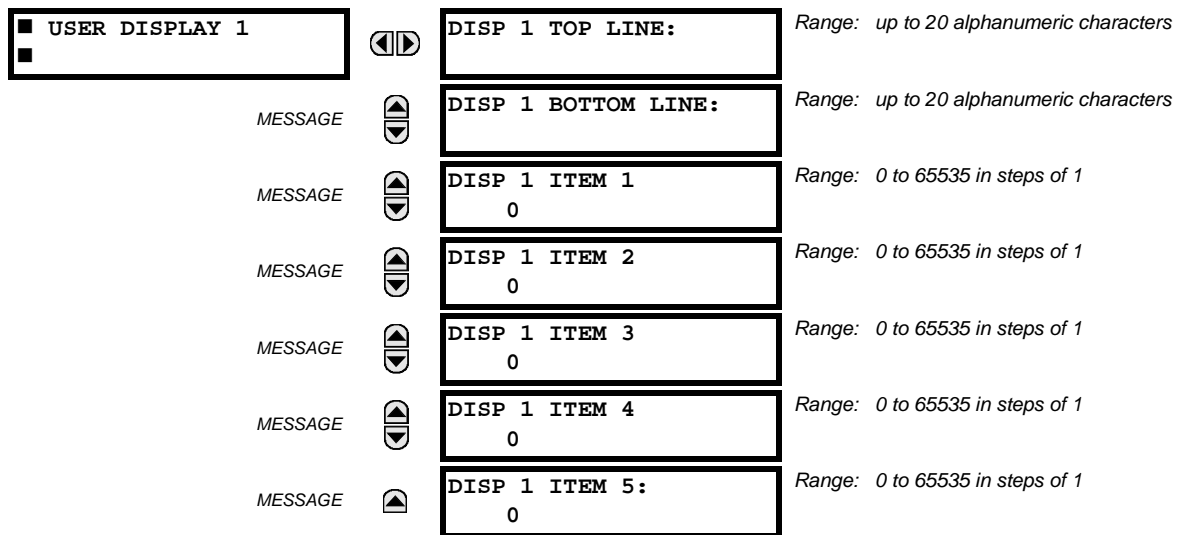
- **KEYPAD:** Use the Menu key to select the **USER DISPLAYS** menu item to access the first user-definable display (note that only the programmed screens are displayed). The screens can be scrolled using the Up and Down keys. The display disappears after the default message time-out period specified by the **PRODUCT SETUP ⇒ DISPLAY PROPERTIES ⇒ DEFAULT MESSAGE TIMEOUT** setting.
- **USER-PROGRAMMABLE CONTROL INPUT:** The user-definable displays also respond to the **INVOKE AND SCROLL** setting. Any FlexLogic™ operand (in particular, the user-programmable pushbutton operands), can be used to navigate the programmed displays.

On the rising edge of the configured operand (such as when the pushbutton is pressed), the displays are invoked by showing the last user-definable display shown during the previous activity. From this moment onward, the operand acts exactly as the Down key and allows scrolling through the configured displays. The last display wraps up to the first one. The **INVOKE AND SCROLL** input and the Down keypad key operate concurrently.

When the default timer expires (set by the **DEFAULT MESSAGE TIMEOUT** setting), the relay will start to cycle through the user displays. The next activity of the **INVOKE AND SCROLL** input stops the cycling at the currently displayed user display, not at the first user-defined display. The **INVOKE AND SCROLL** pulses must last for at least 250 ms to take effect.

b) USER DISPLAY 1(16)

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ USER-DEFINABLE DISPLAYS ⇒ USER DISPLAY 1(16)



Any existing system display can be automatically copied into an available user display by selecting the existing display and pressing the **ENTER** key. The display will then prompt **ADD TO USER DISPLAY LIST?**. After selecting “Yes”, a message indicates that the selected display has been added to the user display list. When this type of entry occurs, the sub-menus are automatically configured with the proper content – this content may subsequently be edited.

This menu is used **to enter** user-defined text and/or user-selected Modbus-registered data fields into the particular user display. Each user display consists of two 20-character lines (top and bottom). The tilde (~) character is used to mark the start of a data field - the length of the data field needs to be accounted for. Up to 5 separate data fields (**ITEM 1(5)**) can be entered in a user display - the *n*th tilde (~) refers to the *n*th item.

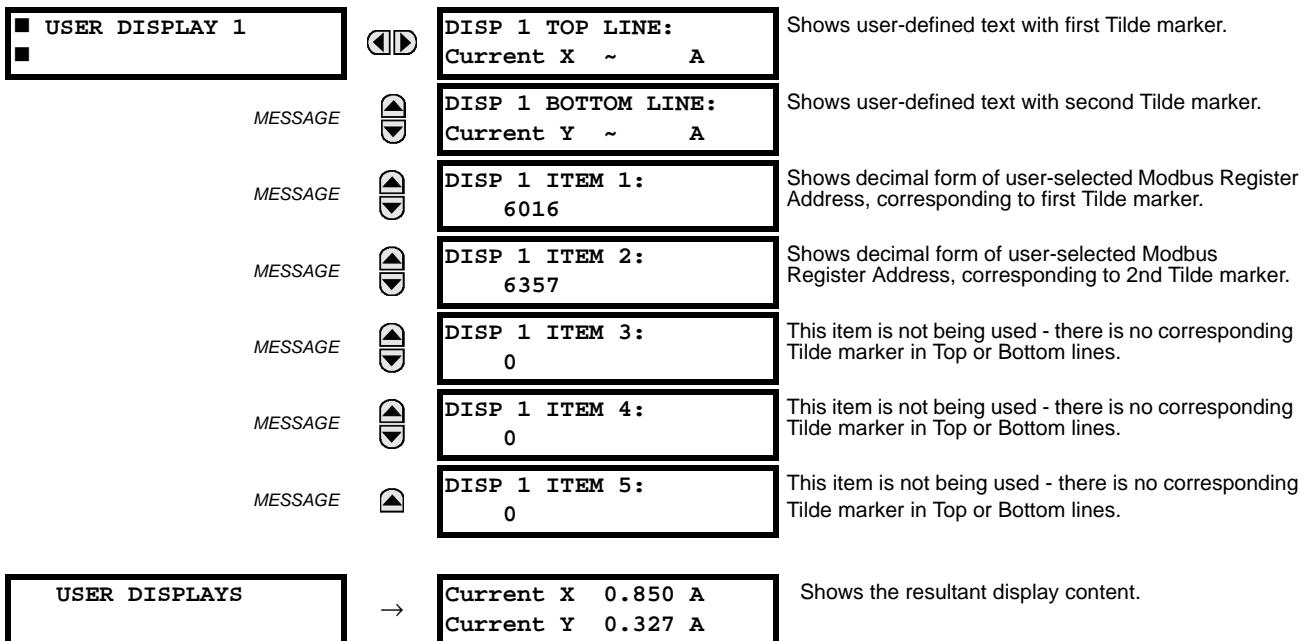
A User Display may be entered from the faceplate keypad or the EnerVista UR Setup interface (preferred for convenience). The following procedure shows how to enter text characters in the top and bottom lines from the faceplate keypad:

1. Select the line to be edited.
2. Press the **ENTER** key to enter text edit mode.
3. Use either Value key to scroll through the characters. A space is selected like a character.
4. Press the **ENTER** key to advance the cursor to the next position.
5. Repeat step 3 and continue entering characters until the desired text is displayed.
6. The **HELP** key may be pressed at any time for context sensitive help information.
7. Press the **ENTER** key to store the new settings.

To enter a numerical value for any of the 5 items (the *decimal form* of the selected Modbus address) from the faceplate keypad, use the number keypad. Use the value of '0' for any items not being used. Use the **HELP** key at any selected system display (setting, actual value, or command) which has a Modbus address, to view the *hexadecimal form* of the Modbus address, then manually convert it to decimal form before entering it (EnerVista UR Setup usage conveniently facilitates this conversion).

Use the **MENU** key to go to the user displays menu **to view** the user-defined content. The current user displays will show in sequence, changing every 4 seconds. While viewing a user display, press the **ENTER** key and then select the “Yes” option **to remove** the display from the user display list. Use the **MENU** key again **to exit** the user displays menu.

An example User Display setup and result is shown below:



5.2.16 DIRECT INPUTS/OUTPUTS

a) MAIN MENU

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ DIRECT I/O

■ DIRECT I/O	◀▶	DIRECT OUTPUT DEVICE ID: 1	Range: 1 to 16
MESSAGE	▲▼	DIRECT I/O CH1 RING CONFIGURATION: Yes	Range: Yes, No
MESSAGE	▲▼	DIRECT I/O CH2 RING CONFIGURATION: Yes	Range: Yes, No
MESSAGE	▲▼	DIRECT I/O DATA RATE: 64 kbps	Range: 64 kbps, 128 kbps
MESSAGE	▲▼	DIRECT I/O CHANNEL CROSSOVER: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	■ CRC ALARM CH1 ■	See page 5-41.
MESSAGE	▲▼	■ CRC ALARM CH2 ■	See page 5-41.
MESSAGE	▲▼	■ UNRETURNED ■ MESSAGES ALARM CH1	See page 5-41.
MESSAGE	▲	■ UNRETURNED ■ MESSAGES ALARM CH2	See page 5-41.

Direct inputs/outputs are intended for exchange of status information (inputs and outputs) between UR-series relays connected directly via Type-7 digital communications cards. The mechanism is very similar to IEC 61850 GSSE, except that communications takes place over a non-switchable isolated network and is optimized for speed. On Type 7 cards that support two channels, direct output messages are sent from both channels simultaneously. This effectively sends direct output messages both ways around a ring configuration. On Type 7 cards that support one channel, direct output messages are sent only in one direction. Messages will be resent (forwarded) when it is determined that the message did not originate at the receiver.

Direct output message timing is similar to GSSE message timing. Integrity messages (with no state changes) are sent at least every 1000 ms. Messages with state changes are sent within the main pass scanning the inputs and asserting the outputs unless the communication channel bandwidth has been exceeded. Two Self-Tests are performed and signaled by the following FlexLogic™ operands:

1. DIRECT RING BREAK (direct input/output ring break). This FlexLogic™ operand indicates that direct output messages sent from a UR-series relay are not being received back by the relay.
2. DIRECT DEVICE 1(16) OFF (direct device offline). This FlexLogic™ operand indicates that direct output messages from at least one direct device are not being received.

Direct input/output settings are similar to remote input/output settings. The equivalent of the remote device name strings for direct inputs/outputs is the **DIRECT OUTPUT DEVICE ID**. The **DIRECT OUTPUT DEVICE ID** identifies the relay in all direct output messages. All UR-series IEDs in a ring should have unique numbers assigned. The IED ID is used to identify the sender of the direct input/output message.

If the direct input/output scheme is configured to operate in a ring (**DIRECT I/O RING CONFIGURATION**: "Yes"), all direct output messages should be received back. If not, the Direct Input/Output Ring Break self-test is triggered. The self-test error is signaled by the DIRECT RING BREAK FlexLogic™ operand.

Select the **DIRECT I/O DATA RATE** to match the data capabilities of the communications channel. Back-to-back connections of the local relays configured with the 7A, 7B, 7C, 7D, 7H, 7I, 7J, 7K, 72 and 73 fiber optic communication cards may be set to 128 kbps. For local relays configured with all other communication cards (i.e. 7E, 7F, 7G, 7L, 7M, 7N, 7P, 7R, 7S, 7T, 7W, 74, 75, 76 and 77), the baud rate will be set to 64 kbps. All IEDs communicating over direct inputs/outputs must be set to

the same data rate. UR-series IEDs equipped with dual-channel communications cards apply the same data rate to both channels. Delivery time for direct input/output messages is approximately 0.2 of a power system cycle at 128 kbps and 0.4 of a power system cycle at 64 kbps, per each 'bridge'.



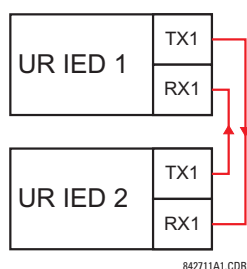
The G.703 and RS422 modules are fixed at 64 kbps only. The **SETTINGS ⇒ **PRODUCT SETUP** ⇒ **DIRECT I/O** ⇒ **DIRECT I/O DATA RATE** setting is not applicable to these modules.**

The **DIRECT I/O CHANNEL CROSSOVER** setting applies to N60s with dual-channel communication cards and allows crossing over messages from Channel 1 to Channel 2. This places all UR-series IEDs into one direct input/output network regardless of the physical media of the two communication channels.

The following application examples illustrate the basic concepts for direct input/output configuration. Please refer to the *Inputs/Outputs* section in this chapter for information on configuring FlexLogic™ operands (flags, bits) to be exchanged.

EXAMPLE 1: EXTENDING THE INPUT/OUTPUT CAPABILITIES OF A UR-SERIES RELAY

Consider an application that requires additional quantities of digital inputs and/or output contacts and/or lines of programmable logic that exceed the capabilities of a single UR-series chassis. The problem is solved by adding an extra UR-series IED, such as the C30, to satisfy the additional input/output and programmable logic requirements. The two IEDs are connected via single-channel digital communication cards as shown in the figure below.



842711A1.CDR

Figure 5–7: INPUT/OUTPUT EXTENSION VIA DIRECT INPUTS/OUTPUTS

In the above application, the following settings should be applied:

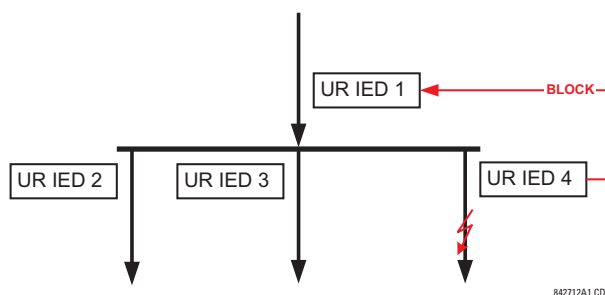
UR IED 1: **DIRECT OUTPUT DEVICE ID:** "1"
DIRECT I/O RING CONFIGURATION: "Yes"
DIRECT I/O DATA RATE: "128 kbps"

UR IED 2: **DIRECT OUTPUT DEVICE ID:** "2"
DIRECT I/O RING CONFIGURATION: "Yes"
DIRECT I/O DATA RATE: "128 kbps"

The message delivery time is about 0.2 of power cycle in both ways (at 128 kbps); i.e., from Device 1 to Device 2, and from Device 2 to Device 1. Different communications cards can be selected by the user for this back-to-back connection (fiber, G.703, or RS422).

EXAMPLE 2: INTERLOCKING BUSBAR PROTECTION

A simple interlocking busbar protection scheme could be accomplished by sending a blocking signal from downstream devices, say 2, 3, and 4, to the upstream device that monitors a single incomer of the busbar, as shown below.



842712A1.CDR

Figure 5–8: SAMPLE INTERLOCKING BUSBAR PROTECTION SCHEME

For increased reliability, a dual-ring configuration (shown below) is recommended for this application.

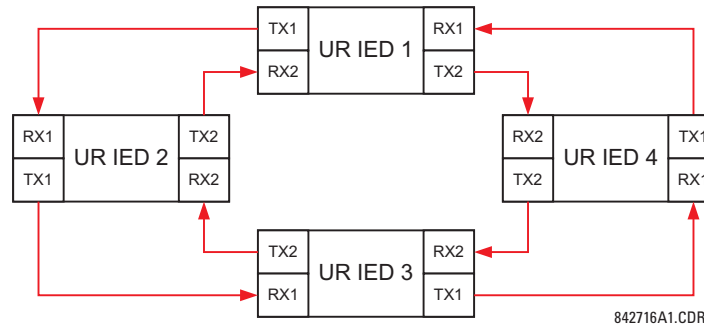


Figure 5-9: INTERLOCKING BUS PROTECTION SCHEME VIA DIRECT INPUTS/OUTPUTS

In the above application, the following settings should be applied:

UR IED 1:	DIRECT OUTPUT DEVICE ID: "1"	UR IED 2:	DIRECT OUTPUT DEVICE ID: "2"
	DIRECT I/O RING CONFIGURATION: "Yes"		DIRECT I/O RING CONFIGURATION: "Yes"
UR IED 3:	DIRECT OUTPUT DEVICE ID: "3"	UR IED 4:	DIRECT OUTPUT DEVICE ID: "4"
	DIRECT I/O RING CONFIGURATION: "Yes"		DIRECT I/O RING CONFIGURATION: "Yes"

Message delivery time is approximately 0.2 of power system cycle (at 128 kbps) times number of 'bridges' between the origin and destination. Dual-ring configuration effectively reduces the maximum 'communications distance' by a factor of two.

In this configuration the following delivery times are expected (at 128 kbps) if both rings are healthy:

IED 1 to IED 2: 0.2 of power system cycle; IED 1 to IED 3: 0.4 of power system cycle;
 IED 1 to IED 4: 0.2 of power system cycle; IED 2 to IED 3: 0.2 of power system cycle;
 IED 2 to IED 4: 0.4 of power system cycle; IED 3 to IED 4: 0.2 of power system cycle

If one ring is broken (say TX2/RX2) the delivery times are as follows:

IED 1 to IED 2: 0.2 of power system cycle; IED 1 to IED 3: 0.4 of power system cycle;
 IED 1 to IED 4: 0.6 of power system cycle; IED 2 to IED 3: 0.2 of power system cycle;
 IED 2 to IED 4: 0.4 of power system cycle; IED 3 to IED 4: 0.2 of power system cycle

A coordinating timer for this bus protection scheme could be selected to cover the worst case scenario (0.4 of power system cycle). Upon detecting a broken ring, the coordination time should be adaptively increased to 0.6 of power system cycle. The complete application requires addressing a number of issues such as failure of both the communications rings, failure or out-of-service conditions of one of the relays, etc. Self-monitoring flags of the direct inputs/outputs feature would be primarily used to address these concerns.

EXAMPLE 3: PILOT-AIDED SCHEMES

Consider the three-terminal line protection application shown below:

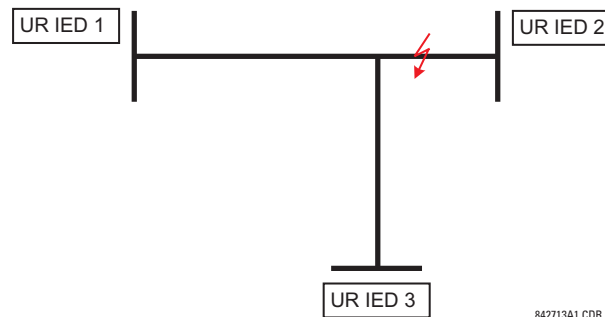


Figure 5-10: THREE-TERMINAL LINE APPLICATION

A permissive pilot-aided scheme could be implemented in a two-ring configuration as shown below (IEDs 1 and 2 constitute a first ring, while IEDs 2 and 3 constitute a second ring):

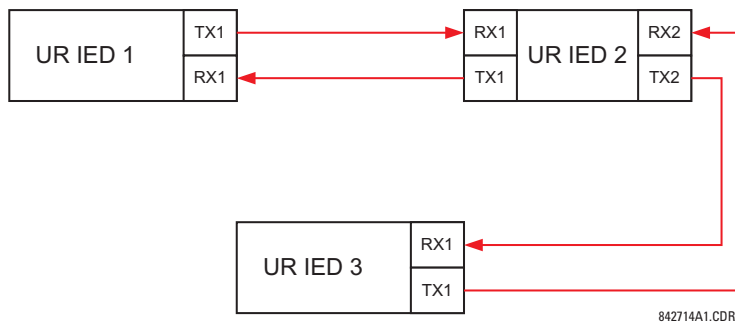


Figure 5-11: SINGLE-CHANNEL OPEN LOOP CONFIGURATION

In the above application, the following settings should be applied:

UR IED 1:	DIRECT OUTPUT DEVICE ID: "1"	UR IED 2:	DIRECT OUTPUT DEVICE ID: "2"
	DIRECT I/O RING CONFIGURATION: "Yes"		DIRECT I/O RING CONFIGURATION: "Yes"
UR IED 3:	DIRECT OUTPUT DEVICE ID: "3"		
	DIRECT I/O RING CONFIGURATION: "Yes"		

In this configuration the following delivery times are expected (at 128 kbps):

IED 1 to IED 2: 0.2 of power system cycle; IED 1 to IED 3: 0.5 of power system cycle;
IED 2 to IED 3: 0.2 of power system cycle

In the above scheme, IEDs 1 and 3 do not communicate directly. IED 2 must be configured to forward the messages as explained in the *Inputs/Outputs* section. A blocking pilot-aided scheme should be implemented with more security and, ideally, faster message delivery time. This could be accomplished using a dual-ring configuration as shown below.

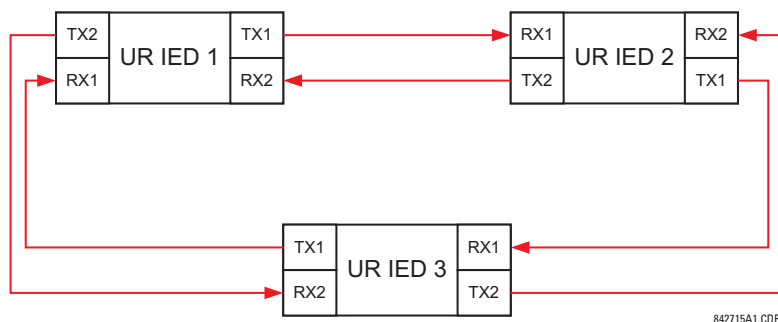


Figure 5-12: DUAL-CHANNEL CLOSED LOOP (DUAL-RING) CONFIGURATION

In the above application, the following settings should be applied:

UR IED 1:	DIRECT OUTPUT DEVICE ID: "1"	UR IED 2:	DIRECT OUTPUT DEVICE ID: "2"
	DIRECT I/O RING CONFIGURATION: "Yes"		DIRECT I/O RING CONFIGURATION: "Yes"
UR IED 3:	DIRECT OUTPUT DEVICE ID: "3"		
	DIRECT I/O RING CONFIGURATION: "Yes"		

In this configuration the following delivery times are expected (at 128 kbps) if both the rings are healthy:

IED 1 to IED 2: 0.2 of power system cycle; IED 1 to IED 3: 0.2 of power system cycle;
IED 2 to IED 3: 0.2 of power system cycle

The two communications configurations could be applied to both permissive and blocking schemes. Speed, reliability and cost should be taken into account when selecting the required architecture.

b) CRC ALARM CH1(2)

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ DIRECT I/O ⇒ CRC ALARM CH1(2)

■ CRC ALARM CH1	◀▶	CRC ALARM CH1 FUNCTION: Disabled	Range: Enabled, Disabled
MESSAGE	▲▼	CRC ALARM CH1 MESSAGE COUNT: 600	Range: 100 to 10000 in steps of 1
MESSAGE	▲▼	CRC ALARM CH1 THRESHOLD: 10	Range: 1 to 1000 in steps of 1
MESSAGE	▲	CRC ALARM CH1 EVENTS: Disabled	Range: Enabled, Disabled

The N60 checks integrity of the incoming direct input/output messages using a 32-bit CRC. The CRC Alarm function is available for monitoring the communication medium noise by tracking the rate of messages failing the CRC check. The monitoring function counts all incoming messages, including messages that failed the CRC check. A separate counter adds up messages that failed the CRC check. When the failed CRC counter reaches the user-defined level specified by the **CRC ALARM CH1 THRESHOLD** setting within the user-defined message count **CRC ALARM 1 CH1 COUNT**, the DIR IO CH1 CRC ALARM FlexLogic™ operand is set.

When the total message counter reaches the user-defined maximum specified by the **CRC ALARM CH1 MESSAGE COUNT** setting, both the counters reset and the monitoring process is restarted.

The operand shall be configured to drive an output contact, user-programmable LED, or selected communication-based output. Latching and acknowledging conditions - if required - should be programmed accordingly.

The CRC Alarm function is available on a per-channel basis. The total number of direct input/output messages that failed the CRC check is available as the **ACTUAL VALUES ⇒ STATUS ⇒ DIRECT INPUTS ⇒ CRC FAIL COUNT CH1(2)** actual value.

- **Message Count and Length of the Monitoring Window:** To monitor communications integrity, the relay sends 1 message per second (at 64 kbps) or 2 messages per second (128 kbps) even if there is no change in the direct outputs. For example, setting the **CRC ALARM CH1 MESSAGE COUNT** to “10000”, corresponds a time window of about 160 minutes at 64 kbps and 80 minutes at 128 kbps. If the messages are sent faster as a result of direct outputs activity, the monitoring time interval will shorten. This should be taken into account when determining the **CRC ALARM CH1 MESSAGE COUNT** setting. For example, if the requirement is a maximum monitoring time interval of 10 minutes at 64 kbps, then the **CRC ALARM CH1 MESSAGE COUNT** should be set to $10 \times 60 \times 1 = 600$.
- **Correlation of Failed CRC and Bit Error Rate (BER):** The CRC check may fail if one or more bits in a packet are corrupted. Therefore, an exact correlation between the CRC fail rate and the BER is not possible. Under certain assumptions an approximation can be made as follows. A direct input/output packet containing 20 bytes results in 160 bits of data being sent and therefore, a transmission of 63 packets is equivalent to 10,000 bits. A BER of 10^{-4} implies 1 bit error for every 10,000 bits sent/received. Assuming the best case of only 1 bit error in a failed packet, having 1 failed packet for every 63 received is about equal to a BER of 10^{-4} .

c) UNRETURNED MESSAGES ALARM CH1(2)

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ DIRECT I/O ⇒ UNRETURNED MESSAGES ALARM CH1(2)

■ UNRETURNED	◀▶	UNRET MSGS ALARM CH1 FUNCTION: Disabled	Range: Enabled, Disabled
■ MESSAGES ALARM CH1			
MESSAGE	▲▼	UNRET MSGS ALARM CH1 MESSAGE COUNT: 600	Range: 100 to 10000 in steps of 1
MESSAGE	▲▼	UNRET MSGS ALARM CH1 THRESHOLD: 10	Range: 1 to 1000 in steps of 1
MESSAGE	▲	UNRET MSGS ALARM CH1 EVENTS: Disabled	Range: Enabled, Disabled

The N60 checks integrity of the direct input/output communication ring by counting unreturned messages. In the ring configuration, all messages originating at a given device should return within a pre-defined period of time. The Unreturned Messages Alarm function is available for monitoring the integrity of the communication ring by tracking the rate of unre-

turned messages. This function counts all the outgoing messages and a separate counter adds the messages have failed to return. When the unreturned messages counter reaches the user-definable level specified by the **UNRET MSGS ALARM CH1 THRESHOLD** setting and within the user-defined message count **UNRET MSGS ALARM CH1 COUNT**, the DIR IO CH1 UNRET ALM FlexLogic™ operand is set.

When the total message counter reaches the user-defined maximum specified by the **UNRET MSGS ALARM CH1 MESSAGE COUNT** setting, both the counters reset and the monitoring process is restarted.

The operand shall be configured to drive an output contact, user-programmable LED, or selected communication-based output. Latching and acknowledging conditions, if required, should be programmed accordingly.

The Unreturned Messages Alarm function is available on a per-channel basis and is active only in the ring configuration. The total number of unreturned input/output messages is available as the **ACTUAL VALUES ⇒ STATUS ⇒ DIRECT INPUTS ⇒ UNRETURNED MSG COUNT CH1(2)** actual value.

5.2.17 INSTALLATION

PATH: SETTINGS ⇒ PRODUCT SETUP ⇒ INSTALLATION

■ INSTALLATION

■

MESSAGE

⏮⏭

⏮⏭

RELAY SETTINGS:
Not Programmed

RELAY NAME:
Relay-1

Range: Not Programmed, Programmed

Range: up to 20 alphanumeric characters

To safeguard against the installation of a relay without any entered settings, the unit will not allow signaling of any output relay until **RELAY SETTINGS** is set to "Programmed". This setting is defaulted to "Not Programmed" when at the factory. The **UNIT NOT PROGRAMMED** self-test error message is displayed until the relay is put into the "Programmed" state.

The **RELAY NAME** setting allows the user to uniquely identify a relay. This name will appear on generated reports. This name is also used to identify specific devices which are engaged in automatically sending/receiving data over the Ethernet communications channel using the IEC 61850 protocol.

5.3.1 AC INPUTS

a) CURRENT BANKS

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ AC INPUTS ⇒ CURRENT BANK F1

■ CURRENT BANK F1	◀▶	PHASE CT F1 PRIMARY: 1 A	Range: 1 to 65000 A in steps of 1
MESSAGE	▲▼	PHASE CT F1 SECONDARY: 1 A	Range: 1 A, 5 A
MESSAGE	▲▼	GROUND CT F1 PRIMARY: 1 A	Range: 1 to 65000 A in steps of 1
MESSAGE	▲	GROUND CT F1 SECONDARY: 1 A	Range: 1 A, 5 A

Six banks of phase/ground CTs can be set, where the current banks are denoted in the following format (*X* represents the module slot position letter):

Xa, where ***X*** = {**F**, **M**, **U**} and ***a*** = {**1**, **5**}.

See the *Introduction to AC Sources* section at the beginning of this chapter for additional details.

These settings are critical for all features that have settings dependent on current measurements. When the relay is ordered, the CT module must be specified to include a standard or sensitive ground input. As the phase CTs are connected in Wye (star), the calculated phasor sum of the three phase currents ($I_A + I_B + I_C = \text{Neutral Current} = 3I_o$) is used as the input for the neutral overcurrent elements. In addition, a zero-sequence (core balance) CT which senses current in all of the circuit primary conductors, or a CT in a neutral grounding conductor may also be used. For this configuration, the ground CT primary rating must be entered. To detect low level ground fault currents, the sensitive ground input may be used. In this case, the sensitive ground CT primary rating must be entered. Refer to Chapter 3 for more details on CT connections.

Enter the rated CT primary current values. For both 1000:5 and 1000:1 CTs, the entry would be 1000. For correct operation, the CT secondary rating must match the setting (which must also correspond to the specific CT connections used).

The following example illustrates how multiple CT inputs (current banks) are summed as one source current. Given If the following current banks:

F1: CT bank with 500:1 ratio; F5: CT bank with 1000: ratio; M1: CT bank with 800:1 ratio

The following rule applies:

$$\text{SRC 1} = \text{F1} + \text{F5} + \text{M1} \quad (\text{EQ 5.7})$$

1 pu is the highest primary current. In this case, 1000 is entered and the secondary current from the 500:1 ratio CT will be adjusted to that created by a 1000:1 CT before summation. If a protection element is set up to act on SRC 1 currents, then a pickup level of 1 pu will operate on 1000 A primary.

The same rule applies for current sums from CTs with different secondary taps (5 A and 1 A).

b) VOLTAGE BANKS

PATH: SETTINGS ⇒ ⚙ SYSTEM SETUP ⇒ AC INPUTS ⇒ ⚙ VOLTAGE BANK F5(U5)

<div>VOLTAGE BANK F5</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div>	<div> <div>◀▶</div> <div> <div>PHASE VT F5</div> <div>CONNECTION: Wye</div> </div> </div>	Range: Wye, Delta
	<div> <div>▲▼</div> <div> <div>PHASE VT F5</div> <div>SECONDARY: 66.4 V</div> </div> </div>	Range: 50.0 to 240.0 V in steps of 0.1
	<div> <div>▲▼</div> <div> <div>PHASE VT F5</div> <div>RATIO: 1.00 :1</div> </div> </div>	Range: 1.00 to 24000.00 in steps of 0.01
	<div> <div>▲▼</div> <div> <div>AUXILIARY VT F5</div> <div>CONNECTION: Vag</div> </div> </div>	Range: Vn, Vag, Vbg, Vcg, Vab, Vbc, Vca
	<div> <div>▲▼</div> <div> <div>AUXILIARY VT F5</div> <div>SECONDARY: 66.4 V</div> </div> </div>	Range: 50.0 to 240.0 V in steps of 0.1
	<div> <div>▲</div> <div> <div>AUXILIARY VT F5</div> <div>RATIO: 1.00 :1</div> </div> </div>	Range: 1.00 to 24000.00 in steps of 0.01



Because energy parameters are accumulated, these values should be recorded and then reset immediately prior to changing VT characteristics.

Three banks of phase/auxiliary VTs can be set, where voltage banks are denoted in the following format (X represents the module slot position letter):

Xa, where **X** = {F, M, U} and **a** = {5}.

See the *Introduction to AC Sources* section at the beginning of this chapter for additional details.

With VTs installed, the relay can perform voltage measurements as well as power calculations. Enter the **PHASE VT F5 CONNECTION** made to the system as “Wye” or “Delta”. An open-delta source VT connection would be entered as “Delta”. See the *Typical Wiring Diagram* in Chapter 3 for details.



The nominal **PHASE VT F5 SECONDARY** voltage setting is the voltage across the relay input terminals when nominal voltage is applied to the VT primary.

For example, on a system with a 13.8 kV nominal primary voltage and with a 14400:120 volt VT in a Delta connection, the secondary voltage would be 115, i.e. $(13800 / 14400) \times 120$. For a Wye connection, the voltage value entered must be the phase to neutral voltage which would be $115 / \sqrt{3} = 66.4$.

On a 14.4 kV system with a Delta connection and a VT primary to secondary turns ratio of 14400:120, the voltage value entered would be 120, i.e. $14400 / 120$.

5.3.2 POWER SYSTEM

PATH: SETTINGS ⇒ ↓ SYSTEM SETUP ⇒ ↓ POWER SYSTEM

<div>■ POWER SYSTEM</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div>	<div>◀▶</div>	<div>NOMINAL FREQUENCY:</div> <div>60 Hz</div>	Range: 25 to 60 Hz in steps of 1
	<div>▲▼</div>	<div>PHASE ROTATION:</div> <div>ABC</div>	Range: ABC, ACB
	<div>▲▼</div>	<div>FREQUENCY AND PHASE REFERENCE: SRC 1</div>	Range: SRC 1, SRC 2, SRC 3, SRC 4, SRC 5, SRC 6
	<div>▲</div>	<div>FREQUENCY TRACKING:</div> <div>Enabled</div>	Range: Disabled, Enabled

The power system **NOMINAL FREQUENCY** value is used as a default to set the digital sampling rate if the system frequency cannot be measured from available signals. This may happen if the signals are not present or are heavily distorted. Before reverting to the nominal frequency, the frequency tracking algorithm holds the last valid frequency measurement for a safe period of time while waiting for the signals to reappear or for the distortions to decay.

The phase sequence of the power system is required to properly calculate sequence components and power parameters. The **PHASE ROTATION** setting matches the power system phase sequence. Note that this setting informs the relay of the actual system phase sequence, either ABC or ACB. CT and VT inputs on the relay, labeled as A, B, and C, must be connected to system phases A, B, and C for correct operation.

The **FREQUENCY AND PHASE REFERENCE** setting determines which signal source is used (and hence which AC signal) for phase angle reference. The AC signal used is prioritized based on the AC inputs that are configured for the signal source: phase voltages takes precedence, followed by auxiliary voltage, then phase currents, and finally ground current.

For three phase selection, phase A is used for angle referencing ($V_{\text{ANGLE REF}} = V_A$), while Clarke transformation of the phase signals is used for frequency metering and tracking ($V_{\text{FREQUENCY}} = (2V_A - V_B - V_C)/3$) for better performance during fault, open pole, and VT and CT fail conditions.

The phase reference and frequency tracking AC signals are selected based upon the Source configuration, regardless of whether or not a particular signal is actually applied to the relay.

Phase angle of the reference signal will always display zero degrees and all other phase angles will be relative to this signal. If the pre-selected reference signal is not measurable at a given time, the phase angles are not referenced.

The phase angle referencing is done via a phase locked loop, which can synchronize independent UR-series relays if they have the same AC signal reference. These results in very precise correlation of time tagging in the event recorder between different UR-series relays provided the relays have an IRIG-B connection.



FREQUENCY TRACKING should only be set to "Disabled" in very unusual circumstances; consult the factory for special variable-frequency applications.

5.3.3 SIGNAL SOURCES

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ SIGNAL SOURCES ⇒ SOURCE 1(6)

<div> <div>SOURCE 1</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> </div>	<div> <div>◀▶</div> <div>SOURCE 1 NAME:</div> <div>SRC 1</div> </div>	Range: up to 6 alphanumeric characters
	<div> <div>▲▼</div> <div>SOURCE 1 PHASE CT:</div> <div>None</div> </div>	Range: None, F1, F5, F1+F5,... up to a combination of any 6 CTs. Only Phase CT inputs are displayed.
	<div> <div>▲▼</div> <div>SOURCE 1 GROUND CT:</div> <div>None</div> </div>	Range: None, F1, F5, F1+F5,... up to a combination of any 6 CTs. Only Ground CT inputs are displayed.
	<div> <div>▲▼</div> <div>SOURCE 1 PHASE VT:</div> <div>None</div> </div>	Range: None, F1, F5, M1, M5, U1, U5 Only phase voltage inputs will be displayed.
	<div> <div>▲</div> <div>SOURCE 1 AUX VT:</div> <div>None</div> </div>	Range: None, F1, F5, M1, M5, U1, U5 Only auxiliary voltage inputs will be displayed.

Six identical source menus are available. The "SRC 1" text can be replaced by with a user-defined name appropriate for the associated source.

"F", "M", and "U" represent the module slot position. The number directly following these letters represents either the first bank of four channels (1, 2, 3, 4) called "1" or the second bank of four channels (5, 6, 7, 8) called "5" in a particular CT/VT module. Refer to the *Introduction to AC Sources* section at the beginning of this chapter for additional details on this concept.

It is possible to select the sum of up to six (6) CTs. The first channel displayed is the CT to which all others will be referred. For example, the selection "F1+F5" indicates the sum of each phase from channels "F1" and "F5", scaled to whichever CT has the higher ratio. Selecting "None" hides the associated actual values.

The approach used to configure the AC sources consists of several steps; first step is to specify the information about each CT and VT input. For CT inputs, this is the nominal primary and secondary current. For VTs, this is the connection type, ratio and nominal secondary voltage. Once the inputs have been specified, the configuration for each source is entered, including specifying which CTs will be summed together.

User Selection of AC Parameters for Comparator Elements:

CT/VT modules automatically calculate all current and voltage parameters from the available inputs. Users must select the specific input parameters to be measured by every element in the relevant settings menu. The internal design of the element specifies which type of parameter to use and provides a setting for source selection. In elements where the parameter may be either fundamental or RMS magnitude, such as phase time overcurrent, two settings are provided. One setting specifies the source, the second setting selects between fundamental phasor and RMS.

AC Input Actual Values:

The calculated parameters associated with the configured voltage and current inputs are displayed in the current and voltage sections of actual values. Only the phasor quantities associated with the actual AC physical input channels will be displayed here. All parameters contained within a configured source are displayed in the sources section of the actual values.

DISTURBANCE DETECTORS (INTERNAL):

The 50DD element is a sensitive current disturbance detector that detects any disturbance on the protected system. 50DD is intended for use in conjunction with measuring elements, blocking of current based elements (to prevent maloperation as a result of the wrong settings), and starting oscillography data capture. A disturbance detector is provided for each Source.

The 50DD function responds to the changes in magnitude of the sequence currents. The disturbance detector scheme logic is as follows:

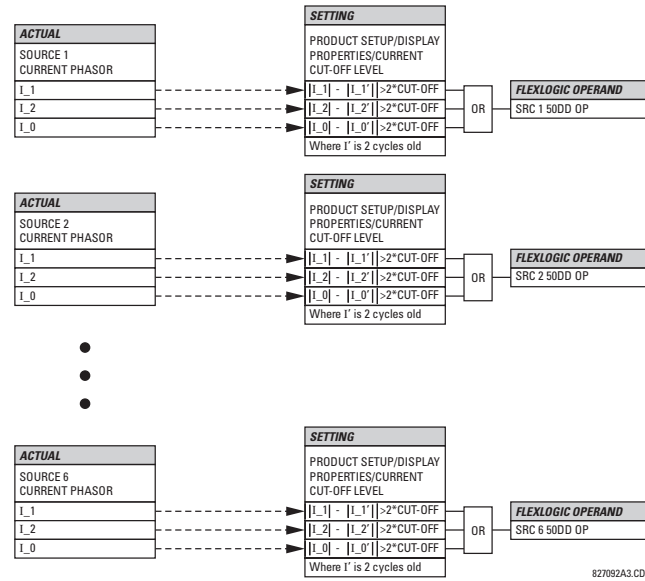


Figure 5-13: DISTURBANCE DETECTOR LOGIC DIAGRAM

The disturbance detector responds to the change in currents of twice the current cut-off level. The default cut-off threshold is 0.02 pu; thus by default the disturbance detector responds to a change of 0.04 pu. The metering sensitivity setting (**PRODUCT SETUP** ⇒ **DISPLAY PROPERTIES** ⇒ **CURRENT CUT-OFF LEVEL**) controls the sensitivity of the disturbance detector accordingly.

EXAMPLE USE OF SOURCES:

An example of the use of sources, with a relay with three CT/VT modules, is shown in the diagram below. A relay could have the following hardware configuration:

INCREASING SLOT POSITION LETTER -->		
CT/VT MODULE 1	CT/VT MODULE 2	CT/VT MODULE 3
CTs	CTs	VTs

This configuration could be used on a two winding transformer, with one winding connected into a breaker-and-a-half system. The following figure shows the arrangement of sources used to provide the functions required in this application, and the CT/VT inputs that are used to provide the data.

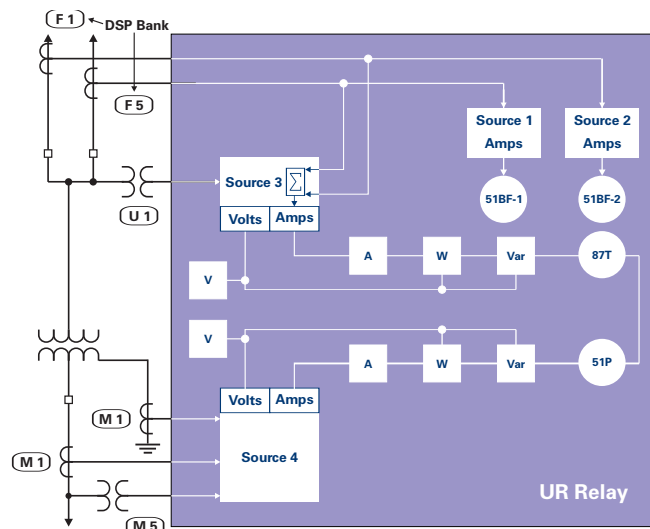


Figure 5-14: EXAMPLE USE OF SOURCES

5.3.4 BREAKERS

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ BREAKERS ⇒ BREAKER 1(2)

■ BREAKER 1	◀▶	BREAKER 1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	BREAKER1 PUSH BUTTON CONTROL: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	BREAKER 1 NAME: Bkr 1	Range: up to 6 alphanumeric characters
MESSAGE	▲▼	BREAKER 1 MODE: 3-Pole	Range: 3-Pole, 1-Pole
MESSAGE	▲▼	BREAKER 1 OPEN: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	BREAKER 1 CLOSE: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	BREAKER 1 ϕ A/3-POLE: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	BREAKER 1 ϕ B: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	BREAKER 1 ϕ C: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	BREAKER 1 EXT ALARM: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	BREAKER 1 ALARM DELAY: 0.000 s	Range: 0.000 to 1 000 000.000 s in steps of 0.001
MESSAGE	▲▼	MANUAL CLOSE RECAL TIME: 0.000 s	Range: 0.000 to 1 000 000.000 s in steps of 0.001
MESSAGE	▲▼	BREAKER 1 OUT OF SV: Off	Range: FlexLogic™ operand
MESSAGE	▲	XCBR ST.LOC OPERAND: Off	Range: FlexLogic™ operand

A description of the operation of the breaker control and status monitoring features is provided in Chapter 4. Only information concerning programming of the associated settings is covered here. These features are provided for two breakers; a user may use only those portions of the design relevant to a single breaker, which must be Breaker 1.

- **BREAKER 1(2) FUNCTION:** Set to "Enable" to allow the operation of any breaker control feature.
- **BREAKER1(2) PUSH BUTTON CONTROL:** Set to "Enable" to allow faceplate push button operations.
- **BREAKER 1(2) NAME:** Assign a user-defined name (up to 6 characters) to the breaker. This name will be used in flash messages related to Breaker 1.
- **BREAKER 1(2) MODE:** Selects "3-pole" mode, where all breaker poles are operated simultaneously, or "1-pole" mode where all breaker poles are operated either independently or simultaneously.
- **BREAKER 1(2) OPEN:** Selects an operand that creates a programmable signal to operate an output relay to open Breaker No. 1.
- **BREAKER 1(2) CLOSE:** Selects an operand that creates a programmable signal to operate an output relay to close Breaker No. 1.
- **BREAKER 1(2) ϕ A/3-POLE:** Selects an operand, usually a contact input connected to a breaker auxiliary position tracking mechanism. This input can be either a 52/a or 52/b contact, or a combination the 52/a and 52/b contacts, that

must be programmed to create a logic 0 when the breaker is open. If **BREAKER 1 MODE** is selected as "3-Pole", this setting selects a single input as the operand used to track the breaker open or closed position. If the mode is selected as "1-Pole", the input mentioned above is used to track phase A and settings **BREAKER 1 Φ B** and **BREAKER 1 Φ C** select operands to track phases B and C, respectively.

- **BREAKER 1(2) FB:** If the mode is selected as 3-pole, this setting has no function. If the mode is selected as 1-pole, this input is used to track phase B as above for phase A.
- **BREAKER 1(2) FC:** If the mode is selected as 3-pole, this setting has no function. If the mode is selected as 1-pole, this input is used to track phase C as above for phase A.
- **BREAKER 1(2) EXT ALARM:** Selects an operand, usually an external contact input, connected to a breaker alarm reporting contact.
- **BREAKER 1(2) ALARM DELAY:** Sets the delay interval during which a disagreement of status among the three pole position tracking operands will not declare a pole disagreement, to allow for non-simultaneous operation of the poles.
- **MANUAL CLOSE RECAL1 TIME:** Sets the interval required to maintain setting changes in effect after an operator has initiated a manual close command to operate a circuit breaker.
- **BREAKER 1(2) OUT OF SV:** Selects an operand indicating that Breaker 1(2) is out-of-service.
- **XCBR ST.LOC OPERAND:** Selects a FlexLogic™ operand to provide a value for the IEC 61850 XCBR1(2) St.Loc data item.

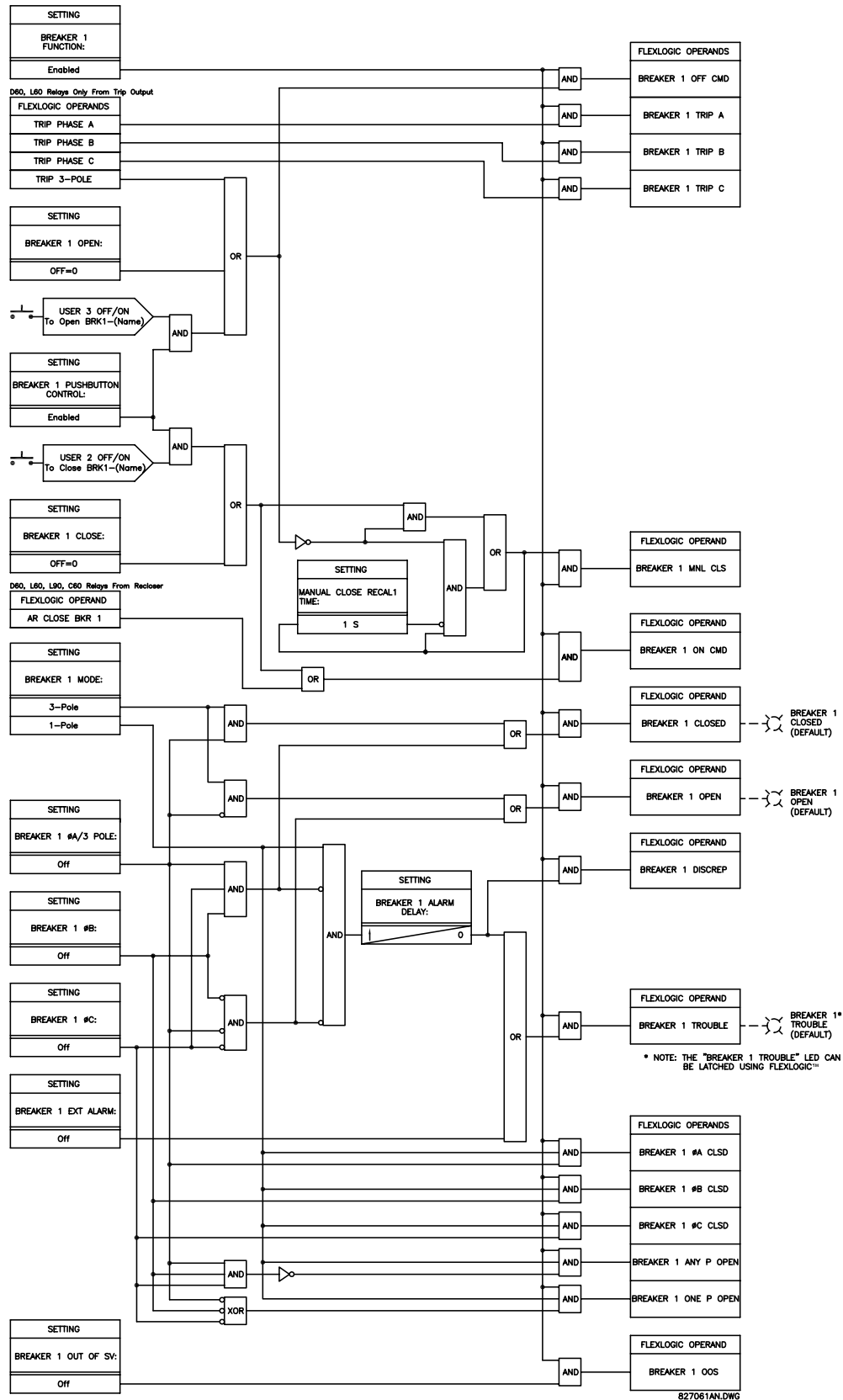


Figure 5-15: DUAL BREAKER CONTROL SCHEME LOGIC

5.3.5 PHASOR MEASUREMENT UNIT

a) MAIN MENU

PATH: SETTINGS ⇨⇩ SYSTEM SETUP ⇨⇩ PHASOR MEASUREMENT UNIT

■ PHASOR MEASUREMENT ■ UNIT	◀▶	■ PHASOR MEASUREMENT ■ UNIT 1	See below.
MESSAGE	▲▼	■ PHASOR MEASUREMENT ■ UNIT 2	See below.
MESSAGE	▲▼	■ PHASOR MEASUREMENT ■ UNIT 3	See below.
MESSAGE	▲▼	■ PHASOR MEASUREMENT ■ UNIT 4	See below.
MESSAGE	▲	■ REPORTING OVER ■ NETWORK	See page 5-66.

The **PHASOR MEASUREMENT UNIT 1** through **4** menus allow specifying basic parameters of the measurements process such as signal source, ID and station name, calibration data, triggering, recording, and content for transmission on each of the supported ports. The reporting ports menus allow specifying the content and rate of reporting on each of the supported ports. The PMU settings are organized in five logical groups as follows:

PATH: SETTINGS ⇨⇩ SYSTEM SETUP ⇨⇩ PHASOR MEASUREMENT UNIT ⇨⇩ PHASOR MEASUREMENT UNIT 1(4)

■ PHASOR MEASUREMENT ■ UNIT 1	◀▶	■ PMU 1 BASIC ■ CONFIGURATION	See page 5-52.
MESSAGE	▲▼	■ PMU 1 ■ CALIBRATION	See page 5-53.
MESSAGE	▲▼	■ PMU 1 ■ COMMUNICATION	See page 5-54.
MESSAGE	▲▼	■ PMU 1 ■ TRIGGERING	See page 5-56.
MESSAGE	▲	■ PMU 1 ■ RECORDING	See page 5-63.

b) BASIC CONFIGURATION

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR... ⇒ PHASOR MEASUREMENT UNIT 1(4) ⇒ PMU 1 BASIC CONFIGURATION

PMU 1 BASIC CONFIGURATION	PMU 1 FUNCTION: Disabled	Range: Enabled, Disabled
MESSAGE	PMU 1 IDCODE: 1	Range: 1 to 65534 in steps of 1
MESSAGE	PMU 1 STN: GE-UR-PMU	Range: 16 alphanumeric characters
MESSAGE	PMU 1 SIGNAL SOURCE: SRC 1	Range: SRC 1, SRC 2, SRC 3, SRC 4, SRC 5, SRC 6
MESSAGE	PMU 1 POST-FILTER: Symm-3-point	Range: available post-filters as per table below

This section contains basic data such as functions, source settings, and names.

- **PMU 1 FUNCTION:** This setting enables the PMU 1 functionality. Any associated functions (such as the recorder or triggering comparators) will not function if this setting is “Disabled”. Use the command frame to force the communication portion of the feature to start/stop transmission of data. When the transmission is turned off, the PMU is fully operational in terms of calculating and recording the phasors.
- **PMU 1 IDCODE:** This setting assigns a numerical ID to the PMU. It corresponds to the IDCODE field of the data, configuration, header, and command frames of the C37.118 protocol. The PMU uses this value when sending data, configuration, and header frames and responds to this value when receiving the command frame.
- **PMU 1 STN:** This setting assigns an alphanumeric ID to the PMU station. It corresponds to the STN field of the configuration frame of the C37.118 protocol. This value is a 16-character ASCII string as per the C37.118 standard.
- **PMU 1 SIGNAL SOURCE:** This setting specifies one of the available N60 signal sources for processing in the PMU. Note that any combination of voltages and currents can be configured as a source. The current channels could be configured as sums of physically connected currents. This facilitates PMU applications in breaker-and-a-half, ring-bus, and similar arrangements. The PMU feature calculates voltage phasors for actual voltage (A, B, C, and auxiliary) and current (A, B, C, and ground) channels of the source, as well as symmetrical components (0, 1, and 2) of both voltages and currents. When configuring communication and recording features of the PMU, the user could select – from the above superset – the content to be sent out or recorded.
- **PMU 1 POST-FILTER:** This setting specifies amount of post-filtering applied to raw synchrophasor measurements. The raw measurements are produced at the rate of nominal system frequency using one-cycle data windows. This setting is provided to deal with interfering frequencies and to balance speed and accuracy of synchrophasor measurements for different applications. The following filtering choices are available:

Table 5–4: POST-FILTER CHOICES

SELECTION	CHARACTERISTIC OF THE FILTER
None	No post-filtering
Symm-3-point	Symmetrical 3-point filter (1 historical point, 1 present point, 1 future point)
Symm-5-point	Symmetrical 5-point filter (2 historical points, 1 present point, 2 future points)
Symm-7-point	Symmetrical 7-point filter (3 historical points, 1 present point, 3 future points)

This setting applies to all channels of the PMU. It is effectively for recording and transmission on all ports configured to use data of this PMU.

c) CALIBRATION

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR... ⇒ PHASOR MEASUREMENT UNIT 1(4) ⇒ PMU 1 CALIBRATION

■ PMU 1 ■ CALIBRATION			PMU 1 VA CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 VB CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 VC CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 VX CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 IA CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 IB CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 IC CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 IG CALIBRATION	Range: -5.00 to 5.00° in steps of 0.05
			ANGLE: 0.00°	
MESSAGE			PMU 1 SEQ VOLT SHIFT	Range: -180 to 180° in steps of 30
			ANGLE: 0°	
MESSAGE			PMU 1 SEQ CURR SHIFT	Range: -180 to 180° in steps of 30
			ANGLE: 0°	

This menu contains user angle calibration data. This data is combined with the factory adjustments to shift the phasors for better accuracy.

- **PMU 1 VA... IG CALIBRATION ANGLE:** These settings recognize applications with protection class voltage and current sources, and allow the user to calibrate each channel (four voltages and four currents) individually to offset errors introduced by VTs, CTs, and cabling. The setting values are effectively added to the measured angles. Therefore, enter a positive correction of the secondary signal lags the true signal; and negative value if the secondary signal leads the true signal.
- **PMU 1 SEQ VOLT SHIFT ANGLE:** This setting allows correcting positive- and negative-sequence voltages for vector groups of power transformers located between the PMU voltage point, and the reference node. This angle is effectively added to the positive-sequence voltage angle, and subtracted from the negative-sequence voltage angle. Note that:
 1. When this setting is not "0°", the phase and sequence voltages will not agree. Unlike sequence voltages, the phase voltages cannot be corrected in a general case, and therefore are reported as measured.
 2. When receiving synchrophasor data at multiple locations, with possibly different reference nodes, it may be more beneficial to allow the central locations to perform the compensation of sequence voltages.
 3. This setting applies to PMU data only. The N60 calculates symmetrical voltages independently for protection and control purposes without applying this correction.
 4. When connected to line-to-line voltages, the PMU calculates symmetrical voltages with the reference to the AG voltage, and not to the physically connected AB voltage (see the *Metering Conventions* section in Chapter 6).
- **PMU 1 SEQ CURR SHIFT ANGLE:** This setting allows correcting positive and negative-sequence currents for vector groups of power transformers located between the PMU current point and the reference node. The setting has the same meaning for currents as the **PMU 1 SEQ VOLT SHIFT ANGLE** setting has for voltages. Normally, the two correcting angles are set identically, except rare applications when the voltage and current measuring points are located at different windings of a power transformer.

d) PMU COMMUNICATION

PATH: SETTINGS ⇨ ↓ SYSTEM SETUP ⇨ ↓ PHASOR MEASUREMENT... ⇨ ↓ PMU 1 COMMUNICATION ⇨ ↓ PMU 1 COMM PORT 1(3)

PMU 1	PMU1 COMM PORT 1:	None	Range: None, Network
COMM PORT 1	PMU1 PORT 1 PHS-1	PMU 1 V1	Range: available synchrophasor values
MESSAGE	PMU1 PORT 1 PHS-1	NM: GE-UR-PMU1-V1	Range: 16-character ASCII string
		↓	
MESSAGE	PMU1 PORT 1 PHS-14	PMU 1 V1	Range: available synchrophasor values
MESSAGE	PMU1 PORT 1 PHS-14	NM: GE-UR-PMU1-V1	Range: 16 alphanumeric characters
MESSAGE	PMU1 PORT 1 A-CH-1:	Off	Range: available FlexAnalog values
MESSAGE	PMU1 PORT 1 A-CH-1	NM: AnalogChannel1	Range: 16 alphanumeric characters
		↓	
MESSAGE	PMU1 PORT 1 A-CH-8:	Off	Range: available FlexAnalog values
MESSAGE	PMU1 PORT 1 A-CH-8	NM: AnalogChannel8	Range: 16 alphanumeric characters
MESSAGE	PMU1 PORT 1 D-CH-1:	Off	Range: FlexLogic™ operands
MESSAGE	PMU1 PORT 1 D-CH-1	NM: DigitalChannel1	Range: 16 alphanumeric characters
MESSAGE	PMU1 PORT 1 D-CH-1	NORMAL STATE: Off	Range: On, Off
		↓	
MESSAGE	PMU1 PORT 1 D-CH-16:	Off	Range: FlexLogic™ operands
MESSAGE	PMU1 PORT 1 D-CH-16	NM: DigitalChannel16	Range: 16 alphanumeric characters
MESSAGE	PMU1 PORT 1 D-CH-16	NORMAL STATE: Off	Range: On, Off

This section contains configuration of the communication portion of the feature. Three identical menus are available to configure independent transmission on ports 1, 2 and 3. Multiple transmissions can be used for redundancy or to serve similar data to multiple different recipients.

- **PMU1 COMM PORT 1:** This setting specifies the first communication port for transmission of the PMU data. The four ports are configured under individual menus.

- **PMU1 PORT 1 PHS-1(14):** These settings specify synchrophasors to be transmitted from the superset of all synchronized measurements. The available synchrophasor values are tabulated below.

SELECTION	MEANING
Va	First voltage channel, either Va or Vab
Vb	Second voltage channel, either Vb or Vbc
Vc	Third voltage channel, either Vc or Vca
Vx	Fourth voltage channel
Ia	Phase A current, physical channel or summation as per the source settings
Ib	Phase B current, physical channel or summation as per the source settings
Ic	Phase C current, physical channel or summation as per the source settings
Ig	Fourth current channel, physical or summation as per the source settings
V1	Positive-sequence voltage, referenced to Va
V2	Negative-sequence voltage, referenced to Va
V0	Zero-sequence voltage
I1	Positive-sequence current, referenced to Ia
I2	Negative-sequence current, referenced to Ia
I0	Zero-sequence current

These settings allow for optimizing the frame size and maximizing transmission channel usage, depending on a given application. Select “Off” to suppress transmission of a given value.

- **PMU1 PORT 1 PHS-1(14) NM:** These settings allow for custom naming of the synchrophasor channels. Sixteen-character ASCII strings are allowed as in the CHNAM field of the configuration frame. These names are typically based on station, bus, or breaker names.
- **PMU1 PORT 1 A-CH-1(8):** These settings specify any analog data measured by the relay to be included as a user-selectable analog channel of the data frame. Up to eight analog channels can be configured to send any FlexAnalog value from the relay. Examples include active and reactive power, per phase or three-phase power, power factor, temperature via RTD inputs, and THD. The configured analog values are sampled concurrently with the synchrophasor instant and sent as 16-bit integer values.
- **PMU1 PORT 1 A-CH-1(8) NM:** These settings allow for custom naming of the analog channels. Sixteen-character ASCII strings are allowed as in the CHNAM field of the configuration frame.
- **PMU1 PORT 1 D-CH-1(16):** These settings specify any digital flag measured by the relay to be included as a user-selectable digital channel of the data frame. Up to sixteen analog channels can be configured to send any FlexLogic™ operand from the relay. The configured digital flags are sampled concurrently with the synchrophasor instant. The values are mapped into a 2-byte integer number, with byte 1 LSB corresponding to the digital channel 1 and byte 2 MSB corresponding to digital channel 16.
- **PMU1 PORT 1 D-CH-1(16) NM:** These settings allow for custom naming of the digital channels. Sixteen-character ASCII strings are allowed as in the CHNAM field of the configuration frame.
- **PMU1 PORT 1 D-CH-1(16) NORMAL STATE:** These settings allow for specifying a “normal state” for each digital channel. These states are transmitted in configuration frames to the data concentrator.

e) PMU TRIGGERING OVERVIEW

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR... ⇒ PHASOR MEASUREMENT UNIT 1(4) ⇒ PMU 1 TRIGGERING

<div>PMU 1</div> <div>TRIGGERING</div>	<div>PMU1 USER TRIGGER: Off</div>	Range: FlexLogic™ operands
MESSAGE	<div>PMU 1 FREQUENCY</div> <div>TRIGGER</div>	See page 5-57.
MESSAGE	<div>PMU 1 VOLTAGE</div> <div>TRIGGER</div>	See page 5-58.
MESSAGE	<div>PMU 1 CURRENT</div> <div>TRIGGER</div>	See page 5-59.
MESSAGE	<div>PMU 1 POWER</div> <div>TRIGGER</div>	See page 5-60.
MESSAGE	<div>PMU 1 df/dt</div> <div>TRIGGER</div>	See page 5-61.

Each logical PMU contains five triggering mechanisms to facilitate triggering of the associated PMU recorder, and/or cross-triggering of other PMUs of the system. They are:

- overfrequency and underfrequency
- overvoltage and undervoltage
- overcurrent
- overpower
- high rate of change of frequency

The pre-configured triggers could be augmented with a user-specified condition built freely using programmable logic of the relay. The entire triggering logic is refreshed once every two power system cycles.

All five triggering functions and the user-definable condition are consolidated (ORed) and connected to the PMU recorder. Each trigger can be programmed to log its operation into the event recorder, and to signal its operation via targets. The five triggers drive the STAT bits of the data frame to inform the destination of the synchrophasor data regarding the cause of trigger. The following convention is adopted to drive bits 11, 3, 2, 1, and 0 of the STAT word.

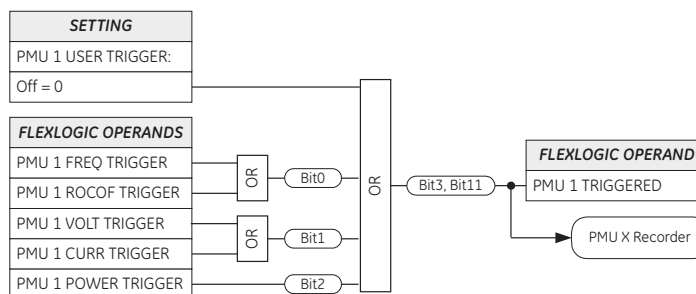


Figure 5-16: STAT BITS LOGIC

f) FREQUENCY TRIGGERING

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR MEASUREMENT... ⇒ PMU 1 TRIGGERING ⇒ PMU 1 FREQUENCY TRIGGER

PMU 1 FREQUENCY TRIGGER	PMU 1 FREQ TRIGGER FUNCTION: Disabled	Range: Enabled, Disabled
MESSAGE	PMU 1 FREQ TRIGGER LOW-FREQ: 49.00 Hz	Range: 20.00 to 70.00 Hz in steps of 0.01
MESSAGE	PMU 1 FREQ TRIGGER HIGH-FREQ: 61.00 Hz	Range: 20.00 to 70.00 Hz in steps of 0.01
MESSAGE	PMU 1 FREQ TRIGGER PKP TIME: 0.10 s	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	PMU 1 FREQ TRIGGER DPO TIME: 1.00 s	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	PMU 1 FREQ TRIG BLK: Off	Range: FlexLogic™ operand
MESSAGE	PMU 1 FREQ TRIGGER TARGET: Self-Reset	Range: Self-Reset, Latched, Disabled
MESSAGE	PMU 1 FREQ TRIGGER EVENTS: Disabled	Range: Enabled, Disabled

The trigger responds to the frequency signal of the PMU source. The frequency is calculated from either phase voltages, auxiliary voltage, phase currents and ground current, in this hierarchy, depending on the source configuration as per N60 standards. This element requires the frequency is above the minimum measurable value. If the frequency is below this value, such as when the circuit is de-energized, the trigger will drop out.

- **PMU 1 FREQ TRIGGER LOW-FREQ:** This setting specifies the low threshold for the abnormal frequency trigger. The comparator applies a 0.03 Hz hysteresis.
- **PMU 1 FREQ TRIGGER HIGH-FREQ:** This setting specifies the high threshold for the abnormal frequency trigger. The comparator applies a 0.03 Hz hysteresis.
- **PMU 1 FREQ TRIGGER PKP TIME:** This setting could be used to filter out spurious conditions and avoid unnecessary triggering of the recorder.
- **PMU 1 FREQ TRIGGER DPO TIME:** This setting could be used to extend the trigger after the situation returned to normal. This setting is of particular importance when using the recorder in the forced mode (recording as long as the triggering condition is asserted).

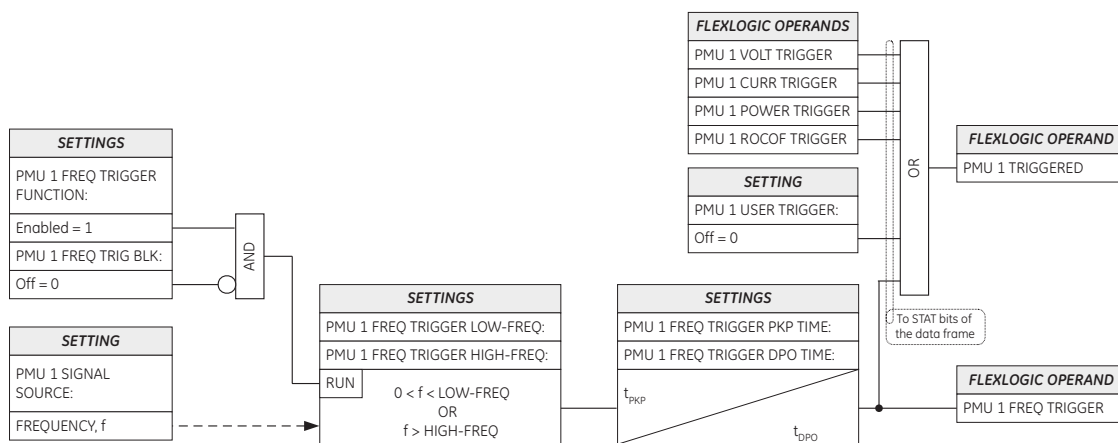


Figure 5-17: FREQUENCY TRIGGER SCHEME LOGIC

g) VOLTAGE TRIGGERING

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR MEASUREMENT... ⇒ PMU 1 TRIGGERING ⇒ PMU 1 VOLTAGE TRIGGER

PMU 1 VOLTAGE	PMU 1 VOLT TRIGGER	Range: Enabled, Disabled
TRIGGER	FUNCTION: Disabled	
MESSAGE	PMU 1 VOLT TRIGGER LOW-VOLT: 0.800 pu	Range: 0.250 to 1.250 pu in steps of 0.001
MESSAGE	PMU 1 VOLT TRIGGER HIGH-VOLT: 1.200 pu	Range: 0.250 to 1.250 pu in steps of 0.001
MESSAGE	PMU 1 VOLT TRIGGER PKP TIME: 0.10 s	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	PMU 1 VOLT TRIGGER DPO TIME: 1.00 s	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	PMU 1 VOLT TRIG BLK: Off	Range: FlexLogic™ operand
MESSAGE	PMU 1 VOLT TRIGGER TARGET: Self-Reset	Range: Self-Reset, Latched, Disabled
MESSAGE	PMU 1 VOLT TRIGGER EVENTS: Disabled	Range: Enabled, Disabled

This element responds to abnormal voltage. Separate thresholds are provided for low and high voltage. In terms of signaling its operation, the element does not differentiate between the undervoltage and overvoltage events. The trigger responds to the phase voltage signal of the PMU source. All voltage channel (A, B, and C or AB, BC, and CA) are processed independently and could trigger the recorder. A minimum voltage supervision of 0.1 pu is implemented to prevent pickup on a de-energized circuit, similarly to the undervoltage protection element.

- **PMU 1 VOLT TRIGGER LOW-VOLT:** This setting specifies the low threshold for the abnormal voltage trigger, in per-unit of the PMU source. 1 pu is a nominal voltage value defined as the nominal secondary voltage times VT ratio. The comparator applies a 3% hysteresis.
- **PMU 1 VOLT TRIGGER HIGH-VOLT:** This setting specifies the high threshold for the abnormal voltage trigger, in per-unit of the PMU source. 1 pu is a nominal voltage value defined as the nominal secondary voltage times VT ratio. The comparator applies a 3% hysteresis.
- **PMU 1 VOLT TRIGGER PKP TIME:** This setting could be used to filter out spurious conditions and avoid unnecessary triggering of the recorder.
- **PMU 1 VOLT TRIGGER DPO TIME:** This setting could be used to extend the trigger after the situation returned to normal. This setting is of particular importance when using the recorder in the forced mode (recording as long as the triggering condition is asserted).

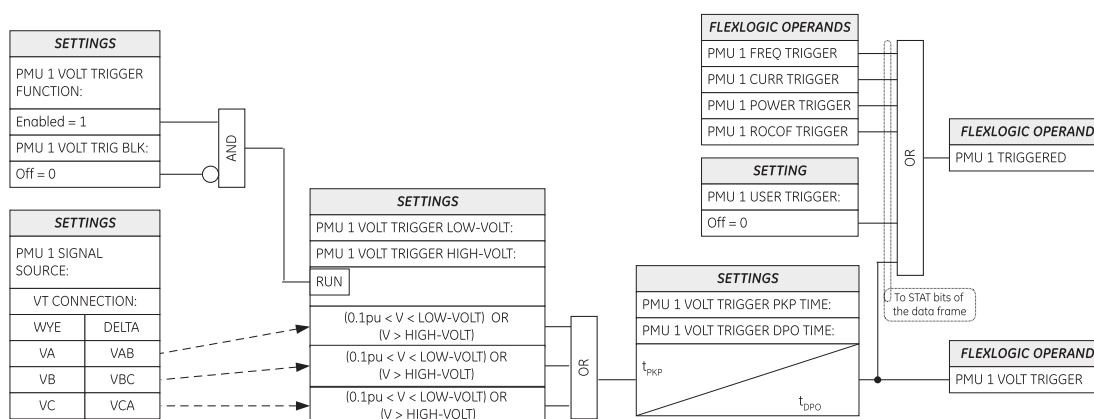


Figure 5-18: VOLTAGE TRIGGER SCHEME LOGIC

h) CURRENT TRIGGERING

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR MEASUREMENT... ⇒ PMU 1 TRIGGERING ⇒ PMU 1 CURRENT TRIGGER

<div> <div>PMU 1 CURRENT</div> <div>TRIGGER</div> </div>	<div> <div>PMU 1 CURR TRIGGER</div> <div>FUNCTION: Disabled</div> </div>	Range: Enabled, Disabled
MESSAGE	<div> <div>PMU 1 CURR TRIGGER</div> <div>PICKUP: 1.800 pu</div> </div>	Range: 0.100 to 30.000 pu in steps of 0.001
MESSAGE	<div> <div>PMU 1 CURR TRIGGER</div> <div>PKP TIME: 0.10 s</div> </div>	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	<div> <div>PMU 1 CURR TRIGGER</div> <div>DPO TIME: 1.00 s</div> </div>	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	<div> <div>PMU 1 CURR TRIG BLK:</div> <div>Off</div> </div>	Range: FlexLogic™ operand
MESSAGE	<div> <div>PMU 1 CURR TRIGGER</div> <div>TARGET: Self-Reset</div> </div>	Range: Self-Reset, Latched, Disabled
MESSAGE	<div> <div>PMU 1 CURR TRIGGER</div> <div>EVENTS: Disabled</div> </div>	Range: Enabled, Disabled

This element responds to elevated current. The trigger responds to the phase current signal of the PMU source. All current channel (A, B, and C) are processed independently and could trigger the recorder.

- **PMU 1 CURR TRIGGER PICKUP:** This setting specifies the pickup threshold for the overcurrent trigger, in per unit of the PMU source. A value of 1 pu is a nominal primary current. The comparator applies a 3% hysteresis.
- **PMU 1 CURR TRIGGER PKP TIME:** This setting could be used to filter out spurious conditions and avoid unnecessary triggering of the recorder.
- **PMU 1 CURR TRIGGER DPO TIME:** This setting could be used to extend the trigger after the situation returned to normal. This setting is of particular importance when using the recorder in the forced mode (recording as long as the triggering condition is asserted).

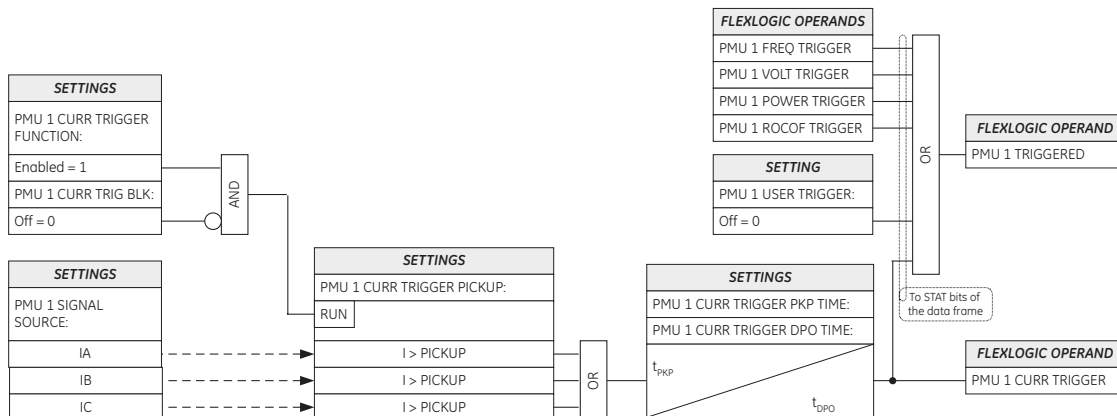


Figure 5-19: CURRENT TRIGGER SCHEME LOGIC

i) POWER TRIGGERING

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR MEASUREMENT... ⇒ PMU 1 TRIGGERING ⇒ PMU 1 POWER TRIGGER

<div> <div>PMU 1 POWER</div> <div>PMU 1 TRIGGER</div> </div>		PMU 1 POWER TRIGGER FUNCTION: Disabled	Range: Enabled, Disabled
	MESSAGE	PMU 1 POWER TRIGGER ACTIVE: 1.250 pu	Range: 0.250 to 3.000 pu in steps of 0.001
	MESSAGE	PMU 1 POWER TRIGGER REACTIVE: 1.250 pu	Range: 0.250 to 3.000 pu in steps of 0.001
	MESSAGE	PMU 1 POWER TRIGGER APPARENT: 1.250 pu	Range: 0.250 to 3.000 pu in steps of 0.001
	MESSAGE	PMU 1 POWER TRIGGER PKP TIME: 0.10 s	Range: 0.00 to 600.00 s in steps of 0.01
	MESSAGE	PMU 1 POWER TRIGGER DPO TIME: 1.00 s	Range: 0.00 to 600.00 s in steps of 0.01
	MESSAGE	PMU 1 PWR TRIG BLK: Off	Range: FlexLogic™ operand
	MESSAGE	PMU 1 POWER TRIGGER TARGET: Self-Reset	Range: Self-Reset, Latched, Disabled
	MESSAGE	PMU 1 POWER TRIGGER EVENTS: Disabled	Range: Enabled, Disabled

5

This element responds to abnormal power. Separate thresholds are provided for active, reactive, and apparent powers. In terms of signaling its operation the element does not differentiate between the three types of power. The trigger responds to the single-phase and three-phase power signals of the PMU source.

- **PMU 1 POWER TRIGGER ACTIVE:** This setting specifies the pickup threshold for the active power of the source. For single-phase power, 1 pu is a product of 1 pu voltage and 1 pu current, or the product of nominal secondary voltage, the VT ratio and the nominal primary current. For the three-phase power, 1 pu is three times that for a single-phase power. The comparator applies a 3% hysteresis.
- **PMU 1 POWER TRIGGER REACTIVE:** This setting specifies the pickup threshold for the reactive power of the source. For single-phase power, 1 pu is a product of 1 pu voltage and 1 pu current, or the product of nominal secondary voltage, the VT ratio and the nominal primary current. For the three-phase power, 1 pu is three times that for a single-phase power. The comparator applies a 3% hysteresis.
- **PMU 1 POWER TRIGGER APPARENT:** This setting specifies the pickup threshold for the apparent power of the source. For single-phase power, 1 pu is a product of 1 pu voltage and 1 pu current, or the product of nominal secondary voltage, the VT ratio and the nominal primary current. For the three-phase power, 1 pu is three times that for a single-phase power. The comparator applies a 3% hysteresis.
- **PMU 1 POWER TRIGGER PKP TIME:** This setting could be used to filter out spurious conditions and avoid unnecessary triggering of the recorder.
- **PMU 1 POWER TRIGGER DPO TIME:** This setting could be used to extend the trigger after the situation returned to normal. This setting is of particular importance when using the recorder in the forced mode (recording as long as the triggering condition is asserted).

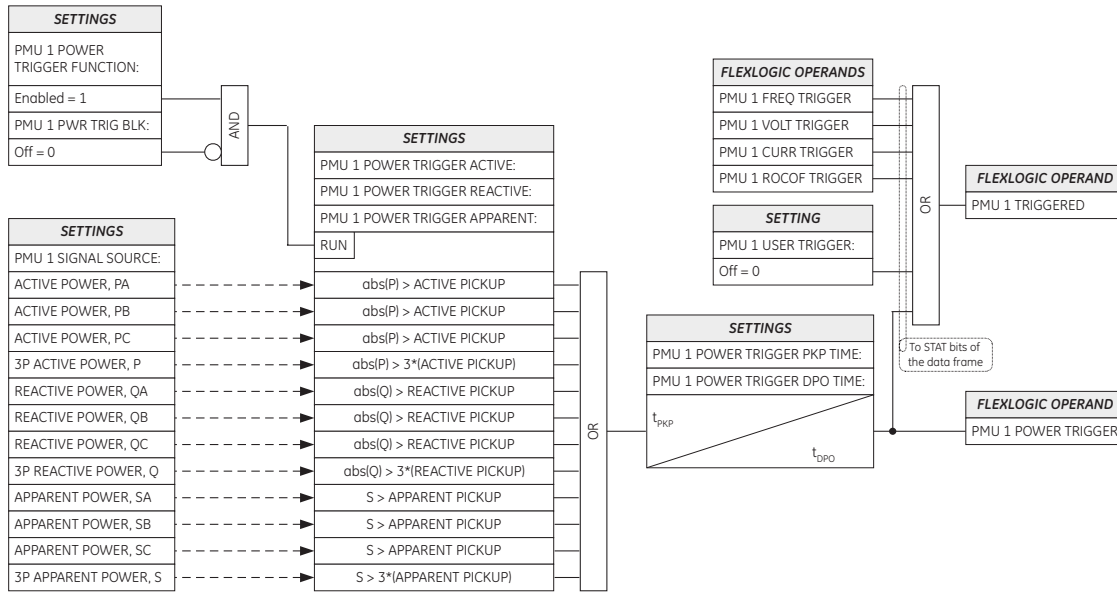


Figure 5-20: POWER TRIGGER SCHEME LOGIC

j) DF/DT TRIGGERING

PATH: SETTINGS ⇌ SYSTEM SETUP ⇌ PHASOR MEASUREMENT... ⇌ PMU 1 TRIGGERING ⇌ PMU 1 df/dt TRIGGER

■ PMU 1 df/dt TRIGGER		PMU 1 df/dt TRIGGER FUNCTION: Disabled	Range: Enabled, Disabled
MESSAGE	▲▼	PMU 1 df/dt TRIGGER RAISE: 0.25 Hz/s	Range: 0.10 to 15.00 Hz/s in steps of 0.01
MESSAGE	▲▼	PMU 1 df/dt TRIGGER FALL: 0.25 Hz/s	Range: 0.10 to 15.00 Hz/s in steps of 0.01
MESSAGE	▲▼	PMU 1 df/dt TRIGGER PKP TIME: 0.10 s	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	▲▼	PMU 1 df/dt TRIGGER DPO TIME: 1.00 s	Range: 0.00 to 600.00 s in steps of 0.01
MESSAGE	▲▼	PMU 1 df/dt TRG BLK: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	PMU 1 df/dt TRIGGER TARGET: Self-Reset	Range: Self-Reset, Latched, Disabled
MESSAGE	▲	PMU 1 df/dt TRIGGER EVENTS: Disabled	Range: Enabled, Disabled

This element responds to frequency rate of change. Separate thresholds are provided for rising and dropping frequency. The trigger responds to the rate of change of frequency (df/dt) of the PMU source.

- **PMU 1 df/dt TRIGGER RAISE:** This setting specifies the pickup threshold for the rate of change of frequency in the raising direction (positive df/dt). The comparator applies a 3% hysteresis.
- **PMU 1 df/dt TRIGGER FALL:** This setting specifies the pickup threshold for the rate of change of frequency in the falling direction (negative df/dt). The comparator applies a 3% hysteresis.
- **PMU 1 df/dt TRIGGER PKP TIME:** This setting could be used to filter out spurious conditions and avoid unnecessary triggering of the recorder.

- PMU 1 df/dt TRIGGER DPO TIME:** This setting could be used to extend the trigger after the situation returned to normal. This setting is of particular importance when using the recorder in the forced mode (recording as long as the triggering condition is asserted).

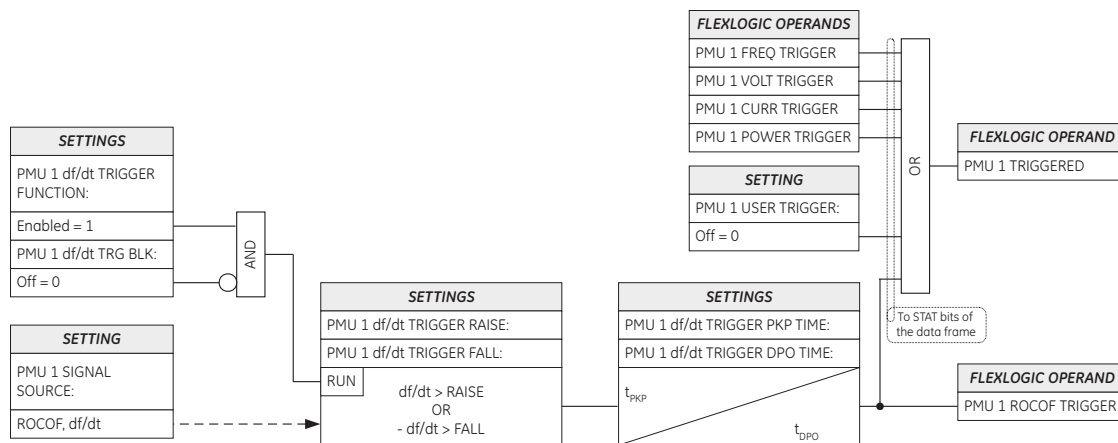


Figure 5-21: RATE OF CHANGE OF FREQUENCY TRIGGER SCHEME LOGIC

k) PMU RECORDING

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR... ⇒ PHASOR MEASUREMENT UNIT 1(4) ⇒ PMU 1 RECORDING

PMU 1 RECORDING	PMU 1 RECORDING RATE: 10/sec	Range: 1, 2, 5, 10, 12, 15, 20, 25, 30, 50, or 60 times per second
MESSAGE	PMU 1 TRIGGERING STYLE: Timed	Range: Timed, Forced. Note: the Forced option is not available with this firmware revision.
MESSAGE	PMU 1 NO OF TIMED RECORDS: 3	Range: 1 to 128 in steps of 1
MESSAGE	PMU 1 TRIGGER MODE: Automatic Overwrite	Range: Automatic Overwrite, Protected
MESSAGE	PMU 1 TIMED TRIGGER POSITION: 10%	Range: 1 to 50% in steps of 1
MESSAGE	PMU 1 REC PHS-1: PMU 1 V1	Range: available synchrophasor values
MESSAGE	PMU 1 REC PHS-1 NM: GE-UR-PMU-V1	Range: 16 character ASCII string
↓		
MESSAGE	PMU 1 REC PHS-14: Off	Range: available synchrophasor values
MESSAGE	PMU 1 REC PHS-14 NM: GE-UR-PMU-PHS-14	Range: 16 character ASCII string
MESSAGE	PMU 1 REC A-CH-1: Off	Range: available FlexAnalog values
MESSAGE	PMU 1 REC A-CH-1 NM: AnalogChannel1	Range: 16 character ASCII string
↓		
MESSAGE	PMU 1 REC A-CH-8: Off	Range: available FlexAnalog values
MESSAGE	PMU 1 REC A-CH-8 NM: AnalogChannel8	Range: 16 character ASCII string
MESSAGE	PMU 1 REC D-CH-1: Off	Range: FlexLogic™ operand
MESSAGE	PMU 1 REC D-CH-1 NM: DigitalChannel1	Range: 16 character ASCII string
↓		
MESSAGE	PMU 1 REC D-CH-16: Off	Range: FlexLogic™ operand
MESSAGE	PMU 1 REC D-CH-16 NM: DigitalChannel16	Range: 16 character ASCII string

Each logical PMU is associated with a recorder. The triggering condition is programmed via the **PMU 1 TRIGGERING** menu. The recorder works with polar values using resolution as in the PMU actual values.

The PMU recorder is cleared by any settings change affecting the PMU recorder. For example, changing the **NOMINAL FREQUENCY**, **PMU 1(4) RECORDING**, and **PMU 1(4) BASIC CONFIGURATION** settings will clear PMU recorder. The PMU records are also cleared when power is cycled to the relay.

- **PMU 1 RECORDING RATE:** This setting specifies the recording rate for the record content. Not all recording rates are applicable to either 50 or 60 Hz systems (for example, recording at 25 phasors a second in a 60 Hz system). The relay supports decimation by integer number of phasors from the nominal system frequency. If the rate of 25 is selected for the 60 Hz system, the relay would decimate the rate of 60 phasors a second by round $(60 / 25) = 2$; that is, it would record at $60 / 2 = 30$ phasors a second.
- **PMU 1 TRIGGERING STYLE:** This setting allows controlling the recording as either “Timed” or “Forced”. If set to “Timed”, the rising edge of the triggering flag starts the recording. All subsequent triggers are ignored until the recording-in-progress is finished. Note that in this mode the recorder responds to a rising edge – to trigger again, the triggering flag needs to reset, and pickup again. If set to “Forced”, the recorder keeps recording as long as the triggering flag is asserted. The following figure illustrates the difference.

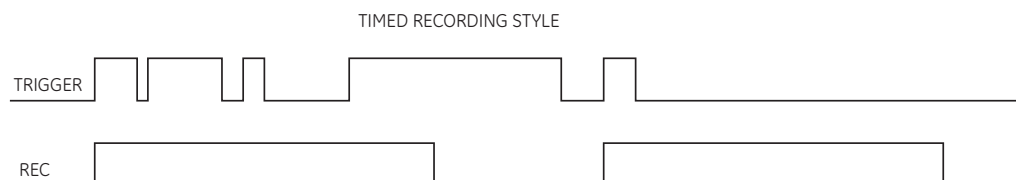


Figure 5–22: TIMED RECORDING STYLE

- **PMU 1 NO OF TIMED RECORDS:** This setting specifies how many timed records are available for a given logical PMU. The length of each record equals available memory divided by the content size and number of records. The higher the number of records, the shorter each record. The relay supports a maximum of 128 records.
- **PMU 1 TRIGGER MODE:** This setting specifies what happens when the recorder uses its entire available memory storage. If set to “Automatic Overwrite”, the last record is erased to facilitate new recording, when triggered.

If set to “Protected”, the recorder stops creating new records when the entire memory is used up by the old un-cleared records. Refer to Chapter 7 for more information on clearing PMU records. In such a case, the IED will stop recording, even if set to the “Automatic Overwrite” mode.

The following set of figures illustrate the concept of memory management via the **PMU 1 TRIGGERING STYLE** and **PMU 1 TRIGGER MODE** settings.

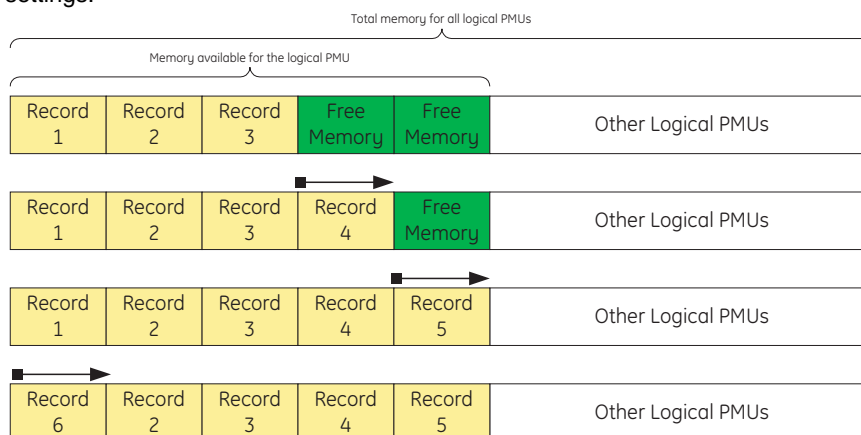


Figure 5–23: “TIMED” RECORDING STYLE AND “AUTOMATIC OVERWRITE” MODE

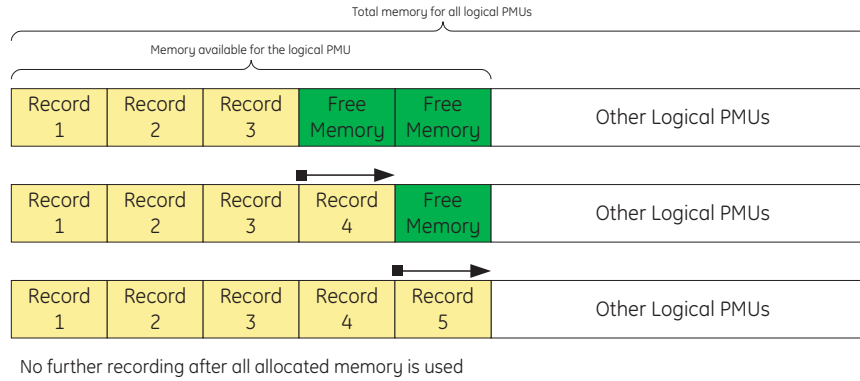


Figure 5–24: “TIMED” RECORDING STYLE AND “PROTECTED” MODE

- **PMU 1 TIMED TRIGGER POSITION:** This setting specifies the amount of pre-trigger data in percent of the entire record.
- **PMU1 PORT 1 PHS-1(14):** These settings specify synchrophasors to be recorded from the superset of all synchronized measurements as indicated in the following table. These settings allow for optimizing the record size and content depending on a given application. Select “Off” to suppress recording of a given value.

VALUE	DESCRIPTION
Va	First voltage channel, either Va or Vab
Vb	Second voltage channel, either Vb or Vbc
Vc	Third voltage channel, either Vc or Vca
Vx	Fourth voltage channel
Ia	Phase A current, physical channel or summation as per the source settings
Ib	Phase B current, physical channel or summation as per the source settings
Ic	Phase C current, physical channel or summation as per the source settings
Ig	Fourth current channel, physical or summation as per the source settings
V1	Positive-sequence voltage, referenced to Va
V2	Negative-sequence voltage, referenced to Va
V0	Zero-sequence voltage
I1	Positive-sequence current, referenced to Ia
I2	Negative-sequence current, referenced to Ia
I0	Zero-sequence current

- **PMU 1 REC PHS-1(14) NM:** These settings allow for custom naming of the synchrophasor channels. Sixteen-character ASCII strings are allowed as in the CHNAM field of the configuration frame. Typically these names would be based on station, bus, or breaker names.
- **PMU 1 REC A-CH-1(8):** These settings specify analog data measured by the relay to be included as a user-selectable analog channel of the record. Up to eight analog channels can be configured to record any FlexAnalog value from the relay. Examples include active and reactive power, per phase or three-phase power, power factor, temperature via RTD inputs, and THD. The configured analogs are sampled concurrently with the synchrophasor instant.
- **PMU 1 REC A-CH-1(8) NM:** These settings allow for custom naming of the analog channels. Sixteen-character ASCII strings are allowed as in the CHNAM field of the configuration frame.
- **PMU 1 REC D-CH-1(16):** These settings specify any digital flag measured by the relay to be included as a user-selectable digital channel in the record. Up to sixteen digital channels can be configured to record any FlexLogic™ operand from the relay. The configured digital flags are sampled concurrently with the synchrophasor instant.
- **PMU 1 REC D-CH-1(16) NM:** This setting allows custom naming of the digital channels. Sixteen-character ASCII strings are allowed as in the CHNAM field of the configuration frame.

PATH: SETTINGS ⇒ SYSTEM SETUP ⇒ PHASOR... ⇒ PHASOR MEASUREMENT UNIT 1(4) ⇒ REPORTING OVER NETWORK

<div> <div> <div>■</div> <div>REPORTING OVER</div> </div> <div> <div>■</div> <div>NETWORK</div> </div> </div>	<div> <div> <div>◀</div> <div>▶</div> </div> </div>	<div> <div>NETWORK REPORTING</div> <div>FUNCTION: Disabled</div> </div>	<div>Range: Enabled, Disabled</div>
<div>MESSAGE</div>	<div> <div>▲</div> <div>▼</div> </div>	<div> <div>NETWORK REPORTING</div> <div>IDCODE: 1</div> </div>	<div>Range: 1 to 65534 in steps of 1</div>
<div>MESSAGE</div>	<div> <div>▲</div> <div>▼</div> </div>	<div> <div>NETWORK REPORTING</div> <div>RATE: 10 per sec</div> </div>	<div>Range: 1, 2, 5, 10, 12, 15, 20, 25, 30, 50, or 60 times per second</div>
<div>MESSAGE</div>	<div> <div>▲</div> <div>▼</div> </div>	<div> <div>NETWORK REPORTING</div> <div>STYLE: Polar</div> </div>	<div>Range: Polar, Rectangular</div>
<div>MESSAGE</div>	<div> <div>▲</div> <div>▼</div> </div>	<div> <div>NETWORK REPORTING</div> <div>FORMAT: Integer</div> </div>	<div>Range: Integer, Floating</div>
<div>MESSAGE</div>	<div> <div>▲</div> </div>	<div> <div>NETWORK PDC CONTROL:</div> <div>Disabled</div> </div>	<div>Range: Enabled, Disabled</div>

- **NETWORK REPORTING IDCODE:** This setting specifies an IDCODE for the entire port. Individual PMU streams transmitted over this port are identified via their own IDCODES as per the **PMU IDCODE** settings. This IDCODE is to be used by the command frame to start/stop transmission, and request configuration or header frames.

- **NETWORK REPORTING RATE:** This setting specifies the reporting rate for the network (Ethernet) port. This value applies to all PMU streams of the device that are assigned to transmit over this port.
- **NETWORK REPORTING STYLE:** This setting selects between reporting synchrophasors in rectangular (real and imaginary) or in polar (magnitude and angle) coordinates. This setting complies with bit-0 of the format field of the C37.118 configuration frame.
- **NETWORK REPORTING FORMAT:** This setting selects between reporting synchrophasors as 16-bit integer or 32-bit IEEE floating point numbers. This setting complies with bit 1 of the format field of the C37.118 configuration frame. Note that this setting applies to synchrophasors only – the user-selectable FlexAnalog channels are always transmitted as 16-bit integer values.
- **NETWORK PDC CONTROL:** The synchrophasor standard allows for user-defined controls originating at the PDC, to be executed on the PMU. The control is accomplished via an extended command frame. The relay decodes the first word of the extended field, EXTFRAME, to drive 16 dedicated FlexLogic operands: PDC NETWORK CNTRL 1 (from the least significant bit) to PDC NETWORK CNTRL 16 (from the most significant bit). Other words, if any, in the EXTFRAME are ignored. The operands are asserted for 5 seconds following reception of the command frame. If the new command frame arrives within the 5 second period, the FlexLogic™ operands are updated, and the 5 second timer is re-started.

This setting enables or disables the control. When enabled, all 16 operands are active; when disabled all 16 operands remain reset.

5.4.1 INTRODUCTION TO FLEXLOGIC™

To provide maximum flexibility to the user, the arrangement of internal digital logic combines fixed and user-programmed parameters. Logic upon which individual features are designed is fixed, and all other logic, from digital input signals through elements or combinations of elements to digital outputs, is variable. The user has complete control of all variable logic through FlexLogic™. In general, the system receives analog and digital inputs which it uses to produce analog and digital outputs. The major sub-systems of a generic UR-series relay involved in this process are shown below.

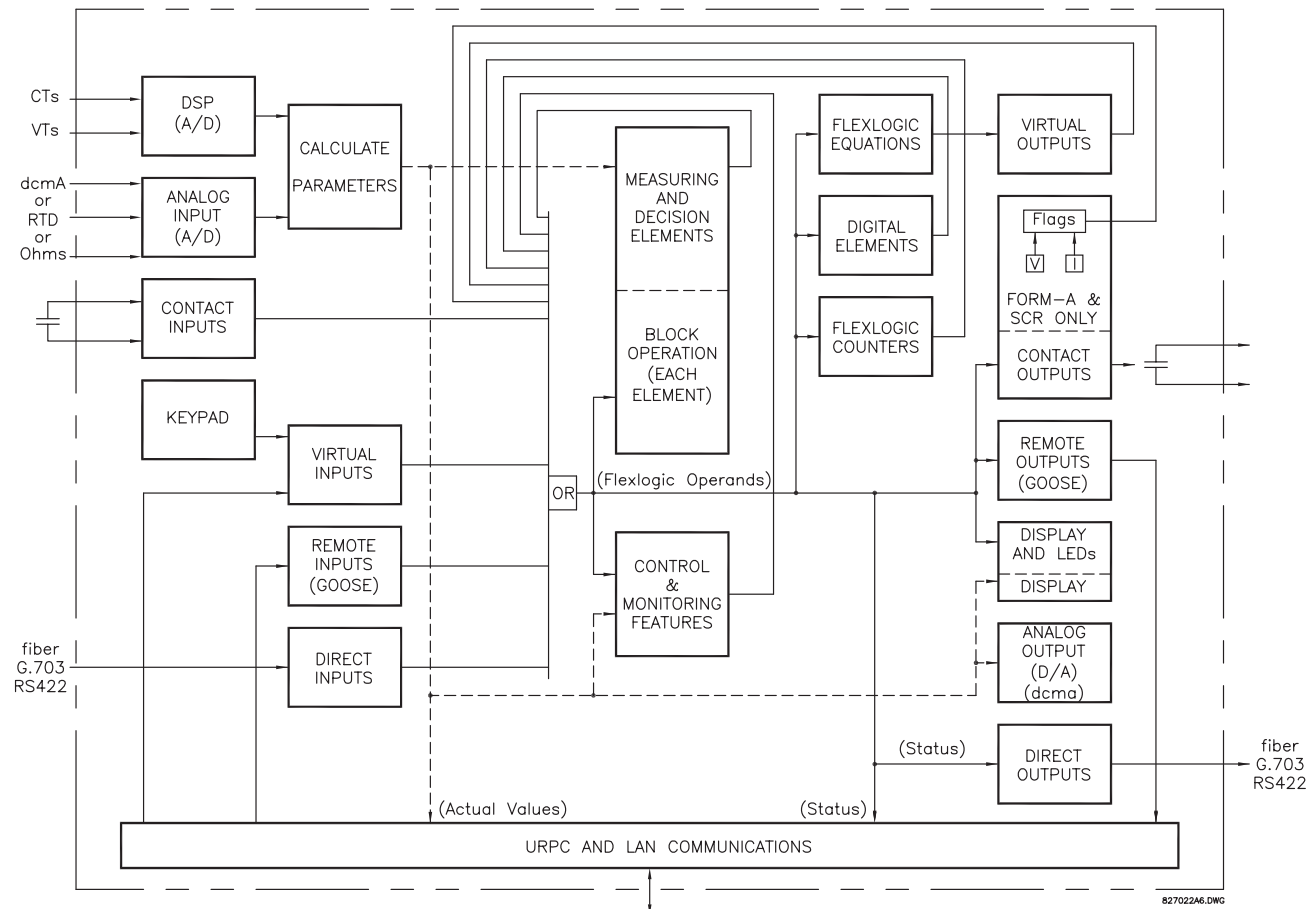


Figure 5-25: UR ARCHITECTURE OVERVIEW

The states of all digital signals used in the N60 are represented by flags (or FlexLogic™ operands, which are described later in this section). A digital “1” is represented by a ‘set’ flag. Any external contact change-of-state can be used to block an element from operating, as an input to a control feature in a FlexLogic™ equation, or to operate a contact output. The state of the contact input can be displayed locally or viewed remotely via the communications facilities provided. If a simple scheme where a contact input is used to block an element is desired, this selection is made when programming the element. This capability also applies to the other features that set flags: elements, virtual inputs, remote inputs, schemes, and human operators.

If more complex logic than presented above is required, it is implemented via FlexLogic™. For example, if it is desired to have the closed state of contact input H7a and the operated state of the phase undervoltage element block the operation of the phase time overcurrent element, the two control input states are programmed in a FlexLogic™ equation. This equation ANDs the two control inputs to produce a ‘virtual output’ which is then selected when programming the phase time overcurrent to be used as a blocking input. Virtual outputs can only be created by FlexLogic™ equations.

Traditionally, protective relay logic has been relatively limited. Any unusual applications involving interlocks, blocking, or supervisory functions had to be hard-wired using contact inputs and outputs. FlexLogic™ minimizes the requirement for auxiliary components and wiring while making more complex schemes possible.

The logic that determines the interaction of inputs, elements, schemes and outputs is field programmable through the use of logic equations that are sequentially processed. The use of virtual inputs and outputs in addition to hardware is available internally and on the communication ports for other relays to use (distributed FlexLogic™).

FlexLogic™ allows users to customize the relay through a series of equations that consist of operators and operands. The operands are the states of inputs, elements, schemes and outputs. The operators are logic gates, timers and latches (with set and reset inputs). A system of sequential operations allows any combination of specified operands to be assigned as inputs to specified operators to create an output. The final output of an equation is a numbered register called a virtual output. Virtual outputs can be used as an input operand in any equation, including the equation that generates the output, as a seal-in or other type of feedback.

A FlexLogic™ equation consists of parameters that are either operands or operators. Operands have a logic state of 1 or 0. Operators provide a defined function, such as an AND gate or a Timer. Each equation defines the combinations of parameters to be used to set a Virtual Output flag. Evaluation of an equation results in either a 1 (=ON, i.e. flag set) or 0 (=OFF, i.e. flag not set). Each equation is evaluated at least 4 times every power system cycle.

Some types of operands are present in the relay in multiple instances; e.g. contact and remote inputs. These types of operands are grouped together (for presentation purposes only) on the faceplate display. The characteristics of the different types of operands are listed in the table below.

Table 5–5: N60 FLEXLOGIC™ OPERAND TYPES

OPERAND TYPE	STATE	EXAMPLE FORMAT	CHARACTERISTICS [INPUT IS '1' (= ON) IF...]
Contact Input	On	Cont Ip On	Voltage is presently applied to the input (external contact closed).
	Off	Cont Ip Off	Voltage is presently not applied to the input (external contact open).
Contact Output (type Form-A contact only)	Voltage On	Cont Op 1 VOn	Voltage exists across the contact.
	Voltage Off	Cont Op 1 VOff	Voltage does not exist across the contact.
	Current On	Cont Op 1 IOn	Current is flowing through the contact.
	Current Off	Cont Op 1 IOff	Current is not flowing through the contact.
Direct Input	On	DIRECT INPUT 1 On	The direct input is presently in the ON state.
Element (Analog)	Pickup	PHASE TOC1 PKP	The tested parameter is presently above the pickup setting of an element which responds to rising values or below the pickup setting of an element which responds to falling values.
	Dropout	PHASE TOC1 DPO	This operand is the logical inverse of the above PKP operand.
	Operate	PHASE TOC1 OP	The tested parameter has been above/below the pickup setting of the element for the programmed delay time, or has been at logic 1 and is now at logic 0 but the reset timer has not finished timing.
	Block	PH DIR1 BLK	The output of the comparator is set to the block function.
Element (Digital)	Pickup	Dig Element 1 PKP	The input operand is at logic 1.
	Dropout	Dig Element 1 DPO	This operand is the logical inverse of the above PKP operand.
	Operate	Dig Element 1 OP	The input operand has been at logic 1 for the programmed pickup delay time, or has been at logic 1 for this period and is now at logic 0 but the reset timer has not finished timing.
Element (Digital Counter)	Higher than	Counter 1 HI	The number of pulses counted is above the set number.
	Equal to	Counter 1 EQL	The number of pulses counted is equal to the set number.
	Lower than	Counter 1 LO	The number of pulses counted is below the set number.
Fixed	On	On	Logic 1
	Off	Off	Logic 0
Remote Input	On	REMOTE INPUT 1 On	The remote input is presently in the ON state.
Virtual Input	On	Virt Ip 1 On	The virtual input is presently in the ON state.
Virtual Output	On	Virt Op 1 On	The virtual output is presently in the set state (i.e. evaluation of the equation which produces this virtual output results in a "1").

The operands available for this relay are listed alphabetically by types in the following table.

Table 5–6: N60 FLEXLOGIC™ OPERANDS (Sheet 1 of 5)

OPERAND TYPE	OPERAND SYNTAX	OPERAND DESCRIPTION
CONTROL PUSHBUTTONS	CONTROL PUSHBTN n ON	Control Pushbutton n ($n = 1$ to 7) is being pressed.
DIRECT DEVICES	DIRECT DEVICE 1On	Flag is set, logic=1
	↓	↓
	DIRECT DEVICE 16On DIRECT DEVICE 1Off	Flag is set, logic=1 Flag is set, logic=1
	↓	↓
	DIRECT DEVICE 16Off	Flag is set, logic=1
DIRECT INPUT/ OUTPUT CHANNEL MONITORING	DIR IO CH1(2) CRC ALARM DIR IO CRC ALARM DIR IO CH1(2) UNRET ALM DIR IO UNRET ALM	The rate of Direct Input messages received on Channel 1(2) and failing the CRC exceeded the user-specified level. The rate of Direct Input messages failing the CRC exceeded the user-specified level on Channel 1 or 2. The rate of returned direct input/output messages on Channel 1(2) exceeded the user-specified level (ring configurations only). The rate of returned direct input/output messages exceeded the user-specified level on Channel 1 or 2 (ring configurations only).
ELEMENT: 8-Bit Comparator	8BIT COMP 1 PKP 8BIT COMP 1 DPO 8BIT COMP 1 OP 8BIT COMP 1 BIT 0 8BIT COMP 1 BIT 1 8BIT COMP 1 BIT 2 8BIT COMP 1 BIT 3 8BIT COMP 1 BIT 4 8BIT COMP 1 BIT 5 8BIT COMP 1 BIT 6 8BIT COMP 1 BIT 7	Comparator 1 has picked up Comparator 1 has dropped out Comparator 1 has operated Bit 0 of Comparator 1 asserted (the least significant bit) Bit 1 of Comparator 1 asserted Bit 2 of Comparator 1 asserted Bit 3 of Comparator 1 asserted Bit 4 of Comparator 1 asserted Bit 5 of Comparator 1 asserted Bit 6 of Comparator 1 asserted (the most significant bit) Sign bit of Comparator 1 asserted (asserted for negative values; de-asserted for positive values)
	8BIT COMP 2 to 8BIT COMP 6	Same set of operands as shown for 8 BIT COMP 1 above
ELEMENT: 8-Bit Switch	8BIT SWITCH 1 BIT 0 8BIT SWITCH 1 BIT 1 8BIT SWITCH 1 BIT 2 8BIT SWITCH 1 BIT 3 8BIT SWITCH 1 BIT 4 8BIT SWITCH 1 BIT 5 8BIT SWITCH 1 BIT 6 8BIT SWITCH 1 BIT 7	Bit 0 of Eight-Bit Switch 1 asserted (the least significant bit) Bit 1 of Eight-Bit Switch 1 asserted Bit 2 of Eight-Bit Switch 1 asserted Bit 3 of Eight-Bit Switch 1 asserted Bit 4 of Eight-Bit Switch 1 asserted Bit 5 of Eight-Bit Switch 1 asserted Bit 6 of Eight-Bit Switch 1 asserted Bit 7 of Eight-Bit Switch 1 asserted (the most significant bit)
	8BIT SWITCH 2 to 8BIT SWITCH 6	Same set of operands as shown for 8 BIT SWITCH 1 above
ELEMENT: Breaker Control	BREAKER 1 OFF CMD BREAKER 1 ON CMD BREAKER 1 ϕ A CLSD BREAKER 1 ϕ B CLSD BREAKER 1 ϕ C CLSD BREAKER 1 CLOSED BREAKER 1 OPEN BREAKER 1 DISCREP BREAKER 1 TROUBLE BREAKER 1 MNL CLS BREAKER 1 TRIP A BREAKER 1 TRIP B BREAKER 1 TRIP C BREAKER 1 ANY P OPEN BREAKER 1 ONE P OPEN BREAKER 1 OOS	Breaker 1 OFF command Breaker 1 ON command Breaker 1 phase A is closed Breaker 1 phase B is closed Breaker 1 phase C is closed Breaker 1 is closed Breaker 1 is open Breaker 1 has discrepancy Breaker 1 trouble alarm Breaker 1 manual close Breaker 1 trip phase A command Breaker 1 trip phase B command Breaker 1 trip phase C command At least one pole of Breaker 1 is open Only one pole of Breaker 1 is open Breaker 1 is out of service
	BREAKER 2	Same set of operands as shown for BREAKER 1
ELEMENT: Digital Counters	Counter 1 HI Counter 1 EQL Counter 1 LO	Digital Counter 1 output is 'more than' comparison value Digital Counter 1 output is 'equal to' comparison value Digital Counter 1 output is 'less than' comparison value
	↓	↓
	Counter 8 HI Counter 8 EQL Counter 8 LO	Digital Counter 8 output is 'more than' comparison value Digital Counter 8 output is 'equal to' comparison value Digital Counter 8 output is 'less than' comparison value

Table 5–6: N60 FLEXLOGIC™ OPERANDS (Sheet 2 of 5)

OPERAND TYPE	OPERAND SYNTAX	OPERAND DESCRIPTION
ELEMENT: Digital Elements	Dig Element 1 PKP Dig Element 1 OP Dig Element 1 DPO ↓ Dig Element 48 PKP Dig Element 48 OP Dig Element 48 DPO	Digital Element 1 is picked up Digital Element 1 is operated Digital Element 1 is dropped out ↓ Digital Element 48 is picked up Digital Element 48 is operated Digital Element 48 is dropped out
ELEMENT: Digitizer	DIGITIZER N BIT 0 DIGITIZER N BIT 1 DIGITIZER N BIT 2 DIGITIZER N BIT 3 DIGITIZER N BIT 4 DIGITIZER N BIT 5 DIGITIZER N BIT 6 DIGITIZER N BIT 7	Bit 0 of Digitizer N (N = 1 to 5) asserted (the least significant bit) Bit 1 of Digitizer N (N = 1 to 5) asserted Bit 2 of Digitizer N (N = 1 to 5) asserted Bit 3 of Digitizer N (N = 1 to 5) asserted Bit 4 of Digitizer N (N = 1 to 5) asserted Bit 5 of Digitizer N (N = 1 to 5) asserted Bit 6 of Digitizer N (N = 1 to 5) asserted (the most significant bit) Bit 7 of Digitizer N (N = 1 to 5) asserted (asserted for negative values, de-asserted for positive values)
	Output = $(-1)^{B7} \times (B6 \times 2^6 + B5 \times 2^5 + \dots + B1 \times 2^1 + B0 \times 2^0)$	
ELEMENT: Sensitive Directional Power	DIR POWER 1 STG1 PKP DIR POWER 1 STG2 PKP DIR POWER 1 STG1 DPO DIR POWER 1 STG2 DPO DIR POWER 1 STG1 OP DIR POWER 1 STG2 OP DIR POWER 1 PKP DIR POWER 1 DPO DIR POWER 1 OP	Stage 1 of the Directional Power element 1 has picked up Stage 2 of the Directional Power element 1 has picked up Stage 1 of the Directional Power element 1 has dropped out Stage 2 of the Directional Power element 1 has dropped out Stage 1 of the Directional Power element 1 has operated Stage 2 of the Directional Power element 1 has operated The Directional Power element has picked up The Directional Power element has dropped out The Directional Power element has operated
	DIR POWER 2	Same set of operands as DIR POWER 1
ELEMENT Frequency Rate of Change	FREQ RATE n PKP FREQ RATE n DPO FREQ RATE n OP	The n-th Frequency Rate of Change element has picked up The n-th Frequency Rate of Change element has dropped out The n-th Frequency Rate of Change element has operated
ELEMENT: FlexElements™	FxE 1 PKP FxE 1 OP FxE 1 DPO ↓ FxE 16 PKP FxE 16 OP FxE 16 DPO	FlexElement™ 1 has picked up FlexElement™ 1 has operated FlexElement™ 1 has dropped out ↓ FlexElement™ 16 has picked up FlexElement™ 16 has operated FlexElement™ 16 has dropped out
ELEMENT Non-Volatile Latches	LATCH 1 ON LATCH 1 OFF ↓ LATCH 16 ON LATCH 16 OFF	Non-Volatile Latch 1 is ON (Logic = 1) Non-Voltage Latch 1 is OFF (Logic = 0) ↓ Non-Volatile Latch 16 is ON (Logic = 1) Non-Voltage Latch 16 is OFF (Logic = 0)
ELEMENT: Open Pole Detector	OPEN POLE OP ΦA OPEN POLE OP ΦB OPEN POLE OP ΦC OPEN POLE OP	Open pole condition is detected in phase A Open pole condition is detected in phase B Open pole condition is detected in phase C Open pole detector is operated
ELEMENT: Overfrequency	OVERFREQ 1 PKP OVERFREQ 1 OP OVERFREQ 1 DPO	Overfrequency 1 has picked up Overfrequency 1 has operated Overfrequency 1 has dropped out
	OVERFREQ 2 to 4	Same set of operands as shown for OVERFREQ 1
ELEMENT: Synchrophasor Phasor Data Concentrator	PDC DIR CH1 CNTRL 1 PDC DIR CH1 CNTRL 2 ↓ PDC DIR CH1 CNTRL 16 PDC DIR CH2 CNTRL 1 PDC DIR CH2 CNTRL 2 ↓ PDC DIR CH2 CNTRL 16 PDC NETWORK CNTRL 1 PDC NETWORK CNTRL 2 ↓ PDC NETWORK CNTRL 16 PDC RS485 CNTRL 1 PDC RS485 CNTRL 2 ↓ PDC RS485 CNTRL 16	Phasor data concentrator asserts control bit 1 as received via direct channel 1 Phasor data concentrator asserts control bit 2 as received via direct channel 1 ↓ Phasor data concentrator asserts control bit 16 received via direct channel 1 Phasor data concentrator asserts control bit 1 as received via direct channel 2 Phasor data concentrator asserts control bit 2 as received via direct channel 2 ↓ Phasor data concentrator asserts control bit 16 received via direct channel 2 Phasor data concentrator asserts control bit 1 as received via the network Phasor data concentrator asserts control bit 2 as received via the network ↓ Phasor data concentrator asserts control bit 16 as received via the network Phasor data concentrator asserts control bit 1 as received via RS485 Phasor data concentrator asserts control bit 2 as received via RS485 ↓ Phasor data concentrator asserts control bit 16 as received via RS485

Table 5–6: N60 FLEXLOGIC™ OPERANDS (Sheet 3 of 5)

OPERAND TYPE	OPERAND SYNTAX	OPERAND DESCRIPTION
ELEMENT: Phase Instantaneous Overcurrent	PHASE IOC1 PKP PHASE IOC1 OP PHASE IOC1 DPO PHASE IOC1 PKP A PHASE IOC1 PKP B PHASE IOC1 PKP C PHASE IOC1 OP A PHASE IOC1 OP B PHASE IOC1 OP C PHASE IOC1 DPO A PHASE IOC1 DPO B PHASE IOC1 DPO C	At least one phase of PHASE IOC1 has picked up At least one phase of PHASE IOC1 has operated At least one phase of PHASE IOC1 has dropped out Phase A of PHASE IOC1 has picked up Phase B of PHASE IOC1 has picked up Phase C of PHASE IOC1 has picked up Phase A of PHASE IOC1 has operated Phase B of PHASE IOC1 has operated Phase C of PHASE IOC1 has operated Phase A of PHASE IOC1 has dropped out Phase B of PHASE IOC1 has dropped out Phase C of PHASE IOC1 has dropped out
	PHASE IOC2IOC12	Same set of operands as shown for PHASE IOC1
ELEMENT: Phase Overvoltage	PHASE OV1 PKP PHASE OV1 OP PHASE OV1 DPO PHASE OV1 PKP A PHASE OV1 PKP B PHASE OV1 PKP C PHASE OV1 OP A PHASE OV1 OP B PHASE OV1 OP C PHASE OV1 DPO A PHASE OV1 DPO B PHASE OV1 DPO C	At least one phase of Overvoltage 1 has picked up At least one phase of Overvoltage 1 has operated At least one phase of Overvoltage 1 has dropped out Phase A of Overvoltage 1 has picked up Phase B of Overvoltage 1 has picked up Phase C of Overvoltage 1 has picked up Phase A of Overvoltage 1 has operated Phase B of Overvoltage 1 has operated Phase C of Overvoltage 1 has operated Phase A of Overvoltage 1 has dropped out Phase B of Overvoltage 1 has dropped out Phase C of Overvoltage 1 has dropped out
	PHASE UV1 PKP PHASE UV1 OP PHASE UV1 DPO PHASE UV1 PKP A PHASE UV1 PKP B PHASE UV1 PKP C PHASE UV1 OP A PHASE UV1 OP B PHASE UV1 OP C PHASE UV1 DPO A PHASE UV1 DPO B PHASE UV1 DPO C	At least one phase of UV1 has picked up At least one phase of UV1 has operated At least one phase of UV1 has dropped out Phase A of UV1 has picked up Phase B of UV1 has picked up Phase C of UV1 has picked up Phase A of UV1 has operated Phase B of UV1 has operated Phase C of UV1 has operated Phase A of UV1 has dropped out Phase B of UV1 has dropped out Phase C of UV1 has dropped out
ELEMENT: Phase Undervoltage	PHASE UV2	Same set of operands as shown for PHASE UV1
	PMU 1 CURR TRIGGER PMU 1 FREQ TRIGGER PMU 1 POWER TRIGGER PMU 1 ROCOF TRIGGER PMU 1 VOLT TRIGGER PMU 1 TRIGGERED	Overcurrent trigger of PMU 1 has operated Abnormal frequency trigger of PMU 1 has operated Overpower trigger of PMU 1 has operated Rate of change of frequency trigger of PMU 1 has operated Abnormal voltage trigger of PMU 1 has operated PMU 1 triggered; no events or targets are generated by this operand
ELEMENT: Synchrophasor One-Shot	PMU ONE-SHOT EXPIRED PMU ONE-SHOT OP PMU ONE-SHOT PENDING	Indicates the one-shot operation has been executed, and the present time is at least 30 seconds past the scheduled one-shot time Indicates the one-shot operation is pending; that is, the present time is before the scheduled one-shot time Indicates the one-shot operation and remains asserted for 30 seconds afterwards
	PMU 2 to 4	Same set of operands as shown for PMU 1
ELEMENT: Power Swing Detect	POWER SWING OUTER POWER SWING MIDDLE POWER SWING INNER POWER SWING BLOCK POWER SWING TMRX PKP POWER SWING TRIP POWER SWING 50DD POWER SWING INCOMING POWER SWING OUTGOING POWER SWING UN/BLOCK	Positive Sequence impedance in outer characteristic. Positive Sequence impedance in middle characteristic. Positive Sequence impedance in inner characteristic. Power Swing Blocking element operated. Power Swing Timer x picked up. Out-of-step Tripping operated. The Power Swing element detected a disturbance other than power swing. An unstable power swing has been detected (incoming locus). An unstable power swing has been detected (outgoing locus).

Table 5–6: N60 FLEXLOGIC™ OPERANDS (Sheet 4 of 5)

OPERAND TYPE	OPERAND SYNTAX	OPERAND DESCRIPTION
ELEMENT: Selector Switch	SELECTOR 1 POS Y SELECTOR 1 BIT 0 SELECTOR 1 BIT 1 SELECTOR 1 BIT 2 SELECTOR 1 STP ALARM SELECTOR 1 BIT ALARM SELECTOR 1 ALARM SELECTOR 1 PWR ALARM	Selector Switch 1 is in Position Y (mutually exclusive operands). First bit of the 3-bit word encoding position of Selector 1. Second bit of the 3-bit word encoding position of Selector 1. Third bit of the 3-bit word encoding position of Selector 1. Position of Selector 1 has been pre-selected with the stepping up control input but not acknowledged. Position of Selector 1 has been pre-selected with the 3-bit control input but not acknowledged. Position of Selector 1 has been pre-selected but not acknowledged. Position of Selector Switch 1 is undetermined or restored from memory when the relay powers up and synchronizes to the 3-bit input.
	SELECTOR 2	Same set of operands as shown above for SELECTOR 1
ELEMENT: Setting Group	SETTING GROUP ACT 1 ↓ SETTING GROUP ACT 6	Setting Group 1 is active ↓ Setting Group 6 is active
	SRCx 50DD OP	Source x Disturbance Detector has operated
ELEMENT: VTFF (Voltage Transformer Fuse Failure)	SRCx VT FUSE FAIL OP SRCx VT FUSE FAIL DPO SRCx VT FUSE FAIL VOL LOSS	Source x VT Fuse Failure detector has operated Source x VT Fuse Failure detector has dropped out Source x has lost voltage signals (V2 below 15% AND V1 below 5% of nominal)
ELEMENT: Synchrocheck	SYNC 1 DEAD S OP SYNC 1 DEAD S DPO SYNC 1 SYNC OP SYNC 1 SYNC DPO SYNC 1 CLS OP SYNC 1 CLS DPO SYNC 1 V1 ABOVE MIN SYNC 1 V1 BELOW MAX SYNC 1 V2 ABOVE MIN SYNC 1 V2 BELOW MAX	Synchrocheck 1 dead source has operated Synchrocheck 1 dead source has dropped out Synchrocheck 1 in synchronization has operated Synchrocheck 1 in synchronization has dropped out Synchrocheck 1 close has operated Synchrocheck 1 close has dropped out Synchrocheck 1 V1 is above the minimum live voltage Synchrocheck 1 V1 is below the maximum dead voltage Synchrocheck 1 V2 is above the minimum live voltage Synchrocheck 1 V2 is below the maximum dead voltage
	SYNC 2	Same set of operands as shown for SYNC 1
ELEMENT: Underfrequency	UNDERFREQ 1 PKP UNDERFREQ 1 OP UNDERFREQ 1 DPO	Underfrequency 1 has picked up Underfrequency 1 has operated Underfrequency 1 has dropped out
	UNDERFREQ 2 to 6	Same set of operands as shown for UNDERFREQ 1 above
FIXED OPERANDS	Off	Logic = 0. Does nothing and may be used as a delimiter in an equation list; used as 'Disable' by other features.
	On	Logic = 1. Can be used as a test setting.
INPUTS/OUTPUTS: Contact Inputs	Cont Ip 1 On Cont Ip 2 On ↓ Cont Ip 1 Off Cont Ip 2 Off ↓	(will not appear unless ordered) (will not appear unless ordered) ↓ (will not appear unless ordered) (will not appear unless ordered) ↓
	Cont Op 1 IOn Cont Op 2 IOn ↓ Cont Op 1 IOff Cont Op 2 IOff ↓	(will not appear unless ordered) (will not appear unless ordered) ↓ (will not appear unless ordered) (will not appear unless ordered) ↓
INPUTS/OUTPUTS: Contact Outputs, Current (from detector on Form-A output only)	Cont Op 1 VOn Cont Op 2 VOn ↓ Cont Op 1 VOff Cont Op 2 VOff ↓	(will not appear unless ordered) (will not appear unless ordered) ↓ (will not appear unless ordered) (will not appear unless ordered) ↓
	DIRECT INPUT 1 On ↓ DIRECT INPUT 64 On	Flag is set, logic=1 ↓ Flag is set, logic=1
INPUTS/OUTPUTS: Remote Inputs	REMOTE INPUT 1 On ↓ REMOTE INPUT 32 On	Flag is set, logic=1 ↓ Flag is set, logic=1

Table 5–6: N60 FLEXLOGIC™ OPERANDS (Sheet 5 of 5)

OPERAND TYPE	OPERAND SYNTAX	OPERAND DESCRIPTION
INPUTS/OUTPUTS: Virtual Inputs	Virt Ip 1 On ↓ Virt Ip 64 On	Flag is set, logic=1 ↓ Flag is set, logic=1
INPUTS/OUTPUTS: Virtual Outputs	Virt Op 1 On ↓ Virt Op 96 On	Flag is set, logic=1 ↓ Flag is set, logic=1
LED TEST	LED TEST IN PROGRESS	An LED test has been initiated and has not finished.
REMOTE DEVICES	REMOTE DEVICE 1 On ↓ REMOTE DEVICE 16 On ↓ REMOTE DEVICE 1 Off ↓ REMOTE DEVICE 16 Off	Flag is set, logic=1 ↓ Flag is set, logic=1 ↓ Flag is set, logic=1 ↓ Flag is set, logic=1
RESETTING	RESET OP RESET OP (COMMS) RESET OP (OPERAND) RESET OP (PUSHBUTTON)	Reset command is operated (set by all 3 operands below) Communications source of the reset command Operand (assigned in the INPUTS/OUTPUTS ⇒ RESETTING menu) source of the reset command Reset key (pushbutton) source of the reset command
SELF-DIAGNOSTICS	ANY MAJOR ERROR ANY MINOR ERROR ANY SELF-TEST BATTERY FAIL DIRECT DEVICE OFF DIRECT RING BREAK DSP ERROR EEPROM DATA ERROR EQUIPMENT MISMATCH FLEXLOGIC ERR TOKEN IRIG-B FAILURE LATCHING OUT ERROR LOW ON MEMORY NO DSP INTERRUPTS PRI ETHERNET FAIL PROGRAM MEMORY PROTOTYPE FIRMWARE REMOTE DEVICE OFF SEC ETHERNET FAIL SNTP FAILURE SYSTEM EXCEPTION UNIT NOT CALIBRATED UNIT NOT PROGRAMMED WATCHDOG ERROR	Any of the major self-test errors generated (major error) Any of the minor self-test errors generated (minor error) Any self-test errors generated (generic, any error) See description in Chapter 7: Commands and Targets.
UNAUTHORIZED ACCESS ALARM	UNAUTHORIZED ACCESS	Asserted when a password entry fails while accessing a password-protected level of the relay.
USER-PROGRAMMABLE PUSHBUTTONS	PUSHBUTTON x ON PUSHBUTTON x OFF	Pushbutton Number x is in the 'On' position Pushbutton Number x is in the 'Off' position

Some operands can be re-named by the user. These are the names of the breakers in the breaker control feature, the ID (identification) of contact inputs, the ID of virtual inputs, and the ID of virtual outputs. If the user changes the default name/ID of any of these operands, the assigned name will appear in the relay list of operands. The default names are shown in the FlexLogic™ operands table above.

The characteristics of the logic gates are tabulated below, and the operators available in FlexLogic™ are listed in the FlexLogic™ operators table.

Table 5–7: FLEXLOGIC™ GATE CHARACTERISTICS

GATES	NUMBER OF INPUTS	OUTPUT IS '1' (= ON) IF...
NOT	1	input is '0'
OR	2 to 16	any input is '1'
AND	2 to 16	all inputs are '1'
NOR	2 to 16	all inputs are '0'
NAND	2 to 16	any input is '0'
XOR	2	only one input is '1'

Table 5–8: FLEXLOGIC™ OPERATORS

TYPE	SYNTAX	DESCRIPTION	NOTES
Editor	INSERT	Insert a parameter in an equation list.	
	DELETE	Delete a parameter from an equation list.	
End	END	The first END encountered signifies the last entry in the list of processed FlexLogic™ parameters.	
One Shot	POSITIVE ONE SHOT	One shot that responds to a positive going edge.	A 'one shot' refers to a single input gate that generates a pulse in response to an edge on the input. The output from a 'one shot' is True (positive) for only one pass through the FlexLogic™ equation. There is a maximum of 64 'one shots'.
	NEGATIVE ONE SHOT	One shot that responds to a negative going edge.	
	DUAL ONE SHOT	One shot that responds to both the positive and negative going edges.	
Logic Gate	NOT	Logical Not	Operates on the previous parameter.
	OR(2)	2 input OR gate	Operates on the 2 previous parameters.
	OR(16)	16 input OR gate	Operates on the 16 previous parameters.
	AND(2)	2 input AND gate	Operates on the 2 previous parameters.
	AND(16)	16 input AND gate	Operates on the 16 previous parameters.
	NOR(2)	2 input NOR gate	Operates on the 2 previous parameters.
	NOR(16)	16 input NOR gate	Operates on the 16 previous parameters.
	NAND(2)	2 input NAND gate	Operates on the 2 previous parameters.
	NAND(16)	16 input NAND gate	Operates on the 16 previous parameters.
Timer	XOR(2)	2 input Exclusive OR gate	Operates on the 2 previous parameters.
	LATCH (S,R)	Latch (Set, Reset) - reset-dominant	The parameter preceding LATCH(S,R) is the Reset input. The parameter preceding the Reset input is the Set input.
Timer	TIMER 1	Timer set with FlexLogic™ Timer 1 settings.	The timer is started by the preceding parameter. The output of the timer is TIMER #.
	TIMER 32	Timer set with FlexLogic™ Timer 32 settings.	
Assign Virtual Output	= Virt Op 1	Assigns previous FlexLogic™ parameter to Virtual Output 1.	The virtual output is set by the preceding parameter
	= Virt Op 96	Assigns previous FlexLogic™ parameter to Virtual Output 96.	

5.4.2 FLEXLOGIC™ RULES

When forming a FlexLogic™ equation, the sequence in the linear array of parameters must follow these general rules:

1. Operands must precede the operator which uses the operands as inputs.
2. Operators have only one output. The output of an operator must be used to create a virtual output if it is to be used as an input to two or more operators.
3. Assigning the output of an operator to a Virtual Output terminates the equation.
4. A timer operator (e.g. "TIMER 1") or virtual output assignment (e.g. "= Virt Op 1") may only be used once. If this rule is broken, a syntax error will be declared.

5.4.3 FLEXLOGIC™ EVALUATION

Each equation is evaluated in the order in which the parameters have been entered.



FlexLogic™ provides latches which by definition have a memory action, remaining in the set state after the set input has been asserted. However, they are *volatile*; i.e. they reset on the re-application of control power.

When making changes to settings, all FlexLogic™ equations are re-compiled whenever any new setting value is entered, so all latches are automatically reset. If it is necessary to re-initialize FlexLogic™ during testing, for example, it is suggested to power the unit down and then back up.

5.4.4 FLEXLOGIC™ EXAMPLE

This section provides an example of implementing logic for a typical application. The sequence of the steps is quite important as it should minimize the work necessary to develop the relay settings. Note that the example presented in the figure below is intended to demonstrate the procedure, not to solve a specific application situation.

In the example below, it is assumed that logic has already been programmed to produce Virtual Outputs 1 and 2, and is only a part of the full set of equations used. When using FlexLogic™, it is important to make a note of each Virtual Output used – a Virtual Output designation (1 to 96) can only be properly assigned once.

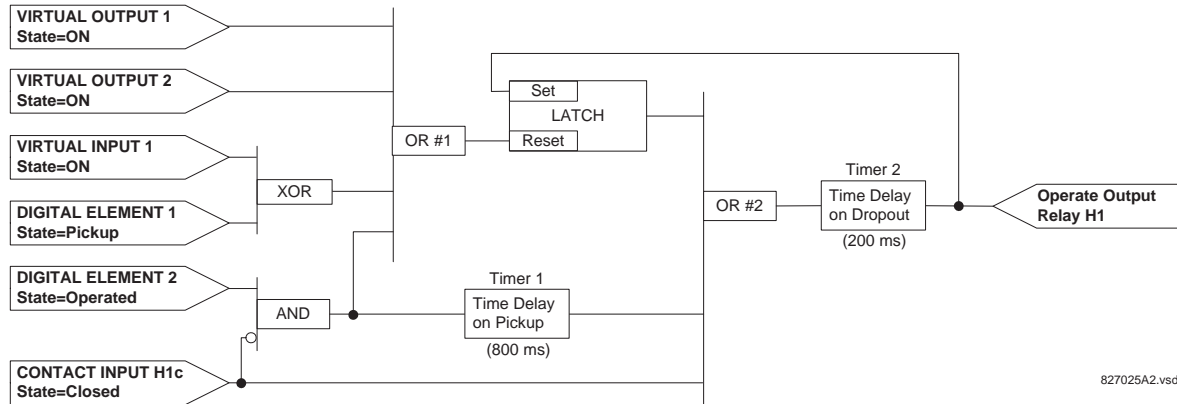


Figure 5–26: EXAMPLE LOGIC SCHEME

1. Inspect the example logic diagram to determine if the required logic can be implemented with the FlexLogic™ operators. If this is not possible, the logic must be altered until this condition is satisfied. Once this is done, count the inputs to each gate to verify that the number of inputs does not exceed the FlexLogic™ limits, which is unlikely but possible. If the number of inputs is too high, subdivide the inputs into multiple gates to produce an equivalent. For example, if 25 inputs to an AND gate are required, connect Inputs 1 through 16 to AND(16), 17 through 25 to AND(9), and the outputs from these two gates to AND(2).

Inspect each operator between the initial operands and final virtual outputs to determine if the output from the operator is used as an input to more than one following operator. If so, the operator output must be assigned as a Virtual Output.

For the example shown above, the output of the AND gate is used as an input to both OR#1 and Timer 1, and must therefore be made a Virtual Output and assigned the next available number (i.e. Virtual Output 3). The final output must also be assigned to a Virtual Output as Virtual Output 4, which will be programmed in the contact output section to operate relay H1 (i.e. Output Contact H1).

Therefore, the required logic can be implemented with two FlexLogic™ equations with outputs of Virtual Output 3 and Virtual Output 4 as shown below.

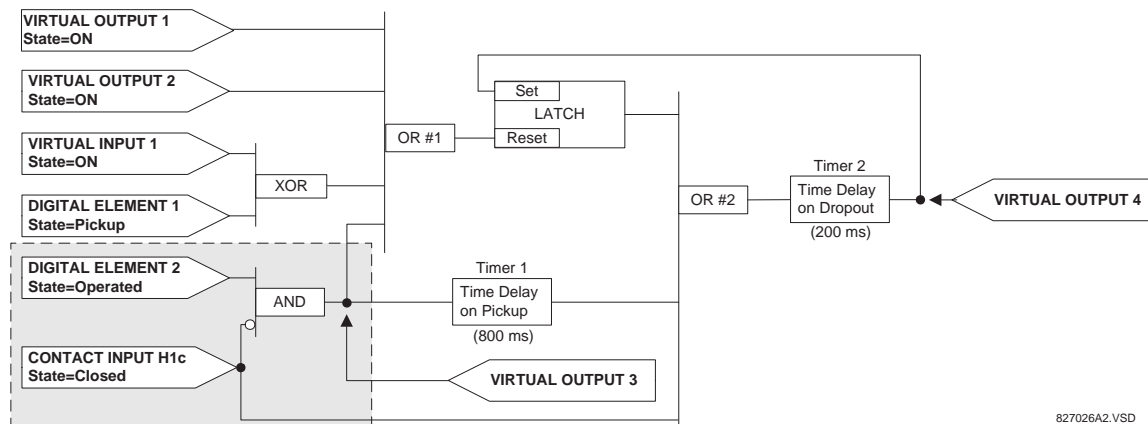


Figure 5–27: LOGIC EXAMPLE WITH VIRTUAL OUTPUTS

2. Prepare a logic diagram for the equation to produce Virtual Output 3, as this output will be used as an operand in the Virtual Output 4 equation (create the equation for every output that will be used as an operand first, so that when these operands are required they will already have been evaluated and assigned to a specific Virtual Output). The logic for Virtual Output 3 is shown below with the final output assigned.

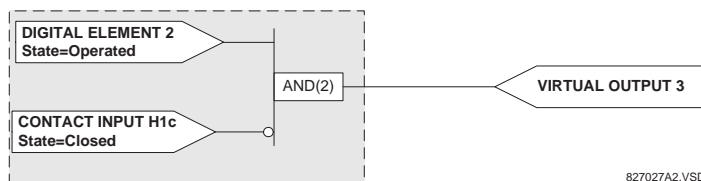


Figure 5–28: LOGIC FOR VIRTUAL OUTPUT 3

3. Prepare a logic diagram for Virtual Output 4, replacing the logic ahead of Virtual Output 3 with a symbol identified as Virtual Output 3, as shown below.

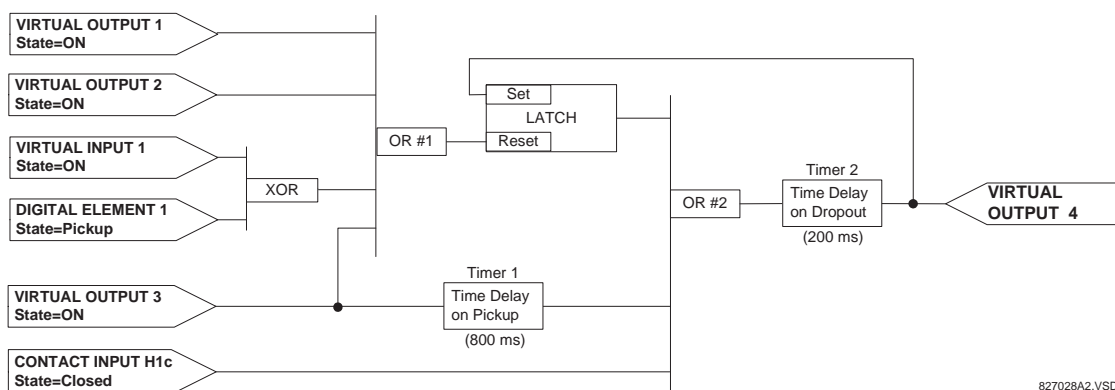


Figure 5–29: LOGIC FOR VIRTUAL OUTPUT 4

4. Program the FlexLogic™ equation for Virtual Output 3 by translating the logic into available FlexLogic™ parameters. The equation is formed one parameter at a time until the required logic is complete. It is generally easier to start at the output end of the equation and work back towards the input, as shown in the following steps. It is also recommended to list operator inputs from bottom to top. For demonstration, the final output will be arbitrarily identified as parameter 99, and each preceding parameter decremented by one in turn. Until accustomed to using FlexLogic™, it is suggested that a worksheet with a series of cells marked with the arbitrary parameter numbers be prepared, as shown below.

01	
02	
03	
04	
05	
⋮	
97	
98	
99	

827029A1.VSD

Figure 5–30: FLEXLOGIC™ WORKSHEET

5. Following the procedure outlined, start with parameter 99, as follows:

99: The final output of the equation is Virtual Output 3, which is created by the operator "= Virt Op n". This parameter is therefore "= Virt Op 3."

- 98: The gate preceding the output is an AND, which in this case requires two inputs. The operator for this gate is a 2-input AND so the parameter is "AND(2)". Note that FlexLogic™ rules require that the number of inputs to most types of operators must be specified to identify the operands for the gate. As the 2-input AND will operate on the two operands preceding it, these inputs must be specified, starting with the lower.
- 97: This lower input to the AND gate must be passed through an inverter (the NOT operator) so the next parameter is "NOT". The NOT operator acts upon the operand immediately preceding it, so specify the inverter input next.
- 96: The input to the NOT gate is to be contact input H1c. The ON state of a contact input can be programmed to be set when the contact is either open or closed. Assume for this example the state is to be ON for a closed contact. The operand is therefore "Cont Ip H1c On".
- 95: The last step in the procedure is to specify the upper input to the AND gate, the operated state of digital element 2. This operand is "DIG ELEM 2 OP".

Writing the parameters in numerical order can now form the equation for VIRTUAL OUTPUT 3:

```
[95] DIG ELEM 2 OP
[96] Cont Ip H1c On
[97] NOT
[98] AND(2)
[99] = Virt Op 3
```

It is now possible to check that this selection of parameters will produce the required logic by converting the set of parameters into a logic diagram. The result of this process is shown below, which is compared to the Logic for Virtual Output 3 diagram as a check.

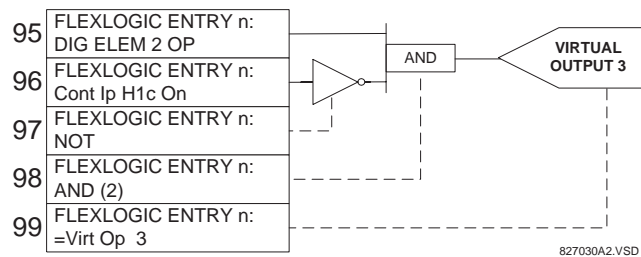


Figure 5–31: FLEXLOGIC™ EQUATION FOR VIRTUAL OUTPUT 3

6. Repeating the process described for VIRTUAL OUTPUT 3, select the FlexLogic™ parameters for Virtual Output 4.

99: The final output of the equation is VIRTUAL OUTPUT 4 which is parameter "= Virt Op 4".

98: The operator preceding the output is Timer 2, which is operand "TIMER 2". Note that the settings required for the timer are established in the timer programming section.

97: The operator preceding Timer 2 is OR #2, a 3-input OR, which is parameter "OR(3)".

96: The lowest input to OR #2 is operand "Cont Ip H1c On".

95: The center input to OR #2 is operand "TIMER 1".

94: The input to Timer 1 is operand "Virt Op 3 On".

93: The upper input to OR #2 is operand "LATCH (S,R)".

92: There are two inputs to a latch, and the input immediately preceding the latch reset is OR #1, a 4-input OR, which is parameter "OR(4)".

91: The lowest input to OR #1 is operand "Virt Op 3 On".

90: The input just above the lowest input to OR #1 is operand "XOR(2)".

89: The lower input to the XOR is operand "DIG ELEM 1 PKP".

88: The upper input to the XOR is operand "Virt Ip 1 On".

87: The input just below the upper input to OR #1 is operand "Virt Op 2 On".

86: The upper input to OR #1 is operand "Virt Op 1 On".

85: The last parameter is used to set the latch, and is operand "Virt Op 4 On".

The equation for VIRTUAL OUTPUT 4 is:

```

[85] Virt Op 4 On
[86] Virt Op 1 On
[87] Virt Op 2 On
[88] Virt Ip 1 On
[89] DIG ELEM 1 PKP
[90] XOR(2)
[91] Virt Op 3 On
[92] OR(4)
[93] LATCH (S,R)
[94] Virt Op 3 On
[95] TIMER 1
[96] Cont Ip H1c On
[97] OR(3)
[98] TIMER 2
[99] = Virt Op 4

```

It is now possible to check that the selection of parameters will produce the required logic by converting the set of parameters into a logic diagram. The result of this process is shown below, which is compared to the Logic for Virtual Output 4 diagram as a check.

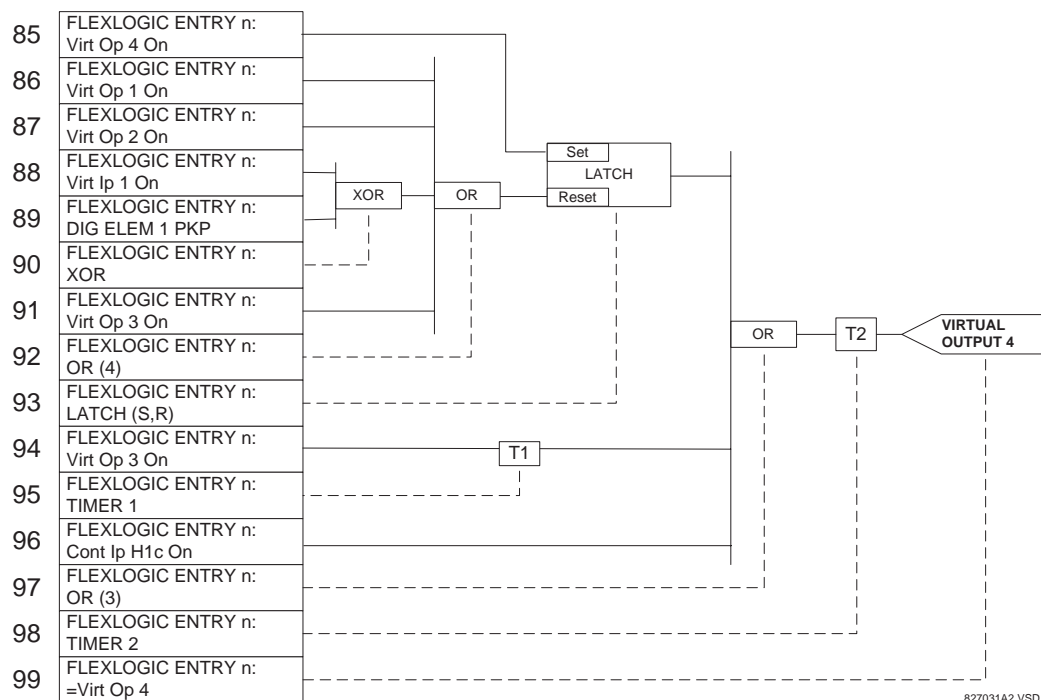


Figure 5–32: FLEXLOGIC™ EQUATION FOR VIRTUAL OUTPUT 4

7. Now write the complete FlexLogic™ expression required to implement the logic, making an effort to assemble the equation in an order where Virtual Outputs that will be used as inputs to operators are created before needed. In cases where a lot of processing is required to perform logic, this may be difficult to achieve, but in most cases will not cause problems as all logic is calculated at least 4 times per power frequency cycle. The possibility of a problem caused by sequential processing emphasizes the necessity to test the performance of FlexLogic™ before it is placed in service.

In the following equation, Virtual Output 3 is used as an input to both Latch 1 and Timer 1 as arranged in the order shown below:

```
DIG ELEM 2 OP
Cont Ip H1c On
NOT
AND(2)
```

```

= Virt Op 3
Virt Op 4 On
Virt Op 1 On
Virt Op 2 On
Virt Ip 1 On
DIG ELEM 1 PKP
XOR(2)
Virt Op 3 On
OR(4)
LATCH (S,R)
Virt Op 3 On
TIMER 1
Cont Ip Hlc On
OR(3)
TIMER 2
= Virt Op 4
END

```

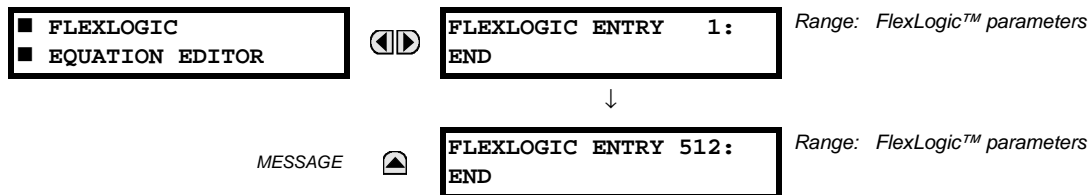
In the expression above, the Virtual Output 4 input to the 4-input OR is listed before it is created. This is typical of a form of feedback, in this case, used to create a seal-in effect with the latch, and is correct.

- The logic should always be tested after it is loaded into the relay, in the same fashion as has been used in the past. Testing can be simplified by placing an "END" operator within the overall set of FlexLogic™ equations. The equations will then only be evaluated up to the first "END" operator.

The "On" and "Off" operands can be placed in an equation to establish a known set of conditions for test purposes, and the "INSERT" and "DELETE" commands can be used to modify equations.

5.4.5 FLEXLOGIC™ EQUATION EDITOR

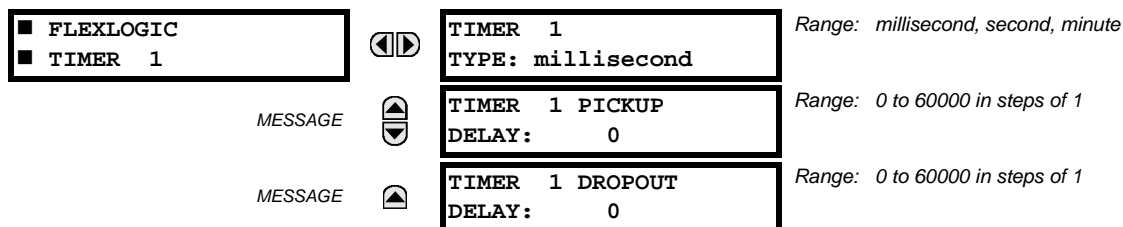
PATH: SETTINGS ⇒ FLEXLOGIC ⇒ FLEXLOGIC EQUATION EDITOR



There are 512 FlexLogic™ entries available, numbered from 1 to 512, with default 'END' entry settings. If a "Disabled" Element is selected as a FlexLogic™ entry, the associated state flag will never be set to '1'. The '+/-' key may be used when editing FlexLogic™ equations from the keypad to quickly scan through the major parameter types.

5.4.6 FLEXLOGIC™ TIMERS

PATH: SETTINGS ⇒ FLEXLOGIC ⇒ FLEXLOGIC TIMERS ⇒ FLEXLOGIC TIMER 1(32)



There are 32 identical FlexLogic™ timers available. These timers can be used as operators for FlexLogic™ equations.

- TIMER 1 TYPE:** This setting is used to select the time measuring unit.
- TIMER 1 PICKUP DELAY:** Sets the time delay to pickup. If a pickup delay is not required, set this function to "0".
- TIMER 1 DROPOUT DELAY:** Sets the time delay to dropout. If a dropout delay is not required, set this function to "0".

5.4.7 FLEXELEMENTS™

PATH: SETTING ⇒ FLEXLOGIC ⇒ FLEXELEMENTS ⇒ FLEXELEMENT 1(16)

<div> <div>■ FLEXELEMENT 1</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> </div>	<div> <div>◀▶</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲▼</div> <div>▲</div> </div>	<div>FLEXELEMENT 1</div> <div>FUNCTION: Disabled</div>	Range: Disabled, Enabled
		<div>FLEXELEMENT 1 NAME:</div> <div>FxE1</div>	Range: up to 6 alphanumeric characters
		<div>FLEXELEMENT 1 +IN:</div> <div>Off</div>	Range: Off, any analog actual value parameter
		<div>FLEXELEMENT 1 -IN:</div> <div>Off</div>	Range: Off, any analog actual value parameter
		<div>FLEXELEMENT 1 INPUT</div> <div>MODE: Signed</div>	Range: Signed, Absolute
		<div>FLEXELEMENT 1 COMP</div> <div>MODE: Level</div>	Range: Level, Delta
		<div>FLEXELEMENT 1</div> <div>DIRECTION: Over</div>	Range: Over, Under
		<div>FLEXELEMENT 1</div> <div>PICKUP: 1.000 pu</div>	Range: -90.000 to 90.000 pu in steps of 0.001
		<div>FLEXELEMENT 1</div> <div>HYSTERESIS: 3.0%</div>	Range: 0.1 to 50.0% in steps of 0.1
		<div>FLEXELEMENT 1 dt</div> <div>UNIT: milliseconds</div>	Range: milliseconds, seconds, minutes
		<div>FLEXELEMENT 1 dt:</div> <div>20</div>	Range: 20 to 86400 in steps of 1
		<div>FLEXELEMENT 1 PKP</div> <div>DELAY: 0.000 s</div>	Range: 0.000 to 65.535 s in steps of 0.001
		<div>FLEXELEMENT 1 RST</div> <div>DELAY: 0.000 s</div>	Range: 0.000 to 65.535 s in steps of 0.001
		<div>FLEXELEMENT 1 BLK:</div> <div>Off</div>	Range: FlexLogic™ operand
		<div>FLEXELEMENT 1</div> <div>TARGET: Self-reset</div>	Range: Self-reset, Latched, Disabled
		<div>FLEXELEMENT 1</div> <div>EVENTS: Disabled</div>	Range: Disabled, Enabled

A FlexElement™ is a universal comparator that can be used to monitor any analog actual value calculated by the relay or a net difference of any two analog actual values of the same type. The effective operating signal could be treated as a signed number or its absolute value could be used as per user's choice.

The element can be programmed to respond either to a signal level or to a rate-of-change (delta) over a pre-defined period of time. The output operand is asserted when the operating signal is higher than a threshold or lower than a threshold as per user's choice.

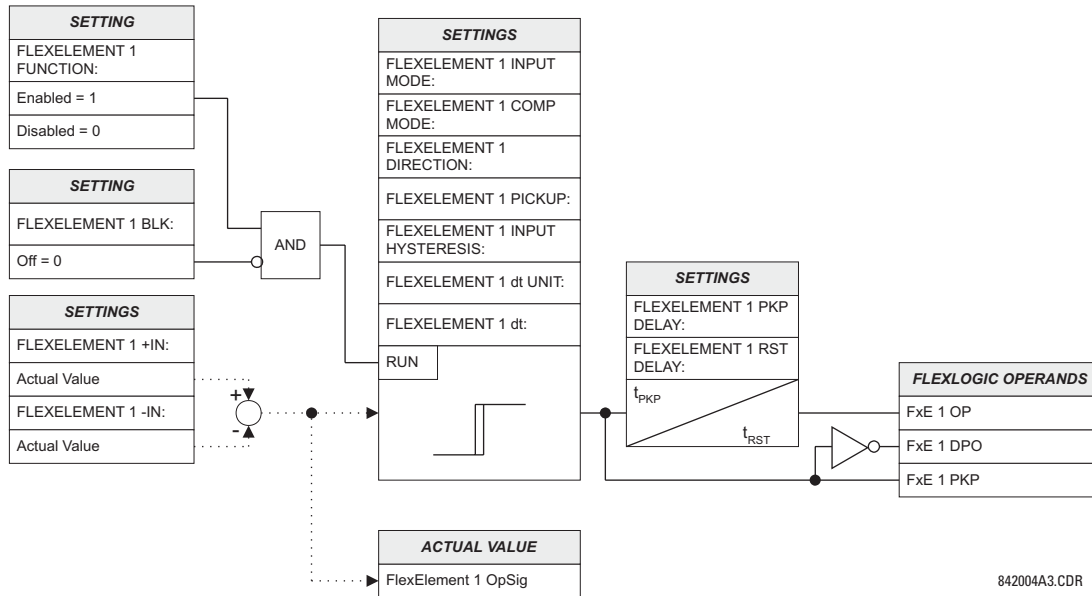


Figure 5-33: FLEXELEMENT™ SCHEME LOGIC

The **FLEXELEMENT 1 +IN** setting specifies the first (non-inverted) input to the FlexElement™. Zero is assumed as the input if this setting is set to “Off”. For proper operation of the element at least one input must be selected. Otherwise, the element will not assert its output operands.

This **FLEXELEMENT 1 –IN** setting specifies the second (inverted) input to the FlexElement™. Zero is assumed as the input if this setting is set to “Off”. For proper operation of the element at least one input must be selected. Otherwise, the element will not assert its output operands. This input should be used to invert the signal if needed for convenience, or to make the element respond to a differential signal such as for a top-bottom oil temperature differential alarm. The element will not operate if the two input signals are of different types, for example if one tries to use active power and phase angle to build the effective operating signal.

The element responds directly to the differential signal if the **FLEXELEMENT 1 INPUT MODE** setting is set to “Signed”. The element responds to the absolute value of the differential signal if this setting is set to “Absolute”. Sample applications for the “Absolute” setting include monitoring the angular difference between two phasors with a symmetrical limit angle in both directions; monitoring power regardless of its direction, or monitoring a trend regardless of whether the signal increases or decreases.

The element responds directly to its operating signal – as defined by the **FLEXELEMENT 1 +IN**, **FLEXELEMENT 1 –IN** and **FLEXELEMENT 1 INPUT MODE** settings – if the **FLEXELEMENT 1 COMP MODE** setting is set to “Level”. The element responds to the rate of change of its operating signal if the **FLEXELEMENT 1 COMP MODE** setting is set to “Delta”. In this case the **FLEXELEMENT 1 dt UNIT** and **FLEXELEMENT 1 dt** settings specify how the rate of change is derived.

The **FLEXELEMENT 1 DIRECTION** setting enables the relay to respond to either high or low values of the operating signal. The following figure explains the application of the **FLEXELEMENT 1 DIRECTION**, **FLEXELEMENT 1 PICKUP** and **FLEXELEMENT 1 HYSTERESIS** settings.

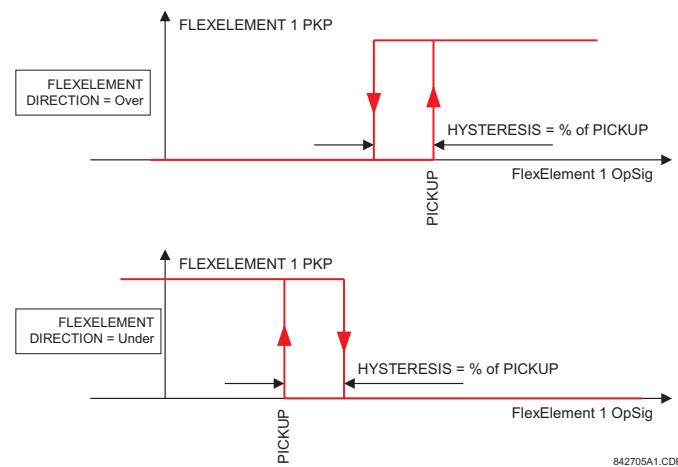


Figure 5-34: FLEXELEMENT™ DIRECTION, PICKUP, AND HYSTERESIS

In conjunction with the **FLEXELEMENT 1 INPUT MODE** setting the element could be programmed to provide two extra characteristics as shown in the figure below.

5

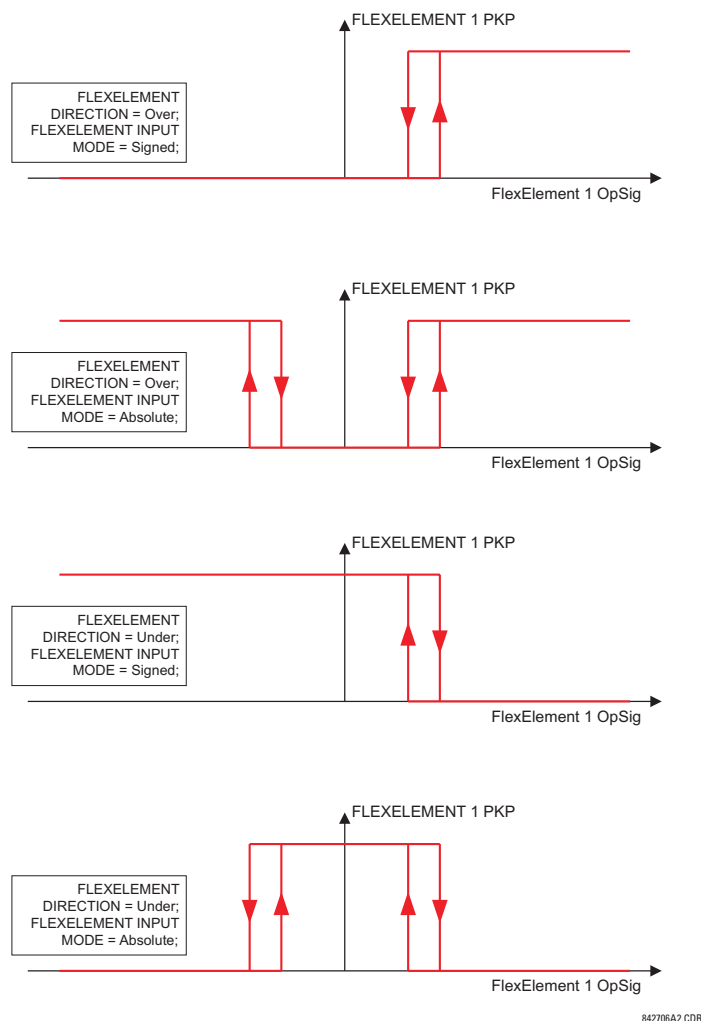


Figure 5-35: FLEXELEMENT™ INPUT MODE SETTING

The **FLEXELEMENT 1 PICKUP** setting specifies the operating threshold for the effective operating signal of the element. If set to “Over”, the element picks up when the operating signal exceeds the **FLEXELEMENT 1 PICKUP** value. If set to “Under”, the element picks up when the operating signal falls below the **FLEXELEMENT 1 PICKUP** value.

The **FLEXELEMENT 1 HYSTERESIS** setting controls the element dropout. It should be noticed that both the operating signal and the pickup threshold can be negative facilitating applications such as reverse power alarm protection. The FlexElement™ can be programmed to work with all analog actual values measured by the relay. The **FLEXELEMENT 1 PICKUP** setting is entered in per-unit values using the following definitions of the base units:

Table 5–9: FLEXELEMENT™ BASE UNITS

dcmA	BASE = maximum value of the DCMA INPUT MAX setting for the two transducers configured under the +IN and –IN inputs.
FREQUENCY	$f_{\text{BASE}} = 1 \text{ Hz}$
PHASE ANGLE	$\phi_{\text{BASE}} = 360 \text{ degrees}$ (see the UR angle referencing convention)
POWER FACTOR	$\text{PF}_{\text{BASE}} = 1.00$
RTDs	BASE = 100°C
SENSITIVE DIR POWER (Sns Dir Power)	$P_{\text{BASE}} = \text{maximum value of } 3 \times V_{\text{BASE}} \times I_{\text{BASE}} \text{ for the +IN and –IN inputs of the sources configured for the sensitive power directional element(s).}$
SOURCE CURRENT	$I_{\text{BASE}} = \text{maximum nominal primary RMS value of the +IN and –IN inputs}$
SOURCE ENERGY (Positive and Negative Watthours, Positive and Negative Varhours)	$E_{\text{BASE}} = 10000 \text{ MWh or MVAh, respectively}$
SOURCE POWER	$P_{\text{BASE}} = \text{maximum value of } V_{\text{BASE}} \times I_{\text{BASE}} \text{ for the +IN and –IN inputs}$
SOURCE VOLTAGE	$V_{\text{BASE}} = \text{maximum nominal primary RMS value of the +IN and –IN inputs}$
SYNCHROCHECK (Max Delta Volts)	$V_{\text{BASE}} = \text{maximum primary RMS value of all the sources related to the +IN and –IN inputs}$

The **FLEXELEMENT 1 HYSTERESIS** setting defines the pickup–dropout relation of the element by specifying the width of the hysteresis loop as a percentage of the pickup value as shown in the FlexElement™ Direction, Pickup, and Hysteresis diagram.

The **FLEXELEMENT 1 DT UNIT** setting specifies the time unit for the setting **FLEXELEMENT 1 dt**. This setting is applicable only if **FLEXELEMENT 1 COMP MODE** is set to “Delta”. The **FLEXELEMENT 1 DT** setting specifies duration of the time interval for the rate of change mode of operation. This setting is applicable only if **FLEXELEMENT 1 COMP MODE** is set to “Delta”.

This **FLEXELEMENT 1 PKP DELAY** setting specifies the pickup delay of the element. The **FLEXELEMENT 1 RST DELAY** setting specifies the reset delay of the element.

5.4.8 NON-VOLATILE LATCHES

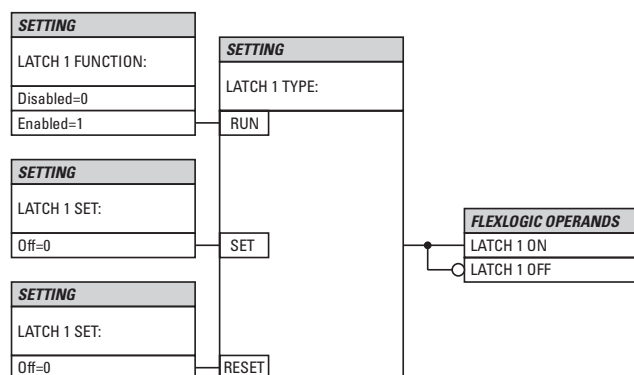
PATH: SETTINGS ⇒ FLEXLOGIC ⇒ NON-VOLATILE LATCHES ⇒ LATCH 1(16)

LATCH 1	◀▶	LATCH 1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	LATCH 1 TYPE: Reset Dominant	Range: Reset Dominant, Set Dominant
MESSAGE	▲▼	LATCH 1 SET: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	LATCH 1 RESET: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	LATCH 1 TARGET: Self-reset	Range: Self-reset, Latched, Disabled
MESSAGE	▲	LATCH 1 EVENTS: Disabled	Range: Disabled, Enabled

The non-volatile latches provide a permanent logical flag that is stored safely and will not reset upon reboot after the relay is powered down. Typical applications include sustaining operator commands or permanently block relay functions, such as Autorecloser, until a deliberate HMI action resets the latch. The settings, logic, and element operation are described below:

- **LATCH 1 TYPE:** This setting characterizes Latch 1 to be Set- or Reset-dominant.
- **LATCH 1 SET:** If asserted, the specified FlexLogic™ operands 'sets' Latch 1.
- **LATCH 1 RESET:** If asserted, the specified FlexLogic™ operand 'resets' Latch 1.

LATCH N TYPE	LATCH N SET	LATCH N RESET	LATCH N ON	LATCH N OFF
Reset Dominant	ON	OFF	ON	OFF
	OFF	OFF	Previous State	Previous State
	ON	ON	OFF	ON
	OFF	ON	OFF	ON
Set Dominant	ON	OFF	ON	OFF
	ON	ON	ON	OFF
	OFF	OFF	Previous State	Previous State
	OFF	ON	OFF	ON



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Figure 5–36: NON-VOLATILE LATCH OPERATION TABLE (N=1 to 16) AND LOGIC

5.5.1 OVERVIEW

Each protection element can be assigned up to six different sets of settings according to Setting Group designations 1 to 6. The performance of these elements is defined by the active Setting Group at a given time. Multiple setting groups allow the user to conveniently change protection settings for different operating situations (e.g. altered power system configuration, season of the year). The active setting group can be preset or selected via the **SETTING GROUPS** menu (see the *Control Elements* section later in this chapter). See also the *Introduction to Elements* section at the beginning of this chapter.

5.5.2 SETTING GROUP

PATH: SETTINGS ⇒ ↓ GROUPED ELEMENTS ⇒ SETTING GROUP 1(6)

■ SETTING GROUP 1	◀▶	■ POWER SWING ■ DETECT	See below.
MESSAGE	▲▼	■ PHASE CURRENT ■	See page 5-94.
MESSAGE	▲▼	■ VOLTAGE ELEMENTS ■	See page 5-95.
MESSAGE	▲▼	■ SUPERVISING ■ ELEMENTS	See page 5-98.
MESSAGE	▲▼	■ SENSITIVE ■ DIRECTIONAL POWER	See page 5-100.

Each of the six Setting Group menus is identical. **SETTING GROUP 1** (the default active group) automatically becomes active if no other group is active (see the *Control Elements* section for additional details).

5.5.3 POWER SWING DETECT

PATH: SETTINGS ⇒ ↓ GROUPED ELEMENTS ⇒ SETTING GROUP 1(6) ⇒ ↓ POWER SWING DETECT

■ POWER SWING ■ DETECT	◀▶	POWER SWING FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	POWER SWING SOURCE: SRC 1	Range: SRC 1, SRC 2
MESSAGE	▲▼	POWER SWING SHAPE: Mho Shape	Range: Mho Shape, Quad Shape
MESSAGE	▲▼	POWER SWING MODE: Two Step	Range: Two Step, Three Step
MESSAGE	▲▼	POWER SWING SUPV: 0.600 pu	Range: 0.050 to 30.000 pu in steps of 0.001
MESSAGE	▲▼	POWER SWING FWD REACH: 50.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE	▲▼	POWER SWING QUAD FWD REACH MID: 60.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE	▲▼	POWER SWING QUAD FWD REACH OUT: 70.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE	▲▼	POWER SWING FWD RCA: 75°	Range: 40 to 90° in steps of 1
MESSAGE	▲▼	POWER SWING REV REACH: 50.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01

MESSAGE		POWER SWING QUAD REV REACH MID: 60.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING QUAD REV REACH OUT: 70.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING REV RCA: 75°	Range: 40 to 90° in steps of 1
MESSAGE		POWER SWING OUTER LIMIT ANGLE: 120°	Range: 40 to 140° in steps of 1
MESSAGE		POWER SWING MIDDLE LIMIT ANGLE: 90°	Range: 40 to 140° in steps of 1
MESSAGE		POWER SWING INNER LIMIT ANGLE: 60°	Range: 40 to 140° in steps of 1
MESSAGE		POWER SWING OUTER RGT BLD: 100.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING OUTER LFT BLD: 100.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING MIDDLE RGT BLD: 100.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING MIDDLE LFT BLD: 100.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING INNER RGT BLD: 100.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING INNER LFT BLD: 100.00 Ω	Range: 0.10 to 500.00 Ω in steps of 0.01
MESSAGE		POWER SWING PICKUP DELAY 1: 0.030 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE		POWER SWING RESET DELAY 1: 0.050 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE		POWER SWING PICKUP DELAY 2: 0.017 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE		POWER SWING PICKUP DELAY 3: 0.009 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE		POWER SWING PICKUP DELAY 4: 0.017 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE		POWER SWING SEAL-IN DELAY: 0.400 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE		POWER SWING TRIP MODE: Delayed	Range: Early, Delayed
MESSAGE		POWER SWING BLK: Off	Range: Flexlogic™ operand
MESSAGE		POWER SWING TARGET: Self-Reset	Range: Self-Reset, Latched, Disabled
MESSAGE		POWER SWING EVENTS: Disabled	Range: Disabled, Enabled

The Power Swing Detect element provides both power swing blocking and out-of-step tripping functions. The element measures the positive-sequence apparent impedance and traces its locus with respect to either two or three user-selectable operating characteristic boundaries. Upon detecting appropriate timing relations, the blocking and/or tripping indication is given through FlexLogic™ operands. The element incorporates an adaptive disturbance detector. This function does not trigger on power swings, but is capable of detecting faster disturbances – faults in particular – that may occur during power swings. Operation of this dedicated disturbance detector is signaled via the POWER SWING 50DD operand.

The Power Swing Detect element asserts two outputs intended for blocking selected protection elements on power swings: POWER SWING BLOCK is a traditional signal that is safely asserted for the entire duration of the power swing, and POWER SWING UN/BLOCK is established in the same way, but resets when an extra disturbance is detected during the power swing. The POWER SWING UN/BLOCK operand may be used for blocking selected protection elements if the intent is to respond to faults during power swing conditions.

Different protection elements respond differently to power swings. If tripping is required for faults during power swing conditions, some elements may be blocked permanently (using the POWER SWING BLOCK operand), and others may be blocked and dynamically unblocked upon fault detection (using the POWER SWING UN/BLOCK operand).

The operating characteristic and logic figures should be viewed along with the following discussion to develop an understanding of the operation of the element.

The Power Swing Detect element operates in three-step or two-step mode:

- **Three-step operation:** The power swing blocking sequence essentially times the passage of the locus of the positive-sequence impedance between the outer and the middle characteristic boundaries. If the locus enters the outer characteristic (indicated by the POWER SWING OUTER FlexLogic™ operand) but stays outside the middle characteristic (indicated by the POWER SWING MIDDLE FlexLogic™ operand) for an interval longer than **POWER SWING PICKUP DELAY 1**, the power swing blocking signal (POWER SWING BLOCK FlexLogic™ operand) is established and sealed-in. The blocking signal resets when the locus leaves the outer characteristic, but not sooner than the **POWER SWING RESET DELAY 1** time.
- **Two-step operation:** If the 2-step mode is selected, the sequence is identical, but it is the outer and inner characteristics that are used to time the power swing locus.

The Out-of-Step Tripping feature operates as follows for three-step and two-step Power Swing Detection modes:

- **Three-step operation:** The out-of-step trip sequence identifies unstable power swings by determining if the impedance locus spends a finite time between the outer and middle characteristics and then a finite time between the middle and inner characteristics. The first step is similar to the power swing blocking sequence. After timer **POWER SWING PICKUP DELAY 1** times out, Latch 1 is set as long as the impedance stays within the outer characteristic.

If afterwards, at any time (given the impedance stays within the outer characteristic), the locus enters the middle characteristic but stays outside the inner characteristic for a period of time defined as **POWER SWING PICKUP DELAY 2**, Latch 2 is set as long as the impedance stays inside the outer characteristic. If afterwards, at any time (given the impedance stays within the outer characteristic), the locus enters the inner characteristic and stays there for a period of time defined as **POWER SWING PICKUP DELAY 3**, Latch 2 is set as long as the impedance stays inside the outer characteristic; the element is now ready to trip.

If the "Early" trip mode is selected, the POWER SWING TRIP operand is set immediately and sealed-in for the interval set by the **POWER SWING SEAL-IN DELAY**. If the "Delayed" trip mode is selected, the element waits until the impedance locus leaves the inner characteristic, then times out the **POWER SWING PICKUP DELAY 2** and sets Latch 4; the element is now ready to trip. The trip operand is set later, when the impedance locus leaves the outer characteristic.

- **Two-step operation:** The 2-step mode of operation is similar to the 3-step mode with two exceptions. First, the initial stage monitors the time spent by the impedance locus between the outer and inner characteristics. Second, the stage involving the **POWER SWING PICKUP DELAY 2** timer is bypassed. It is up to the user to integrate the blocking (POWER SWING BLOCK) and tripping (POWER SWING TRIP) FlexLogic™ operands with other protection functions and output contacts in order to make this element fully operational.

The element can be set to use either lens (mho) or rectangular (quad) characteristics as illustrated below. When set to "Mho", the element applies the right and left blinders as well. If the blinders are not required, their settings should be set high enough to effectively disable the blinders.

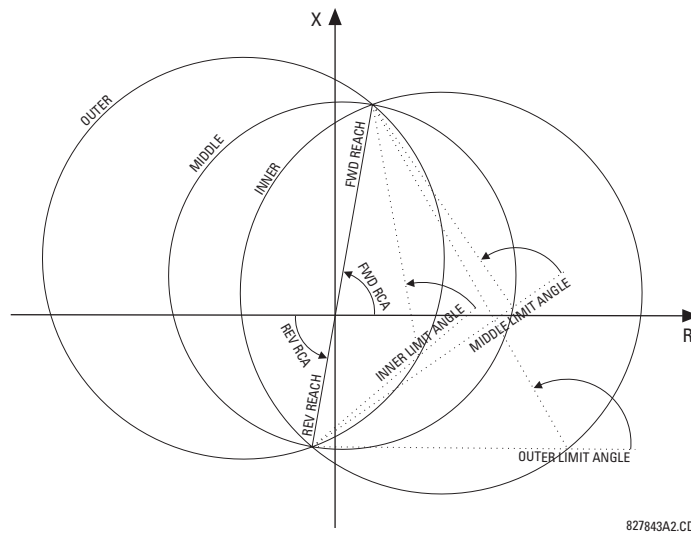


Figure 5-37: POWER SWING DETECT MHO OPERATING CHARACTERISTICS

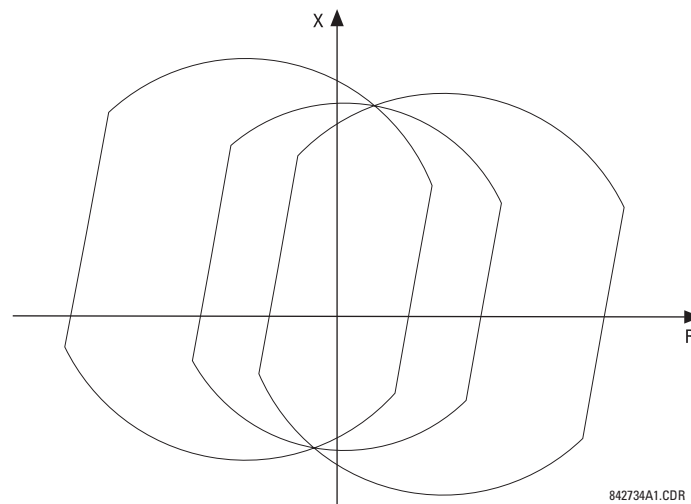


Figure 5-38: EFFECTS OF BLINDERS ON THE MHO CHARACTERISTICS

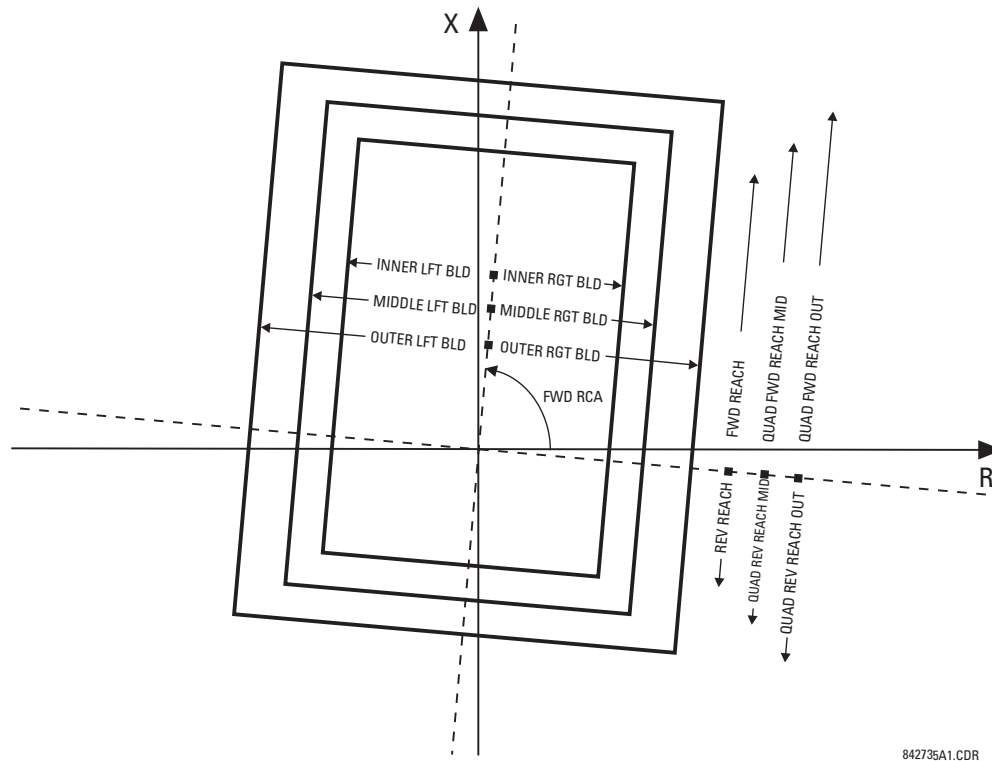


Figure 5-39: POWER SWING DETECT QUAD OPERATING CHARACTERISTICS

The FlexLogic™ output operands for the Power Swing Detect element are described below:

- The POWER SWING OUTER, POWER SWING MIDDLE, POWER SWING INNER, POWER SWING TMR2 PKP, POWER SWING TMR3 PKP, and POWER SWING TMR4 PKP FlexLogic™ operands are auxiliary operands that could be used to facilitate testing and special applications.
- The POWER SWING BLOCK FlexLogic™ operand shall be used to block selected protection elements such as distance functions.
- The POWER SWING UN/BLOCK FlexLogic™ operand shall be used to block those protection elements that are intended to be blocked under power swings, but subsequently unblocked should a fault occur after the power swing blocking condition has been established.
- The POWER SWING 50DD FlexLogic™ operand indicates that an adaptive disturbance detector integrated with the element has picked up. This operand will trigger on faults occurring during power swing conditions. This includes both three-phase and single-pole-open conditions.
- The POWER SWING INCOMING FlexLogic™ operand indicates an unstable power swing with an incoming locus (the locus enters the inner characteristic).
- The POWER SWING OUTGOING FlexLogic™ operand indicates an unstable power swing with an outgoing locus (the locus leaving the outer characteristic). This operand can be used to count unstable swings and take certain action only after pre-defined number of unstable power swings.
- The POWER SWING TRIP FlexLogic™ operand is a trip command.

The settings for the Power Swing Detect element are described below:

- **POWER SWING FUNCTION:** This setting enables/disables the entire Power Swing Detection element. The setting applies to both power swing blocking and out-of-step tripping functions.
- **POWER SWING SOURCE:** The source setting identifies the Signal Source for both blocking and tripping functions.
- **POWER SWING SHAPE:** This setting selects the shapes (either “Mho” or “Quad”) of the outer, middle and, inner characteristics of the power swing detect element. The operating principle is not affected. The “Mho” characteristics use the left and right blinders.

- **POWER SWING MODE:** This setting selects between the 2-step and 3-step operating modes and applies to both power swing blocking and out-of-step tripping functions. The 3-step mode applies if there is enough space between the maximum load impedances and distance characteristics of the relay that all three (outer, middle, and inner) characteristics can be placed between the load and the distance characteristics. Whether the spans between the outer and middle as well as the middle and inner characteristics are sufficient should be determined by analysis of the fastest power swings expected in correlation with settings of the power swing timers.

The 2-step mode uses only the outer and inner characteristics for both blocking and tripping functions. This leaves more space in heavily loaded systems to place two power swing characteristics between the distance characteristics and the maximum load, but allows for only one determination of the impedance trajectory.

- **POWER SWING SUPV:** A common overcurrent pickup level supervises all three power swing characteristics. The supervision responds to the positive sequence current.
- **POWER SWING FWD REACH:** This setting specifies the forward reach of all three mho characteristics and the inner quad characteristic. For a simple system consisting of a line and two equivalent sources, this reach should be higher than the sum of the line and remote source positive-sequence impedances. Detailed transient stability studies may be needed for complex systems in order to determine this setting. The angle of this reach impedance is specified by the **POWER SWING FWD RCA** setting.
- **POWER SWING QUAD FWD REACH MID:** This setting specifies the forward reach of the middle quad characteristic. The angle of this reach impedance is specified by the **POWER SWING FWD RCA** setting. The setting is not used if the shape setting is "Mho".
- **POWER SWING QUAD FWD REACH OUT:** This setting specifies the forward reach of the outer quad characteristic. The angle of this reach impedance is specified by the **POWER SWING FWD RCA** setting. The setting is not used if the shape setting is "Mho".
- **POWER SWING FWD RCA:** This setting specifies the angle of the forward reach impedance for the mho characteristics, angles of all the blinders, and both forward and reverse reach impedances of the quad characteristics.
- **POWER SWING REV REACH:** This setting specifies the reverse reach of all three mho characteristics and the inner quad characteristic. For a simple system of a line and two equivalent sources, this reach should be higher than the positive-sequence impedance of the local source. Detailed transient stability studies may be needed for complex systems to determine this setting. The angle of this reach impedance is specified by the **POWER SWING REV RCA** setting for "Mho", and the **POWER SWING FWD RCA** setting for "Quad".
- **POWER SWING QUAD REV REACH MID:** This setting specifies the reverse reach of the middle quad characteristic. The angle of this reach impedance is specified by the **POWER SWING FWD RCA** setting. The setting is not used if the shape setting is "Mho".
- **POWER SWING QUAD REV REACH OUT:** This setting specifies the reverse reach of the outer quad characteristic. The angle of this reach impedance is specified by the **POWER SWING FWD RCA** setting. The setting is not used if the shape setting is "Mho".
- **POWER SWING REV RCA:** This setting specifies the angle of the reverse reach impedance for the mho characteristics. This setting applies to mho shapes only.
- **POWER SWING OUTER LIMIT ANGLE:** This setting defines the outer power swing characteristic. The convention depicted in the Power Swing Detect Characteristic diagram should be observed: values greater than 90° result in an 'apple' shaped characteristic; values less than 90° result in a lens shaped characteristic. This angle must be selected in consideration of the maximum expected load. If the maximum load angle is known, the outer limit angle should be coordinated with a 20° security margin. Detailed studies may be needed for complex systems to determine this setting. This setting applies to mho shapes only.
- **POWER SWING MIDDLE LIMIT ANGLE:** This setting defines the middle power swing detect characteristic. It is relevant only for the 3-step mode. A typical value would be close to the average of the outer and inner limit angles. This setting applies to mho shapes only.
- **POWER SWING INNER LIMIT ANGLE:** This setting defines the inner power swing detect characteristic. The inner characteristic is used by the out-of-step tripping function: beyond the inner characteristic out-of-step trip action is definite (the actual trip may be delayed as per the **TRIP MODE** setting). Therefore, this angle must be selected in consideration to the power swing angle beyond which the system becomes unstable and cannot recover.

The inner characteristic is also used by the power swing blocking function in the 2-step mode. In this case, set this angle large enough so that the characteristics of the distance elements are safely enclosed by the inner characteristic. This setting applies to mho shapes only.

- **POWER SWING OUTER, MIDDLE, and INNER RGT BLD:** These settings specify the resistive reach of the right blinder. The blinder applies to both “Mho” and “Quad” characteristics. Set these value high if no blinder is required for the “Mho” characteristic.
- **POWER SWING OUTER, MIDDLE, and INNER LFT BLD:** These settings specify the resistive reach of the left blinder. Enter a positive value; the relay automatically uses a negative value. The blinder applies to both “Mho” and “Quad” characteristics. Set this value high if no blinder is required for the “Mho” characteristic.
- **POWER SWING PICKUP DELAY 1:** All the coordinating timers are related to each other and should be set to detect the fastest expected power swing and produce out-of-step tripping in a secure manner. The timers should be set in consideration to the power swing detect characteristics, mode of power swing detect operation and mode of out-of-step tripping. This timer defines the interval that the impedance locus must spend between the outer and inner characteristics (2-step operating mode), or between the outer and middle characteristics (3-step operating mode) before the power swing blocking signal is established. This time delay must be set shorter than the time required for the impedance locus to travel between the two selected characteristics during the fastest expected power swing. This setting is relevant for both power swing blocking and out-of-step tripping.
- **POWER SWING RESET DELAY 1:** This setting defines the dropout delay for the power swing blocking signal. Detection of a condition requiring a Block output sets Latch 1 after **PICKUP DELAY 1** time. When the impedance locus leaves the outer characteristic, timer **POWER SWING RESET DELAY 1** is started. When the timer times-out the latch is reset. This setting should be selected to give extra security for the power swing blocking action.
- **POWER SWING PICKUP DELAY 2:** Controls the out-of-step tripping function in the 3-step mode only. This timer defines the interval the impedance locus must spend between the middle and inner characteristics before the second step of the out-of-step tripping sequence is completed. This time delay must be set shorter than the time required for the impedance locus to travel between the two characteristics during the fastest expected power swing.
- **POWER SWING PICKUP DELAY 3:** Controls the out-of-step tripping function only. It defines the interval the impedance locus must spend within the inner characteristic before the last step of the out-of-step tripping sequence is completed and the element is armed to trip. The actual moment of tripping is controlled by the **TRIP MODE** setting. This time delay is provided for extra security before the out-of-step trip action is executed.
- **POWER SWING PICKUP DELAY 4:** Controls the out-of-step tripping function in “Delayed” trip mode only. This timer defines the interval the impedance locus must spend outside the inner characteristic but within the outer characteristic before the element is armed for the delayed trip. The delayed trip occurs when the impedance leaves the outer characteristic. This time delay is provided for extra security and should be set considering the fastest expected power swing.
- **POWER SWING SEAL-IN DELAY:** The out-of-step trip FlexLogic™ operand (POWER SWING TRIP) is sealed-in for the specified period of time. The sealing-in is crucial in the delayed trip mode, as the original trip signal is a very short pulse occurring when the impedance locus leaves the outer characteristic after the out-of-step sequence is completed.
- **POWER SWING TRIP MODE:** Selection of the “Early” trip mode results in an instantaneous trip after the last step in the out-of-step tripping sequence is completed. The Early trip mode will stress the circuit breakers as the currents at that moment are high (the electromotive forces of the two equivalent systems are approximately 180° apart). Selection of the “Delayed” trip mode results in a trip at the moment when the impedance locus leaves the outer characteristic. Delayed trip mode will relax the operating conditions for the breakers as the currents at that moment are low. The selection should be made considering the capability of the breakers in the system.
- **POWER SWING BLK:** This setting specifies the FlexLogic™ operand used for blocking the out-of-step function only. The power swing blocking function is operational all the time as long as the element is enabled. The blocking signal resets the output POWER SWING TRIP operand but does not stop the out-of-step tripping sequence.

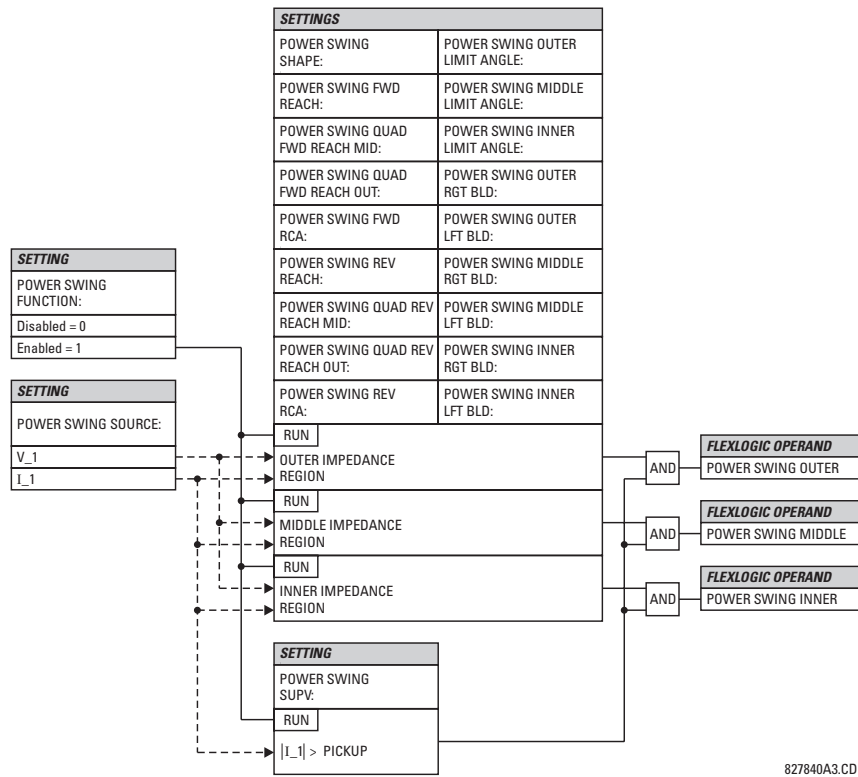


Figure 5-40: POWER SWING DETECT SCHEME LOGIC (1 of 3)

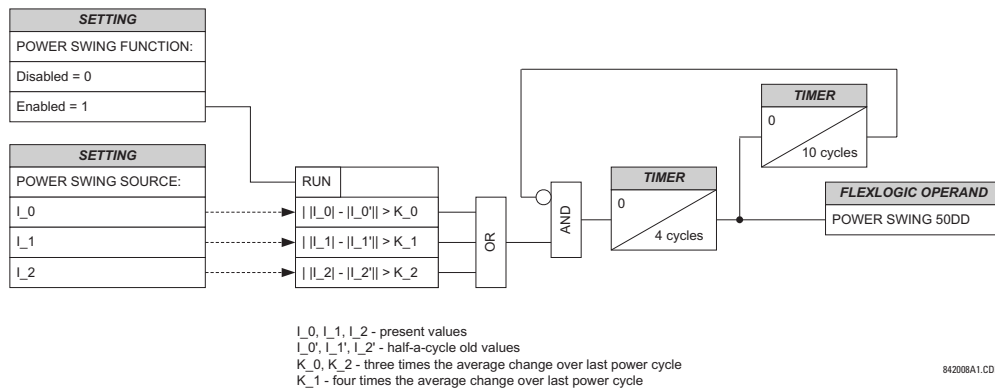


Figure 5-41: POWER SWING DETECT SCHEME LOGIC (2 of 3)

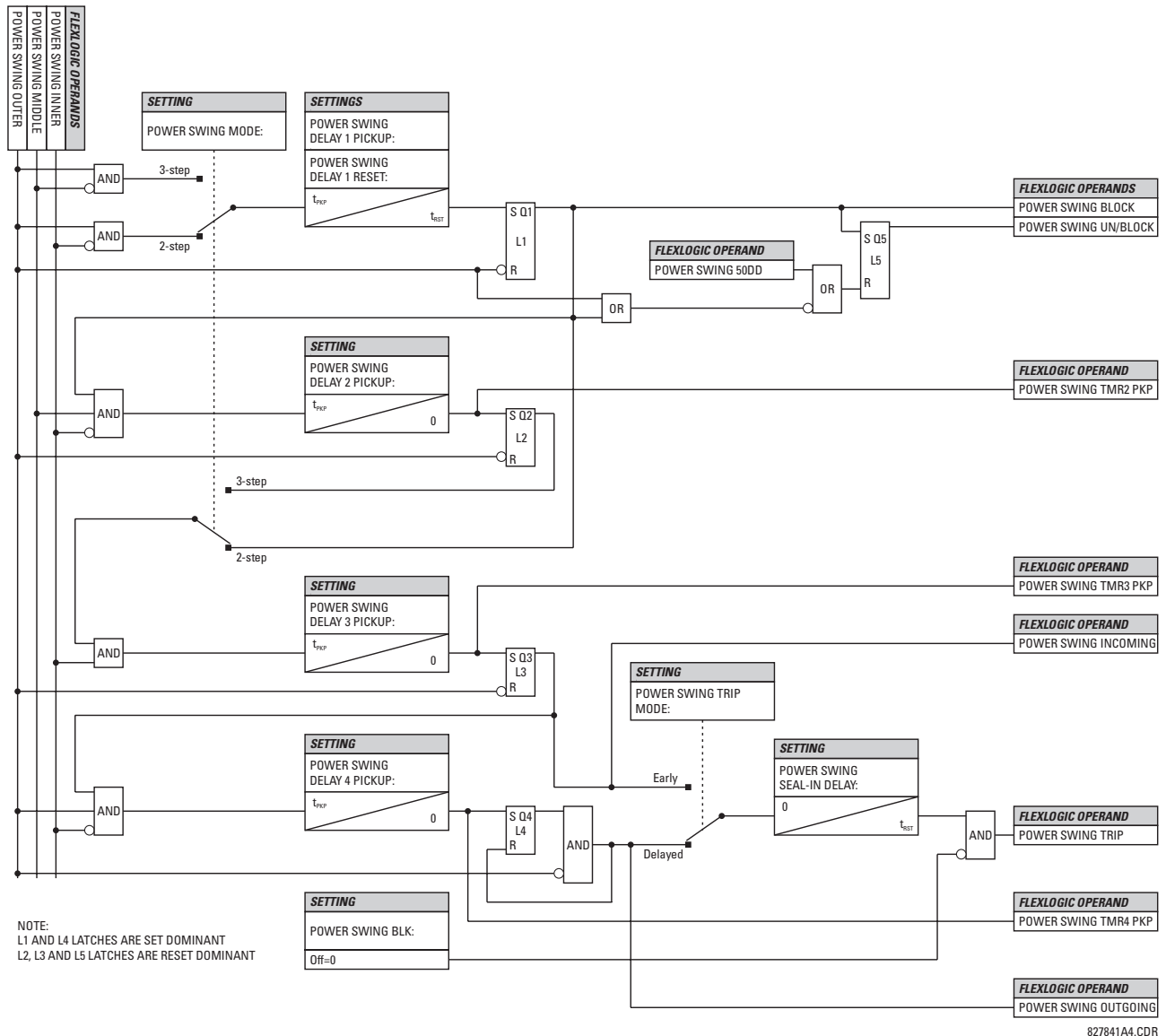


Figure 5–42: POWER SWING DETECT SCHEME LOGIC (3 of 3)

5.5.4 PHASE CURRENT

a) PHASE INSTANTANEOUS OVERCURRENT (ANSI 50P)

PATH: SETTINGS ⇒ GROUPED ELEMENTS ⇒ SETTING GROUP 1(6) ⇒ PHASE CURRENT ⇒ PHASE IOC 1

■ PHASE IOC1		PHASE IOC1	Range: Disabled, Enabled
■		FUNCTION: Disabled	
MESSAGE	▲▼	PHASE IOC1 SIGNAL	Range: SRC 1, SRC 2
		SOURCE: SRC 1	
MESSAGE	▲▼	PHASE IOC1 PICKUP	Range: 0.000 to 30.000 pu in steps of 0.001
		PICKUP: 1.000 pu	
MESSAGE	▲▼	PHASE IOC1 PICKUP	Range: 0.00 to 600.00 s in steps of 0.01
		DELAY: 0.00 s	
MESSAGE	▲▼	PHASE IOC1 RESET	Range: 0.00 to 600.00 s in steps of 0.01
		DELAY: 0.00 s	
MESSAGE	▲▼	PHASE IOC1 BLOCK A:	Range: FlexLogic™ operand
		Off	
MESSAGE	▲▼	PHASE IOC1 BLOCK B:	Range: FlexLogic™ operand
		Off	
MESSAGE	▲▼	PHASE IOC1 BLOCK C:	Range: FlexLogic™ operand
		Off	
MESSAGE	▲▼	PHASE IOC1	Range: Self-reset, Latched, Disabled
		TARGET: Self-reset	
MESSAGE	▲	PHASE IOC1	Range: Disabled, Enabled
		EVENTS: Disabled	

The phase instantaneous overcurrent element may be used as an instantaneous element with no intentional delay or as a Definite Time element. The input current is the fundamental phasor magnitude.

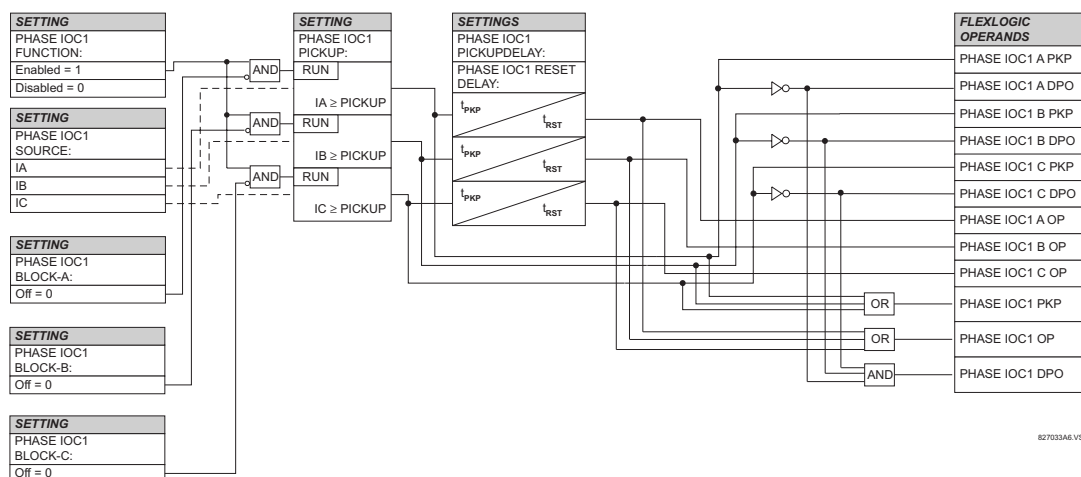
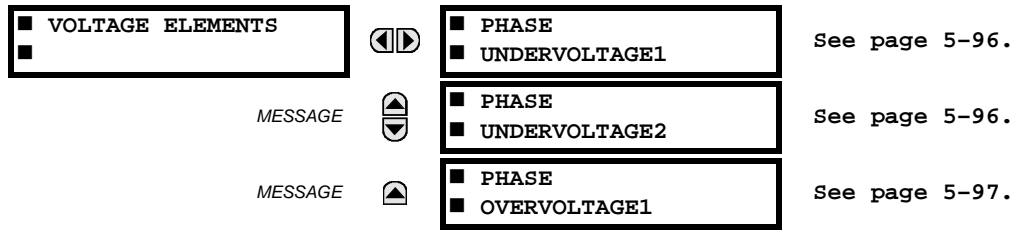


Figure 5-43: PHASE IOC1 SCHEME LOGIC

5.5.5 VOLTAGE ELEMENTS

a) MAIN MENU

PATH: SETTINGS ⇒ GROUPED ELEMENTS ⇒ SETTING GROUP 1(6) ⇒ VOLTAGE ELEMENTS



These protection elements can be used for a variety of applications such as:

Undervoltage Protection: For voltage sensitive loads, such as induction motors, a drop in voltage increases the drawn current which may cause dangerous overheating in the motor. The undervoltage protection feature can be used to either cause a trip or generate an alarm when the voltage drops below a specified voltage setting for a specified time delay.

Permissive Functions: The undervoltage feature may be used to block the functioning of external devices by operating an output relay when the voltage falls below the specified voltage setting. The undervoltage feature may also be used to block the functioning of other elements through the block feature of those elements.

Source Transfer Schemes: In the event of an undervoltage, a transfer signal may be generated to transfer a load from its normal source to a standby or emergency power source.

The undervoltage elements can be programmed to have a Definite Time delay characteristic. The Definite Time curve operates when the voltage drops below the pickup level for a specified period of time. The time delay is adjustable from 0 to 600.00 seconds in steps of 10 ms. The undervoltage elements can also be programmed to have an inverse time delay characteristic. The undervoltage delay setting defines the family of curves shown below.

$$T = \frac{D}{\left(1 - \frac{V}{V_{pickup}}\right)}$$

where: T = Operating Time
 D = Undervoltage Delay Setting
 ($D = 0.00$ operates instantaneously)
 V = Secondary Voltage applied to the relay
 V_{pickup} = Pickup Level



At 0% of pickup, the operating time equals the UNDERVOLTAGE DELAY setting.

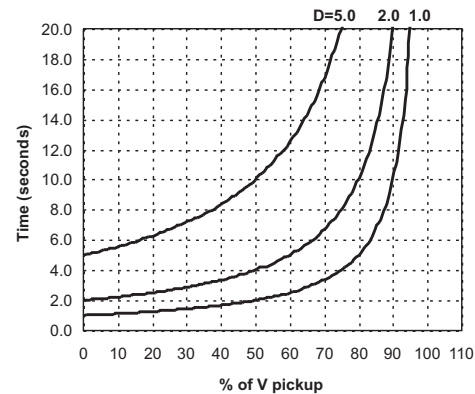


Figure 5-44: INVERSE TIME UNDERVOLTAGE CURVES

b) PHASE UNDERVOLTAGE (ANSI 27P)

PATH: SETTINGS ⇒ GROUPED ELEMENTS ⇒ SETTING GROUP 1(6) ⇒ VOLTAGE ELEMENTS ⇒ PHASE UNDERVOLTAGE1(2)

■ PHASE	◀▶	PHASE UV1	Range: Disabled, Enabled
■ UNDERVOLTAGE1		FUNCTION: Disabled	
MESSAGE	▲▼	PHASE UV1 SIGNAL	Range: SRC 1, SRC 2
		SOURCE: SRC 1	
MESSAGE	▲▼	PHASE UV1 MODE:	Range: Phase to Ground, Phase to Phase
		Phase to Ground	
MESSAGE	▲▼	PHASE UV1	Range: 0.000 to 3.000 pu in steps of 0.001
		PICKUP: 1.000 pu	
MESSAGE	▲▼	PHASE UV1	Range: Definite Time, Inverse Time
		CURVE: Definite Time	
MESSAGE	▲▼	PHASE UV1	Range: 0.00 to 600.00 s in steps of 0.01
		DELAY: 1.00 s	
MESSAGE	▲▼	PHASE UV1 MINIMUM	Range: 0.000 to 3.000 pu in steps of 0.001
		VOLTAGE: 0.100 pu	
MESSAGE	▲▼	PHASE UV1 BLOCK:	Range: FlexLogic™ operand
		Off	
MESSAGE	▲▼	PHASE UV1	Range: Self-reset, Latched, Disabled
		TARGET: Self-reset	
MESSAGE	▲	PHASE UV1	Range: Disabled, Enabled
		EVENTS: Disabled	

This element may be used to give a desired time-delay operating characteristic versus the applied fundamental voltage (phase-to-ground or phase-to-phase for Wye VT connection, or phase-to-phase for Delta VT connection) or as a Definite Time element. The element resets instantaneously if the applied voltage exceeds the dropout voltage. The delay setting selects the minimum operating time of the phase undervoltage. The minimum voltage setting selects the operating voltage below which the element is blocked (a setting of "0" will allow a dead source to be considered a fault condition).

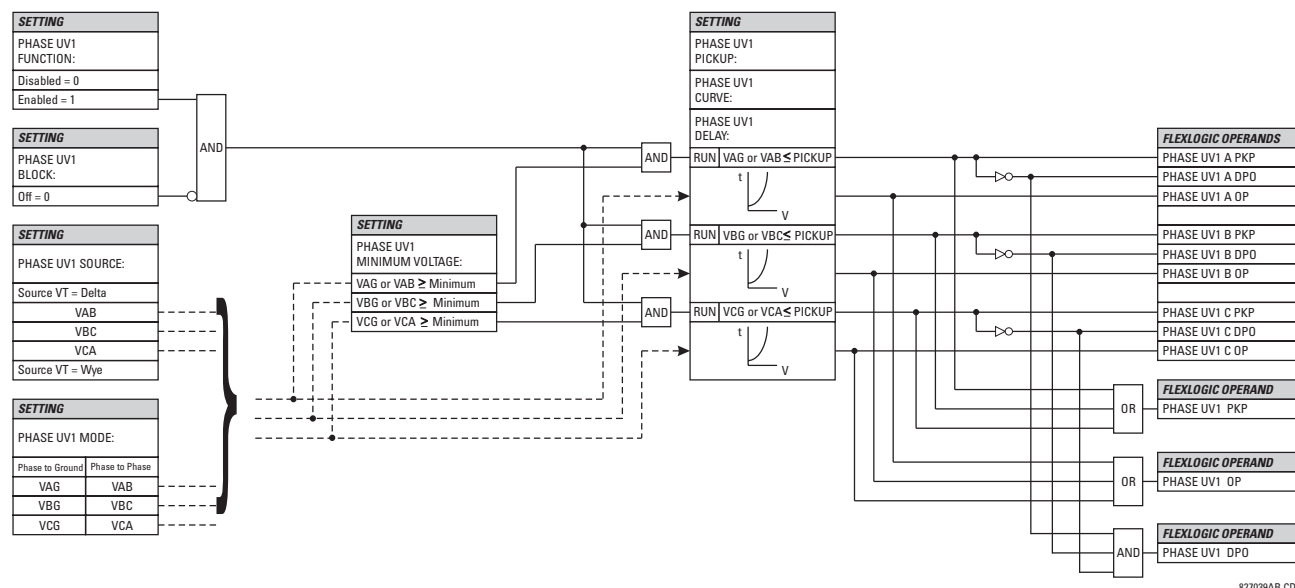


Figure 5-45: PHASE UNDERVOLTAGE1 SCHEME LOGIC

827039AB.CDR

c) PHASE OVERVOLTAGE (ANSI 59P)

PATH: SETTINGS ⇒ GROUPED ELEMENTS ⇒ SETTING GROUP 1(6) ⇒ VOLTAGE ELEMENTS ⇒ PHASE OVERVOLTAGE1

■ PHASE		PHASE OV1	Range: Disabled, Enabled
■ OVERVOLTAGE1		FUNCTION: Disabled	
MESSAGE	▲▼	PHASE OV1 SIGNAL	Range: SRC 1, SRC 2
		SOURCE: SRC 1	
MESSAGE	▲▼	PHASE OV1	Range: 0.000 to 3.000 pu in steps of 0.001
		PICKUP: 1.000 pu	
MESSAGE	▲▼	PHASE OV1 PICKUP	Range: 0.00 to 600.00 s in steps of 0.01
		DELAY: 1.00 s	
MESSAGE	▲▼	PHASE OV1 RESET	Range: 0.00 to 600.00 s in steps of 0.01
		DELAY: 1.00 s	
MESSAGE	▲▼	PHASE OV1 BLOCK:	Range: FlexLogic™ Operand
		Off	
MESSAGE	▲▼	PHASE OV1	Range: Self-reset, Latched, Disabled
		TARGET: Self-reset	
MESSAGE	▲	PHASE OV1	Range: Disabled, Enabled
		EVENTS: Disabled	

The phase overvoltage element may be used as an instantaneous element with no intentional time delay or as a Definite Time element. The input voltage is the phase-to-phase voltage, either measured directly from Delta-connected VTs or as calculated from phase-to-ground (Wye) connected VTs. The specific voltages to be used for each phase are shown below.

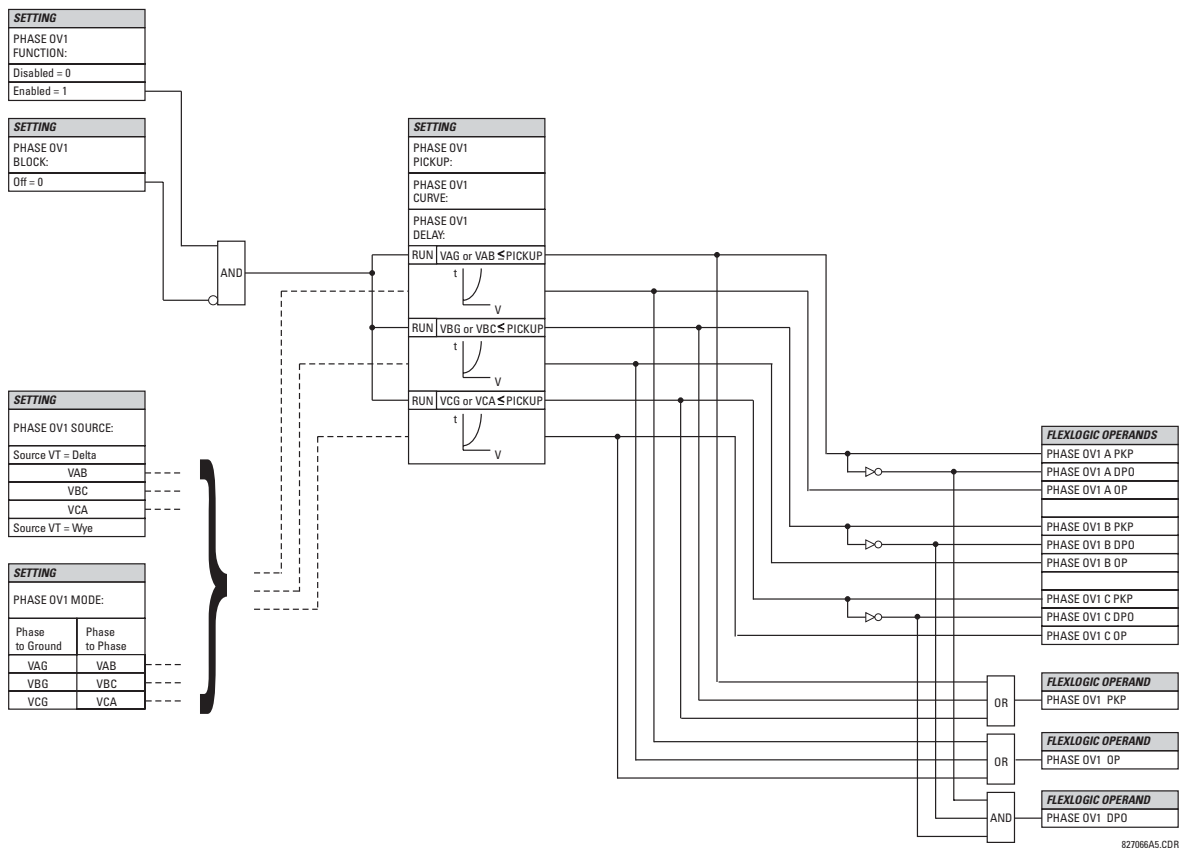


Figure 5-46: PHASE OVERVOLTAGE SCHEME LOGIC

5.5.6 SUPERVISING ELEMENTS

a) OPEN POLE DETECTOR

PATH: SETTINGS ⇒ ↓ GROUPED ELEMENTS ⇒ ↓ SETTING GROUP 1(6) ⇒ ↓ SUPERVISING ELEMENTS ⇒ ↓ OPEN POLE DETECTOR

■ OPEN POLE DETECTOR		◀▶	OPEN POLE FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼		OPEN POLE BLOCK: Off	Range: FlexLogic™ operand
MESSAGE	▲▼		OPEN POLE CURRENT SOURCE: SRC 1	Range: SRC 1, SRC 2
MESSAGE	▲▼		OPEN POLE CURRENT PKP: 0.20 pu	Range: 0.05 to 20.00 pu in steps of 0.01
MESSAGE	▲▼		OPEN POLE BROKEN CONDUCTOR: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼		OPEN POLE VOLTAGE INPUT: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼		OPEN POLE VOLTAGE SOURCE: SRC 1	Range: SRC 1, SRC 2
MESSAGE	▲▼		OPEN POLE φA AUX CO: Off	Range: FlexLogic™ operand
MESSAGE	▲▼		OPEN POLE φB AUX CO: Off	Range: FlexLogic™ operand
MESSAGE	▲▼		OPEN POLE φC AUX CO: Off	Range: FlexLogic™ operand
MESSAGE	▲▼		OPEN POLE PICKUP DELAY: 0.060 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE	▲▼		OPEN POLE RESET DELAY: 0.100 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE	▲▼		OPEN POLE TARGET: Self-reset	Range: Self-reset, Latched, Disabled
MESSAGE	▲		OPEN POLE EVENTS: Disabled	Range: Disabled, Enabled

The Open Pole Detector logic is designed to detect if any pole of the associated circuit breaker is opened or the conductor is broken on the protected power line and cable. The output FlexLogic™ operands can be used in three phase and single phase tripping schemes, in reclosing schemes, in blocking some elements (like CT failure) and in signaling or indication schemes. In single-pole tripping schemes, if OPEN POLE flag is set, any other subsequent fault should cause a three-phase trip regardless of fault type.

This element's logic is built on detecting absence of current in one phase during presence of current in other phases. Phases A, B and C breaker auxiliary contacts (if available) are used in addition to make a logic decision for single-pole tripping applications. If voltage input is available, Low Voltage function is used to detect absence of the monitoring voltage in the associated pole of the breaker.

- **OPEN POLE FUNCTION:** This setting is used to Enable/Disable operation of the element.
- **OPEN POLE BLOCK:** This setting is used to select a FlexLogic™ operand that blocks operation of the element.
- **OPEN POLE CURRENT SOURCE:** This setting is used to select the source for the current for the element.
- **OPEN POLE CURRENT PICKUP:** This setting is used to select the pickup value of the phase current. Pickup setting is the minimum of the range and likely to be somewhat above of the charging current of the line.

- **OPEN POLE BROKEN CONDUCTOR:** This setting enables or disables detection of Broken Conductor or Remote Pole Open conditions.
- **OPEN POLE VOLTAGE INPUT:** This setting is used to Enable/Disable voltage input in making a logical decision. If line VT (not bus VT) is available, voltage input can be set to "Enable".
- **OPEN POLE VOLTAGE SOURCE:** This setting is used to select the source for the voltage for the element.
- **OPEN POLE Φ A(C) AUX CONTACT:** These three settings used to select a FlexLogic™ operand reflecting the state of phase A circuit breaker auxiliary contact 52b type (closed when main breaker contact is open) for single-pole tripping applications. If 2 breakers per line are being employed, both breaker auxiliary contacts feeding into the AND gate (representing auxiliary contacts connected in series) are to be assigned.
- **OPEN POLE PICKUP DELAY:** This setting is used to select the pickup delay of the element.
- **OPEN POLE RESET DELAY:** This setting is used to select the reset delay of the element. Depending on the particular application and whether 1-pole or 3-pole tripping mode is used, this setting should be thoroughly considered. It should comprise the reset time of the operating elements it used in conjunction with the breaker opening time and breaker auxiliary contacts discrepancy with the main contacts.

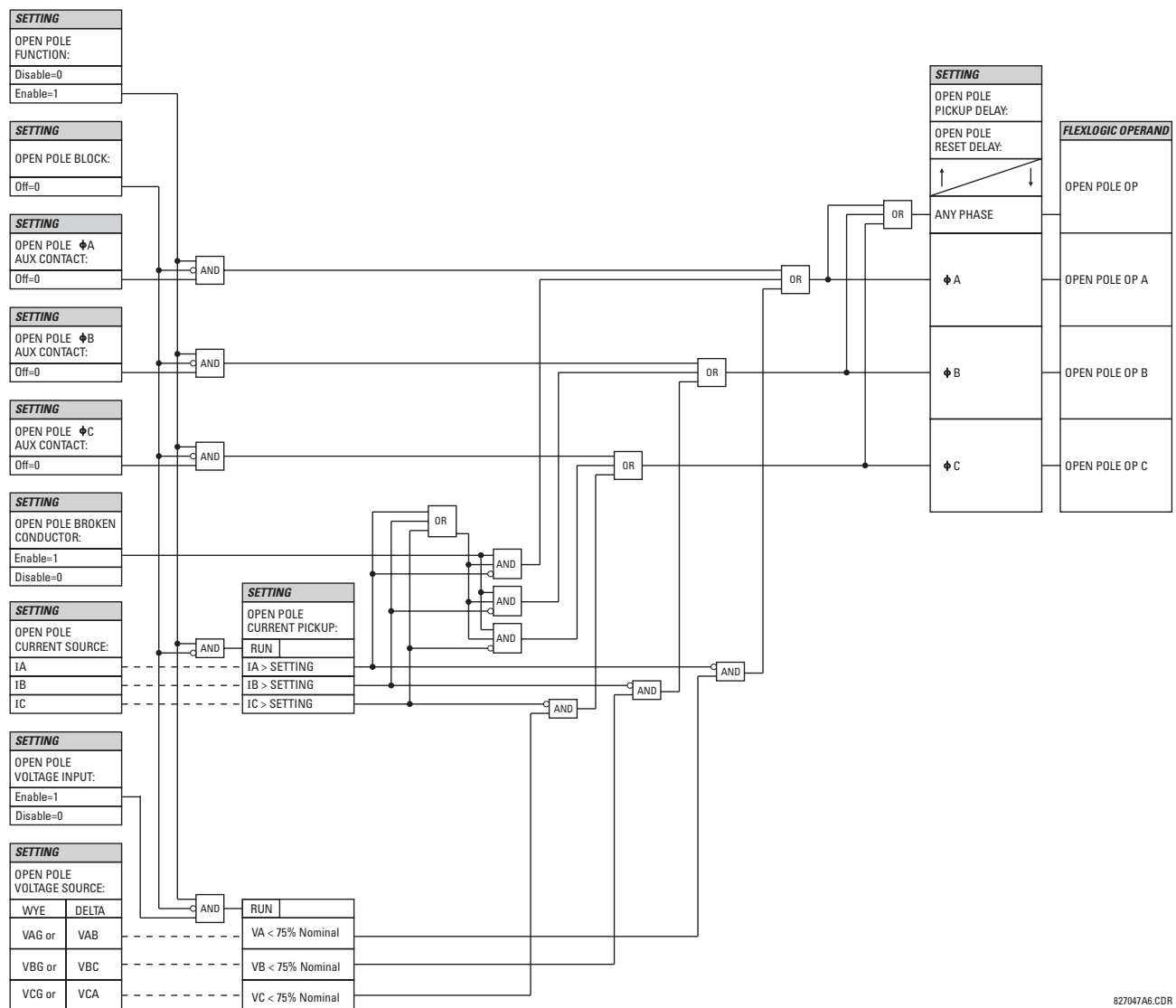


Figure 5-47: OPEN POLE DETECTOR SCHEME LOGIC

5.5.7 SENSITIVE DIRECTIONAL POWER

PATH: SETTINGS ⇒ GROUPED ELEMENTS ⇒ SETTING GROUP 1(6) ⇒ SENSITIVE DIRECTIONAL... ⇒ DIRECTIONAL POWER 1(2)

<div> <div>■ DIRECTIONAL</div> <div>■ POWER 1</div> </div>		DIR POWER 1 FUNCTION: Disabled	Range: Disabled, Enabled
	MESSAGE	DIR POWER 1 SOURCE: SRC 1	Range: SRC 1, SRC 2
	MESSAGE	DIR POWER 1 RCA: 0°	Range: 0 to 359° in steps of 1
	MESSAGE	DIR POWER 1 CALIBRATION: 0.00°	Range: 0 to 0.95° in steps of 0.05
	MESSAGE	DIR POWER 1 STG1 SMIN: 0.100 pu	Range: -1.200 to 1.200 pu in steps of 0.001
	MESSAGE	DIR POWER 1 STG1 DELAY: 0.50 s	Range: 0.00 to 600.00 s in steps of 0.01
	MESSAGE	DIR POWER 1 STG2 SMIN: 0.100 pu	Range: -1.200 to 1.200 pu in steps of 0.001
	MESSAGE	DIR POWER 1 STG2 DELAY: 20.00 s	Range: 0.00 to 600.00 s in steps of 0.01
	MESSAGE	DIR POWER 1 BLK: Off	Range: FlexLogic™ operand
	MESSAGE	DIR POWER 1 TARGET: Self-Reset	Range: Self-Reset, Latched, Disabled
	MESSAGE	DIR POWER 1 EVENTS: Disabled	Range: Disabled, Enabled

The Directional Power element responds to three-phase active power and is designed for reverse power and low forward power applications for synchronous machines or interconnections involving co-generation. The relay measures the three-phase power from either full set of wye-connected VTs or full-set of delta-connected VTs. In the latter case, the two-wattmeter method is used. Refer to the *UR Metering Conventions* section in Chapter 6 for conventions regarding the active and reactive powers used by the Directional Power element.

The element has an adjustable characteristic angle and minimum operating power as shown in the Directional Power Characteristic diagram. The element responds to the following condition:

$$P\cos\theta + Q\sin\theta > SMIN \quad (\text{EQ 5.8})$$

where: P and Q are active and reactive powers as measured per the UR convention,
 θ is a sum of the element characteristic (DIR POWER 1 RCA) and calibration (DIR POWER 1 CALIBRATION) angles, and
 $SMIN$ is the minimum operating power

The operating quantity is available for display as under **ACTUAL VALUES** ⇒ **METERING** ⇒ **SENSITIVE DIRECTIONAL POWER 1(2)**. The element has two independent (as to the pickup and delay settings) stages for alarm and trip, respectively.

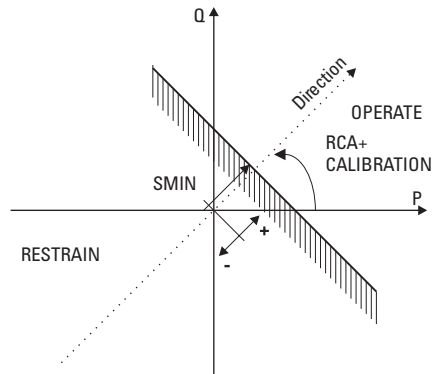


Figure 5-48: DIRECTIONAL POWER CHARACTERISTIC

By making the characteristic angle adjustable and providing for both negative and positive values of the minimum operating power a variety of operating characteristics can be achieved as presented in the figure below. For example, Figure (a) below shows settings for reverse power application, while Figure (b) shows settings for low forward power application.

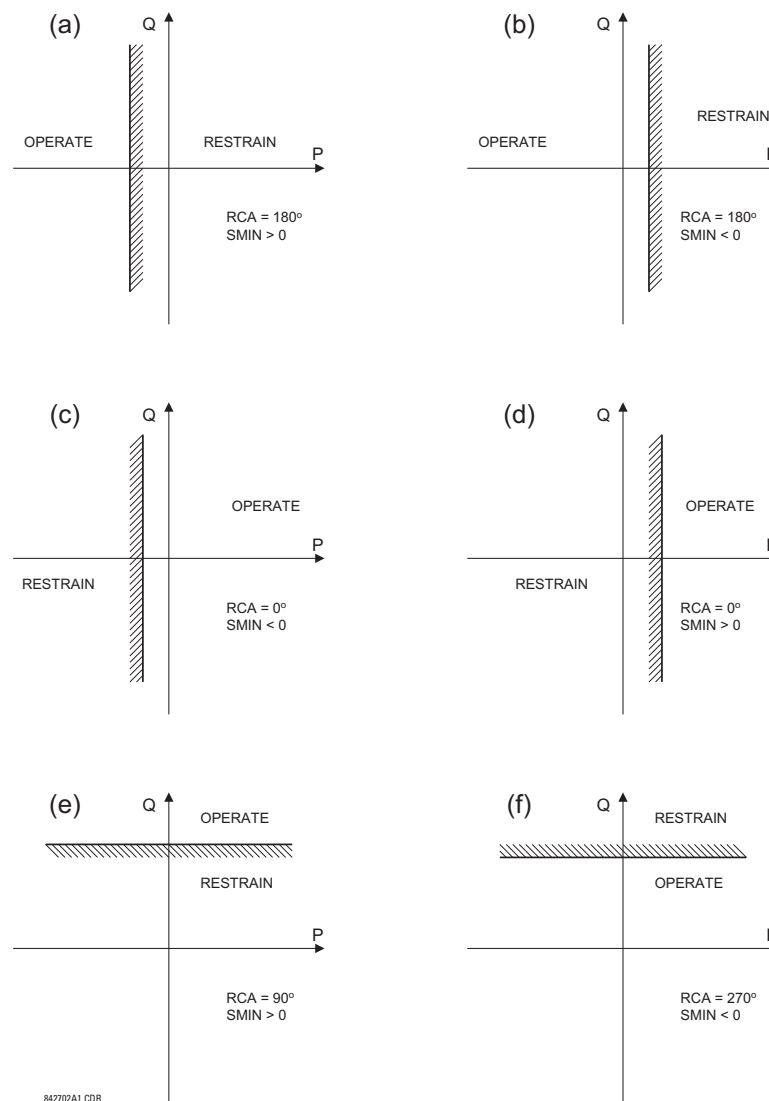


Figure 5-49: DIRECTIONAL POWER ELEMENT SAMPLE APPLICATIONS

- **DIR POWER 1(2) RCA:** Specifies the relay characteristic angle (RCA) for the directional power function. Application of this setting is threefold:
 1. It allows the element to respond to active or reactive power in any direction (active overpower/underpower, etc.).
 2. Together with a precise calibration angle, it allows compensation for any CT and VT angular errors to permit more sensitive settings.
 3. It allows for required direction in situations when the voltage signal is taken from behind a delta-wye connected power transformer and the phase angle compensation is required.

For example, the active overpower characteristic is achieved by setting **DIR POWER 1(2) RCA** to “0°”, reactive overpower by setting **DIR POWER 1(2) RCA** to “90°”, active underpower by setting **DIR POWER 1(2) RCA** to “180°”, and reactive underpower by setting **DIR POWER 1(2) RCA** to “270°”.

- **DIR POWER 1(2) CALIBRATION:** This setting allows the RCA to change in small steps of 0.05°. This may be useful when a small difference in VT and CT angular errors is to be compensated to permit more sensitive settings. This setting virtually enables calibration of the Directional Power function in terms of the angular error of applied VTs and CTs. The element responds to the sum of the **DIR POWER 1(2) RCA** and **DIR POWER 1(2) CALIBRATION** settings.

- **DIR POWER 1(2) STG1 SMIN:** This setting specifies the minimum power as defined along the RCA angle for the stage 1 of the element. The positive values imply a shift towards the operate region along the RCA line. The negative values imply a shift towards the restrain region along the RCA line. Refer to the *Directional Power Sample Applications* figure for an illustration. Together with the RCA, this setting enables a wide range of operating characteristics. This setting applies to three-phase power and is entered in pu. The base quantity is $3 \times \text{VT pu base} \times \text{CT pu base}$.

For example, a setting of 2% for a 200 MW machine, is $0.02 \times 200 \text{ MW} = 4 \text{ MW}$. If 7.967 kV is a primary VT voltage and 10 kA is a primary CT current, the source pu quantity is 239 MVA, and thus, SMIN should be set at $4 \text{ MW} / 239 \text{ MVA} = 0.0167 \text{ pu} \approx 0.017 \text{ pu}$. If the reverse power application is considered, $\text{RCA} = 180^\circ$ and $\text{SMIN} = 0.017 \text{ pu}$.

The element drops out if the magnitude of the positive-sequence current becomes virtually zero, that is, it drops below the cutoff level.

- **DIR POWER 1(2) STG1 DELAY:** This setting specifies a time delay for Stage 1. For reverse power or low forward power applications for a synchronous machine, Stage 1 is typically applied for alarming and Stage 2 for tripping.

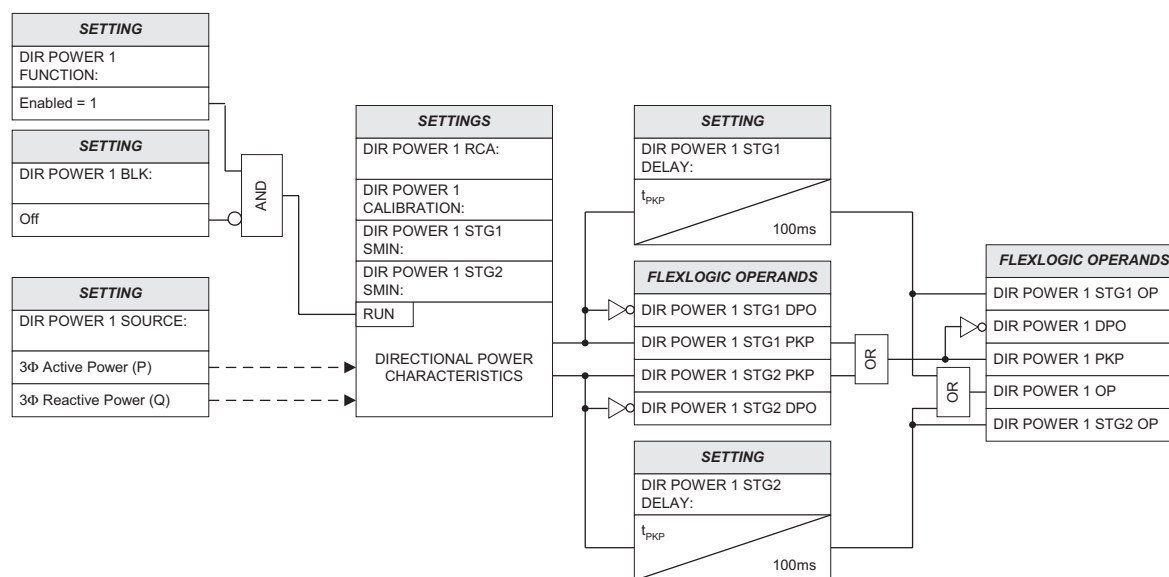


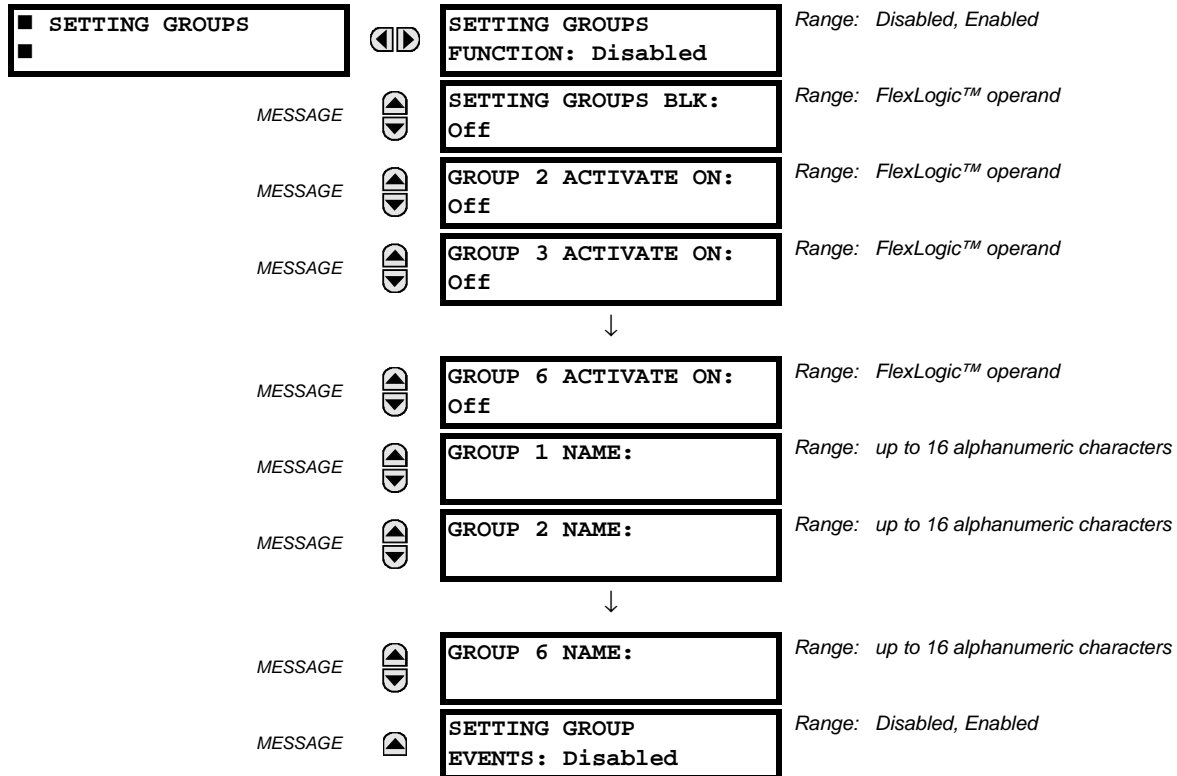
Figure 5-50: DIRECTIONAL POWER SCHEME LOGIC

5.6.1 OVERVIEW

Control elements are generally used for control rather than protection. See the *Introduction to Elements* section at the beginning of this chapter for further information.

5.6.2 SETTING GROUPS

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ SETTINGS GROUPS



The Setting Groups menu controls the activation/deactivation of up to six possible groups of settings in the **GROUPED ELEMENTS** settings menu. The faceplate 'Settings In Use' LEDs indicate which active group (with a non-flashing energized LED) is in service.

The **SETTING GROUPS BLK** setting prevents the active setting group from changing when the FlexLogic™ parameter is set to "On". This can be useful in applications where it is undesirable to change the settings under certain conditions, such as the breaker being open.

Each **GROUP n ACTIVATE ON** setting selects a FlexLogic™ operand which, when set, will make the particular setting group active for use by any grouped element. A priority scheme ensures that only one group is active at a given time – the highest-numbered group which is activated by its **GROUP n ACTIVATE ON** parameter takes priority over the lower-numbered groups. There is no "activate on" setting for Group 1 (the default active group), because Group 1 automatically becomes active if no other group is active.

The **SETTING GROUP 1(6) NAME** settings allow the user to assign a name to each of the six settings groups. Once programmed, this name will appear on the second line of the **GROUPED ELEMENTS ⇒ SETTING GROUP 1(6)** menu display.

The relay can be set up via a FlexLogic™ equation to receive requests to activate or de-activate a particular non-default settings group. The following FlexLogic™ equation (see the figure below) illustrates requests via remote communications (e.g. VIRTUAL INPUT 1) or from a local contact input (e.g. H7a) to initiate the use of a particular settings group, and requests from several overcurrent pickup measuring elements to inhibit the use of the particular settings group. The assigned VIRTUAL OUTPUT 1 operand is used to control the "On" state of a particular settings group.

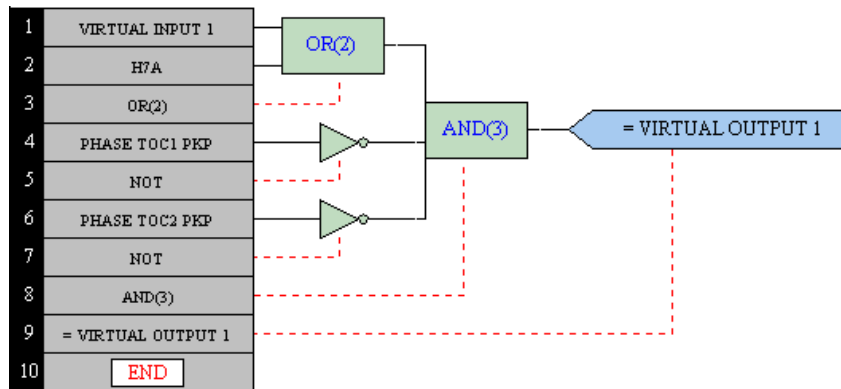


Figure 5-51: EXAMPLE FLEXLOGIC™ CONTROL OF A SETTINGS GROUP

5.6.3 SELECTOR SWITCH

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ SELECTOR SWITCH ⇒ SELECTOR SWITCH 1(2)

<div>■ SELECTOR SWITCH 1</div>		<div>SELECTOR 1 FUNCTION: Disabled</div>	Range: Disabled, Enabled
MESSAGE	<div></div>	<div>SELECTOR 1 FULL RANGE: 7</div>	Range: 1 to 7 in steps of 1
MESSAGE	<div></div>	<div>SELECTOR 1 TIME-OUT: 5.0 s</div>	Range: 3.0 to 60.0 s in steps of 0.1
MESSAGE	<div></div>	<div>SELECTOR 1 STEP-UP: Off</div>	Range: FlexLogic™ operand
MESSAGE	<div></div>	<div>SELECTOR 1 STEP-UP MODE: Time-out</div>	Range: Time-out, Acknowledge
MESSAGE	<div></div>	<div>SELECTOR 1 ACK: Off</div>	Range: FlexLogic™ operand
MESSAGE	<div></div>	<div>SELECTOR 1 3BIT A0: Off</div>	Range: FlexLogic™ operand
MESSAGE	<div></div>	<div>SELECTOR 1 3BIT A1: Off</div>	Range: FlexLogic™ operand
MESSAGE	<div></div>	<div>SELECTOR 1 3BIT A2: Off</div>	Range: FlexLogic™ operand
MESSAGE	<div></div>	<div>SELECTOR 1 3BIT MODE: Time-out</div>	Range: Time-out, Acknowledge
MESSAGE	<div></div>	<div>SELECTOR 1 3BIT ACK: Off</div>	Range: FlexLogic™ operand
MESSAGE	<div></div>	<div>SELECTOR 1 POWER-UP MODE: Restore</div>	Range: Restore, Synchronize, Sync/Restore
MESSAGE	<div></div>	<div>SELECTOR 1 TARGETS: Self-reset</div>	Range: Self-reset, Latched, Disabled
MESSAGE	<div></div>	<div>SELECTOR 1 EVENTS: Disabled</div>	Range: Disabled, Enabled

The Selector Switch element is intended to replace a mechanical selector switch. Typical applications include setting group control or control of multiple logic sub-circuits in user-programmable logic.

The element provides for two control inputs. The step-up control allows stepping through selector position one step at a time with each pulse of the control input, such as a user-programmable pushbutton. The 3-bit control input allows setting the selector to the position defined by a 3-bit word.

The element allows pre-selecting a new position without applying it. The pre-selected position gets applied either after time-out or upon acknowledgement via separate inputs (user setting). The selector position is stored in non-volatile memory. Upon power-up, either the previous position is restored or the relay synchronizes to the current 3-bit word (user setting). Basic alarm functionality alerts the user under abnormal conditions; e.g. the 3-bit control input being out of range.

- **SELECTOR 1 FULL RANGE:** This setting defines the upper position of the selector. When stepping up through available positions of the selector, the upper position wraps up to the lower position (Position 1). When using a direct 3-bit control word for programming the selector to a desired position, the change would take place only if the control word is within the range of 1 to the **SELECTOR FULL RANGE**. If the control word is outside the range, an alarm is established by setting the **SELECTOR ALARM** FlexLogic™ operand for 3 seconds.
- **SELECTOR 1 TIME-OUT:** This setting defines the time-out period for the selector. This value is used by the relay in the following two ways. When the **SELECTOR STEP-UP MODE** is “Time-out”, the setting specifies the required period of inactivity of the control input after which the pre-selected position is automatically applied. When the **SELECTOR STEP-UP MODE** is “Acknowledge”, the setting specifies the period of time for the acknowledging input to appear. The timer is re-started by any activity of the control input. The acknowledging input must come before the **SELECTOR 1 TIME-OUT** timer expires; otherwise, the change will not take place and an alarm will be set.
- **SELECTOR 1 STEP-UP:** This setting specifies a control input for the selector switch. The switch is shifted to a new position at each rising edge of this signal. The position changes incrementally, wrapping up from the last (**SELECTOR 1 FULL RANGE**) to the first (Position 1). Consecutive pulses of this control operand must not occur faster than every 50 ms. After each rising edge of the assigned operand, the time-out timer is restarted and the **SELECTOR SWITCH 1: POS Z CHNG INITIATED** target message is displayed, where **Z** the pre-selected position. The message is displayed for the time specified by the **FLASH MESSAGE TIME** setting. The pre-selected position is applied after the selector times out (“Time-out” mode), or when the acknowledging signal appears before the element times out (“Acknowledge” mode). When the new position is applied, the relay displays the **SELECTOR SWITCH 1: POSITION Z IN USE** message. Typically, a user-programmable pushbutton is configured as the stepping up control input.
- **SELECTOR 1 STEP-UP MODE:** This setting defines the selector mode of operation. When set to “Time-out”, the selector will change its position after a pre-defined period of inactivity at the control input. The change is automatic and does not require any explicit confirmation of the intent to change the selector's position. When set to “Acknowledge”, the selector will change its position only after the intent is confirmed through a separate acknowledging signal. If the acknowledging signal does not appear within a pre-defined period of time, the selector does not accept the change and an alarm is established by setting the **SELECTOR STP ALARM** output FlexLogic™ operand for 3 seconds.
- **SELECTOR 1 ACK:** This setting specifies an acknowledging input for the stepping up control input. The pre-selected position is applied on the rising edge of the assigned operand. This setting is active only under “Acknowledge” mode of operation. The acknowledging signal must appear within the time defined by the **SELECTOR 1 TIME-OUT** setting after the last activity of the control input. A user-programmable pushbutton is typically configured as the acknowledging input.
- **SELECTOR 1 3BIT A0, A1, and A2:** These settings specify a 3-bit control input of the selector. The 3-bit control word pre-selects the position using the following encoding convention:

A2	A1	A0	POSITION
0	0	0	rest
0	0	1	1
0	1	0	2
0	1	1	3
1	0	0	4
1	0	1	5
1	1	0	6
1	1	1	7

The “rest” position (0, 0, 0) does not generate an action and is intended for situations when the device generating the 3-bit control word is having a problem. When **SELECTOR 1 3BIT MODE** is “Time-out”, the pre-selected position is applied in **SELECTOR 1 TIME-OUT** seconds after the last activity of the 3-bit input. When **SELECTOR 1 3BIT MODE** is “Acknowledge”, the pre-selected position is applied on the rising edge of the **SELECTOR 1 3BIT ACK** acknowledging input.

The stepping up control input (**SELECTOR 1 STEP-UP**) and the 3-bit control inputs (**SELECTOR 1 3BIT A0** through **A2**) lock-out mutually: once the stepping up sequence is initiated, the 3-bit control input is inactive; once the 3-bit control sequence is initiated, the stepping up input is inactive.

- **SELECTOR 1 3BIT MODE:** This setting defines the selector mode of operation. When set to “Time-out”, the selector changes its position after a pre-defined period of inactivity at the control input. The change is automatic and does not require explicit confirmation to change the selector position. When set to “Acknowledge”, the selector changes its position only after confirmation via a separate acknowledging signal. If the acknowledging signal does not appear within a pre-defined period of time, the selector rejects the change and an alarm established by invoking the **SELECTOR BIT ALARM** FlexLogic™ operand for 3 seconds.
- **SELECTOR 1 3BIT ACK:** This setting specifies an acknowledging input for the 3-bit control input. The pre-selected position is applied on the rising edge of the assigned FlexLogic™ operand. This setting is active only under the “Acknowledge” mode of operation. The acknowledging signal must appear within the time defined by the **SELECTOR TIME-OUT** setting after the last activity of the 3-bit control inputs. Note that the stepping up control input and 3-bit control input have independent acknowledging signals (**SELECTOR 1 ACK** and **SELECTOR 1 3BIT ACK**, accordingly).
- **SELECTOR 1 POWER-UP MODE:** This setting specifies the element behavior on power up of the relay.

When set to “Restore”, the last position of the selector (stored in the non-volatile memory) is restored after powering up the relay. If the position restored from memory is out of range, position 0 (no output operand selected) is applied and an alarm is set (**SELECTOR 1 PWR ALARM**).

When set to “Synchronize” selector switch acts as follows. For two power cycles, the selector applies position 0 to the switch and activates **SELECTOR 1 PWR ALARM**. After two power cycles expire, the selector synchronizes to the position dictated by the 3-bit control input. This operation does not wait for time-out or the acknowledging input. When the synchronization attempt is unsuccessful (i.e., the 3-bit input is not available (0,0,0) or out of range) then the selector switch output is set to position 0 (no output operand selected) and an alarm is established (**SELECTOR 1 PWR ALARM**).

The operation of “Synch/Restore” mode is similar to the “Synchronize” mode. The only difference is that after an unsuccessful synchronization attempt, the switch will attempt to restore the position stored in the relay memory. The “Synch/Restore” mode is useful for applications where the selector switch is employed to change the setting group in redundant (two relay) protection schemes.

- **SELECTOR 1 EVENTS:** If enabled, the following events are logged:

EVENT NAME	DESCRIPTION
SELECTOR 1 POS Z	Selector 1 changed its position to Z.
SELECTOR 1 STP ALARM	The selector position pre-selected via the stepping up control input has not been confirmed before the time out.
SELECTOR 1 BIT ALARM	The selector position pre-selected via the 3-bit control input has not been confirmed before the time out.

The following figures illustrate the operation of the Selector Switch. In these diagrams, “T” represents a time-out setting.

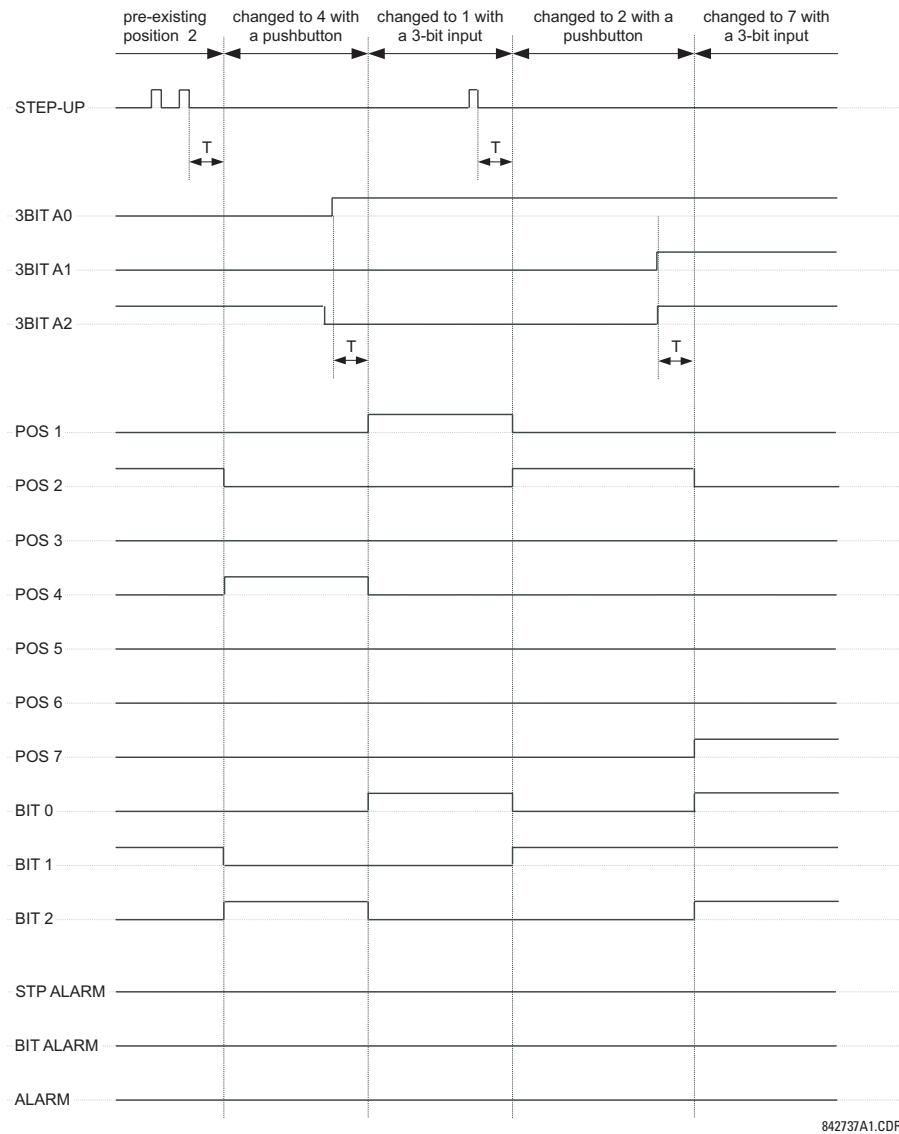
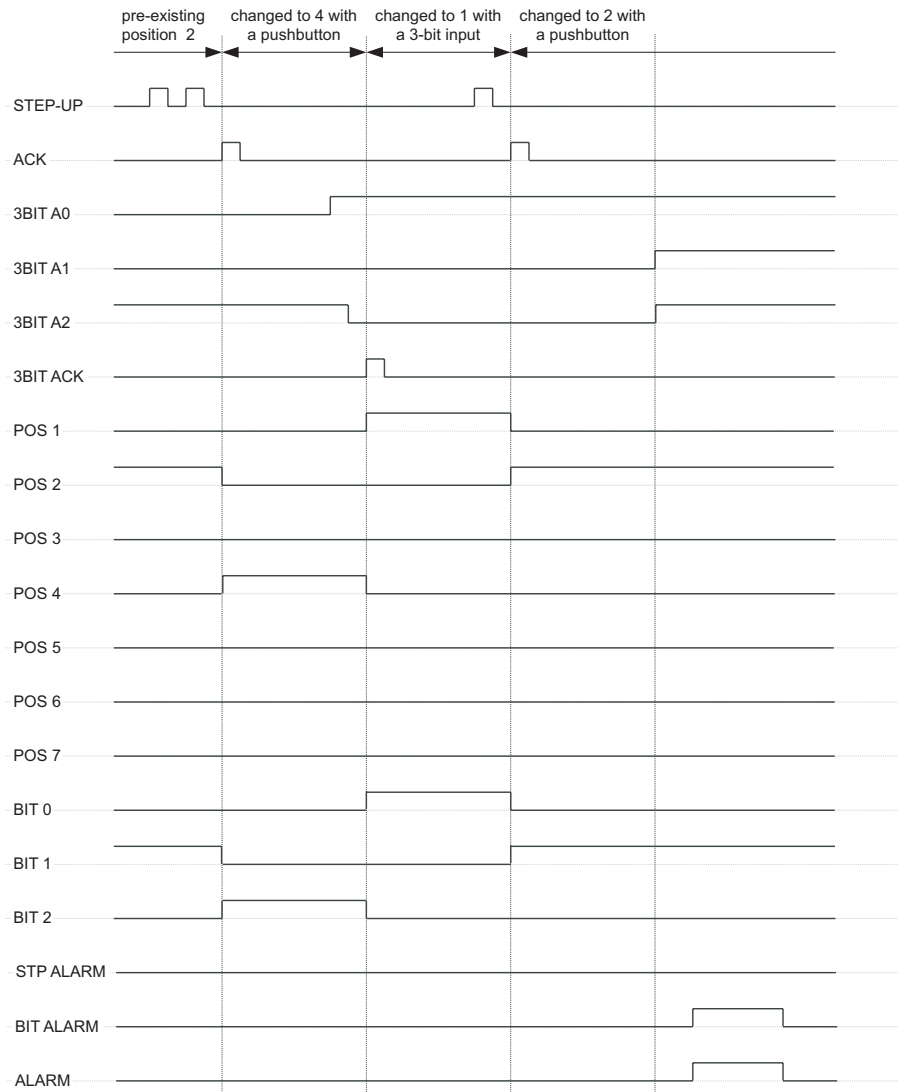


Figure 5-52: TIME-OUT MODE



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Figure 5–53: ACKNOWLEDGE MODE

APPLICATION EXAMPLE

Consider an application where the selector switch is used to control Setting Groups 1 through 4 in the relay. The setting groups are to be controlled from both User-Programmable Pushbutton 1 and from an external device via Contact Inputs 1 through 3. The active setting group shall be available as an encoded 3-bit word to the external device and SCADA via output contacts 1 through 3. The pre-selected setting group shall be applied automatically after 5 seconds of inactivity of the control inputs. When the relay powers up, it should synchronize the setting group to the 3-bit control input.

Make the following changes to Setting Group Control in the **SETTINGS** ⇒ **CONTROL ELEMENTS** ⇒ **SETTING GROUPS** menu:

SETTING GROUPS FUNCTION: "Enabled"

SETTING GROUPS BLK: "Off"

GROUP 2 ACTIVATE ON: "SELECTOR 1 POS 2"

GROUP 3 ACTIVATE ON: "SELECTOR 1 POS 3"

GROUP 4 ACTIVATE ON: "SELECTOR 1 POS 4"

GROUP 5 ACTIVATE ON: "Off"

GROUP 6 ACTIVATE ON: "Off"

Make the following changes to Selector Switch element in the **SETTINGS** ⇒ **CONTROL ELEMENTS** ⇒ **SELECTOR SWITCH** ⇒ **SELECTOR SWITCH 1** menu to assign control to User Programmable Pushbutton 1 and Contact Inputs 1 through 3:

SELECTOR 1 FUNCTION: "Enabled"

SELECTOR 1 FULL-RANGE: "4"

SELECTOR 1 STEP-UP MODE: "Time-out"

SELECTOR 1 TIME-OUT: "5.0 s"

SELECTOR 1 STEP-UP: "PUSHBUTTON 1 ON"

SELECTOR 1 ACK: "Off"

SELECTOR 1 3BIT A0: "CONT IP 1 ON"

SELECTOR 1 3BIT A1: "CONT IP 2 ON"

SELECTOR 1 3BIT A2: "CONT IP 3 ON"

SELECTOR 1 3BIT MODE: "Time-out"

SELECTOR 1 3BIT ACK: "Off"

SELECTOR 1 POWER-UP MODE: "Synchronize"

Now, assign the contact output operation (assume the H6E module) to the Selector Switch element by making the following changes in the **SETTINGS** ⇒ **INPUTS/OUTPUTS** ⇒ **CONTACT OUTPUTS** menu:

OUTPUT H1 OPERATE: "SELECTOR 1 BIT 0"

OUTPUT H2 OPERATE: "SELECTOR 1 BIT 1"

OUTPUT H3 OPERATE: "SELECTOR 1 BIT 2"

Finally, assign configure User-Programmable Pushbutton 1 by making the following changes in the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **USER-PROGRAMMABLE PUSHBUTTONS** ⇒ **USER PUSHBUTTON 1** menu:

PUSHBUTTON 1 FUNCTION: "Self-reset"

PUSHBUTTON 1 DROP-OUT TIME: "0.10 s"

The logic for the selector switch is shown below:

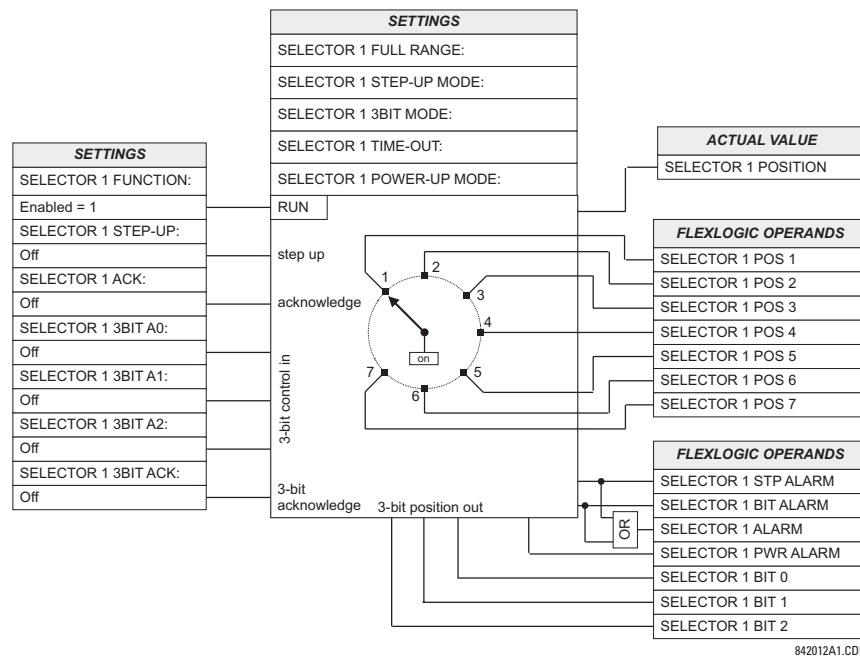


Figure 5-54: SELECTOR SWITCH LOGIC

5.6.4 UNDERFREQUENCY

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ UNDERFREQUENCY ⇒ UNDERFREQUENCY 1(6)

■ UNDERFREQUENCY 1	◀▶	UNDFREQ 1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	UNDFREQ 1 BLOCK: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	UNDFREQ 1 SOURCE: SRC 1	Range: SRC 1, SRC 2
MESSAGE	▲▼	UNDFREQ 1 MIN VOLT/AMP: 0.10 pu	Range: 0.10 to 1.25 pu in steps of 0.01
MESSAGE	▲▼	UNDFREQ 1 PICKUP: 59.50 Hz	Range: 20.00 to 65.00 Hz in steps of 0.01
MESSAGE	▲▼	UNDFREQ 1 PICKUP DELAY: 2.000 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE	▲▼	UNDFREQ 1 RESET DELAY : 2.000 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE	▲▼	UNDFREQ 1 TARGET: Self-reset	Range: Self-reset, Latched, Disabled
MESSAGE	▲	UNDFREQ 1 EVENTS: Disabled	Range: Disabled, Enabled

There are six identical underfrequency elements, numbered from 1 through 6 inclusive.

The steady-state frequency of a power system is a certain indicator of the existing balance between the generated power and the load. Whenever this balance is disrupted through the loss of an important generating unit or the isolation of part of the system from the rest of the system, the effect will be a reduction in frequency. If the control systems of the system generators do not respond fast enough, the system may collapse. A reliable method to quickly restore the balance between load and generation is to automatically disconnect selected loads, based on the actual system frequency. This technique, called "load-shedding", maintains system integrity and minimize widespread outages. After the frequency returns to normal, the load may be automatically or manually restored.

The **UNDFREQ 1 SOURCE** setting is used to select the source for the signal to be measured. The element first checks for a live phase voltage available from the selected Source. If voltage is not available, the element attempts to use a phase current. If neither voltage nor current is available, the element will not operate, as it will not measure a parameter above the minimum voltage/current setting.

The **UNDFREQ 1 MIN VOLT/AMP** setting selects the minimum per unit voltage or current level required to allow the underfrequency element to operate. This threshold is used to prevent an incorrect operation because there is no signal to measure.

This **UNDFREQ 1 PICKUP** setting is used to select the level at which the underfrequency element is to pickup. For example, if the system frequency is 60 Hz and the load shedding is required at 59.5 Hz, the setting will be 59.50 Hz.

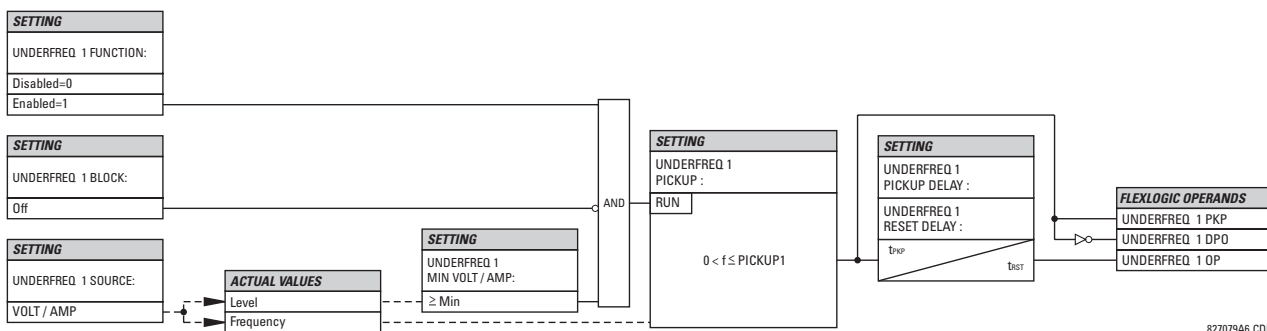


Figure 5-55: UNDERFREQUENCY SCHEME LOGIC

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5.6.5 OVERFREQUENCY

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ OVERFREQUENCY ⇒ OVERFREQUENCY 1(4)

■ OVERFREQUENCY 1	◀▶	OVERFREQ 1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	OVERFREQ 1 BLOCK: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	OVERFREQ 1 SOURCE: SRC 1	Range: SRC 1, SRC 2
MESSAGE	▲▼	OVERFREQ 1 PICKUP: 60.50 Hz	Range: 20.00 to 65.00 Hz in steps of 0.01
MESSAGE	▲▼	OVERFREQ 1 PICKUP DELAY: 0.500 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE	▲▼	OVERFREQ 1 RESET DELAY : 0.500 s	Range: 0.000 to 65.535 s in steps of 0.001
MESSAGE	▲▼	OVERFREQ 1 TARGET: Self-reset	Range: Self-reset, Latched, Disabled
MESSAGE	▲	OVERFREQ 1 EVENTS: Disabled	Range: Disabled, Enabled

There are four overfrequency elements, numbered 1 through 4.

A frequency calculation for a given source is made on the input of a voltage or current channel, depending on which is available. The channels are searched for the signal input in the following order: voltage channel A, auxiliary voltage channel, current channel A, ground current channel. The first available signal is used for frequency calculation.

The steady-state frequency of a power system is an indicator of the existing balance between the generated power and the load. Whenever this balance is disrupted through the disconnection of significant load or the isolation of a part of the system that has a surplus of generation, the effect will be an increase in frequency. If the control systems of the generators do not respond fast enough, to quickly ramp the turbine speed back to normal, the overspeed can lead to the turbine trip. The overfrequency element can be used to control the turbine frequency ramp down at a generating location. This element can also be used for feeder reclosing as part of the "after load shedding restoration".

The **OVERFREQ 1 SOURCE** setting selects the source for the signal to be measured. The **OVERFREQ 1 PICKUP** setting selects the level at which the overfrequency element is to pickup.

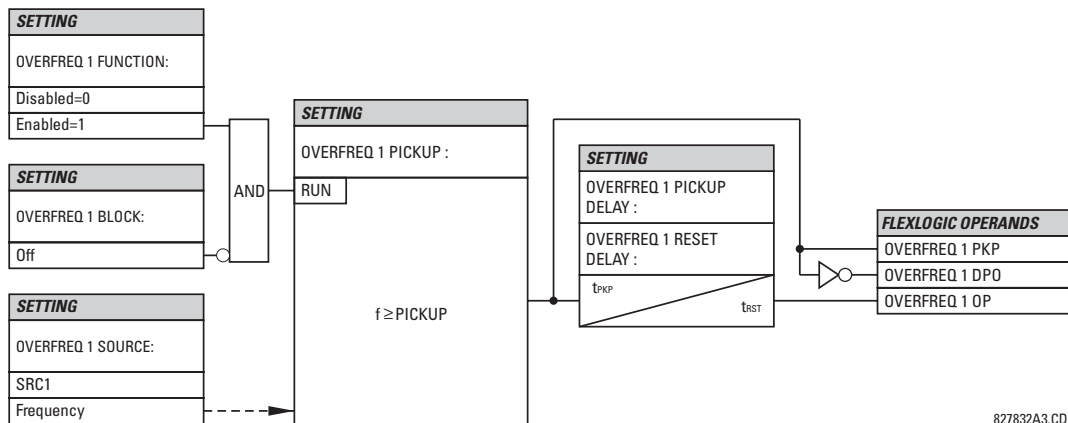


Figure 5-56: OVERFREQUENCY SCHEME LOGIC

5.6.6 SYNCHROCHECK

PATH: SETTINGS ⇨ CONTROL ELEMENTS ⇨ SYNCHROCHECK ⇨ SYNCHROCHECK 1(2)

5	■ SYNCHROCHECK 1	SYNCHK1 FUNCTION: Disabled	Range: Disabled, Enabled
	MESSAGE	SYNCHK1 BLOCK: Off	Range: FlexLogic™ operand
	MESSAGE	SYNCHK1 V1 SOURCE: SRC 1	Range: SRC 1, SRC 2
	MESSAGE	SYNCHK1 V2 SOURCE: SRC 2	Range: SRC 1, SRC 2
	MESSAGE	SYNCHK1 MAX VOLT DIFF: 10000 V	Range: 0 to 400000 V in steps of 1
	MESSAGE	SYNCHK1 MAX ANGLE DIFF: 30°	Range: 0 to 100° in steps of 1
	MESSAGE	SYNCHK1 MAX FREQ DIFF: 1.00 Hz	Range: 0.00 to 2.00 Hz in steps of 0.01
	MESSAGE	SYNCHK1 MAX FREQ HYSTERESIS: 0.06 Hz	Range: 0.00 to 0.10 Hz in steps of 0.01
	MESSAGE	SYNCHK1 DEAD SOURCE SELECT: LV1 and DV2	Range: None, LV1 and DV2, DV1 and LV2, DV1 or DV2, DV1 Xor DV2, DV1 and DV2
	MESSAGE	SYNCHK1 DEAD V1 MAX VOLT: 0.30 pu	Range: 0.00 to 1.25 pu in steps of 0.01
	MESSAGE	SYNCHK1 DEAD V2 MAX VOLT: 0.30 pu	Range: 0.00 to 1.25 pu in steps of 0.01
	MESSAGE	SYNCHK1 LIVE V1 MIN VOLT: 0.70 pu	Range: 0.00 to 1.25 pu in steps of 0.01
	MESSAGE	SYNCHK1 LIVE V2 MIN VOLT: 0.70 pu	Range: 0.00 to 1.25 pu in steps of 0.01
	MESSAGE	SYNCHK1 TARGET: Self-reset	Range: Self-reset, Latched, Disabled
	MESSAGE	SYNCHK1 EVENTS: Disabled	Range: Disabled, Enabled

There are two identical synchrocheck elements available, numbered 1 and 2.

The synchronism check function is intended for supervising the paralleling of two parts of a system which are to be joined by the closure of a circuit breaker. The synchrocheck elements are typically used at locations where the two parts of the system are interconnected through at least one other point in the system.

Synchrocheck verifies that the voltages (V1 and V2) on the two sides of the supervised circuit breaker are within set limits of magnitude, angle and frequency differences. The time that the two voltages remain within the admissible angle difference is determined by the setting of the phase angle difference $\Delta\Phi$ and the frequency difference ΔF (slip frequency). It can be defined as the time it would take the voltage phasor V1 or V2 to traverse an angle equal to $2 \times \Delta\Phi$ at a frequency equal to the frequency difference ΔF . This time can be calculated by:

$$T = \frac{1}{\frac{360^\circ}{2 \times \Delta\Phi} \times \Delta F} \quad (\text{EQ 5.9})$$

where: $\Delta\Phi$ = phase angle difference in degrees; ΔF = frequency difference in Hz.

As an example; for the default values ($\Delta\Phi = 30^\circ$, $\Delta F = 0.1$ Hz), the time while the angle between the two voltages will be less than the set value is:

$$T = \frac{1}{\frac{360^\circ}{2 \times \Delta\Phi} \times \Delta F} = \frac{1}{\frac{360^\circ}{2 \times 30^\circ} \times 0.1 \text{ Hz}} = 1.66 \text{ sec.} \quad (\text{EQ 5.10})$$

If one or both sources are de-energized, the synchrocheck programming can allow for closing of the circuit breaker using undervoltage control to by-pass the synchrocheck measurements (Dead Source function).

- **SYNCHK1 V1 SOURCE:** This setting selects the source for voltage V1 (see NOTES below).
- **SYNCHK1 V2 SOURCE:** This setting selects the source for voltage V2, which must not be the same as used for the V1 (see NOTES below).
- **SYNCHK1 MAX VOLT DIFF:** This setting selects the maximum primary voltage difference in 'kV' between the two sources. A primary voltage magnitude difference between the two input voltages below this value is within the permissible limit for synchronism.
- **SYNCHK1 MAX ANGLE DIFF:** This setting selects the maximum angular difference in degrees between the two sources. An angular difference between the two input voltage phasors below this value is within the permissible limit for synchronism.
- **SYNCHK1 MAX FREQ DIFF:** This setting selects the maximum frequency difference in 'Hz' between the two sources. A frequency difference between the two input voltage systems below this value is within the permissible limit for synchronism.
- **SYNCHK1 MAX FREQ HYSTERESIS:** This setting specifies the required hysteresis for the maximum frequency difference condition. The condition becomes satisfied when the frequency difference becomes lower than **SYNCHK1 MAX FREQ DIFF**. Once the Synchrocheck element has operated, the frequency difference must increase above the **SYNCHK1 MAX FREQ DIFF + SYNCHK1 MAX FREQ HYSTERESIS** sum to drop out (assuming the other two conditions, voltage and angle, remain satisfied).
- **SYNCHK1 DEAD SOURCE SELECT:** This setting selects the combination of dead and live sources that will by-pass synchronism check function and permit the breaker to be closed when one or both of the two voltages (V1 or/and V2) are below the maximum voltage threshold. A dead or live source is declared by monitoring the voltage level. Six options are available:

None:	Dead Source function is disabled
LV1 and DV2:	Live V1 and Dead V2
DV1 and LV2:	Dead V1 and Live V2
DV1 or DV2:	Dead V1 or Dead V2
DV1 Xor DV2:	Dead V1 exclusive-or Dead V2 (one source is Dead and the other is Live)
DV1 and DV2:	Dead V1 and Dead V2
- **SYNCHK1 DEAD V1 MAX VOLT:** This setting establishes a maximum voltage magnitude for V1 in 'pu'. Below this magnitude, the V1 voltage input used for synchrocheck will be considered "Dead" or de-energized.
- **SYNCHK1 DEAD V2 MAX VOLT:** This setting establishes a maximum voltage magnitude for V2 in 'pu'. Below this magnitude, the V2 voltage input used for synchrocheck will be considered "Dead" or de-energized.
- **SYNCHK1 LIVE V1 MIN VOLT:** This setting establishes a minimum voltage magnitude for V1 in 'pu'. Above this magnitude, the V1 voltage input used for synchrocheck will be considered "Live" or energized.
- **SYNCHK1 LIVE V2 MIN VOLT:** This setting establishes a minimum voltage magnitude for V2 in 'pu'. Above this magnitude, the V2 voltage input used for synchrocheck will be considered "Live" or energized.

NOTES ON THE SYNCHROCHECK FUNCTION:

- The selected sources for synchrocheck inputs V1 and V2 (which must not be the same source) may include both a three-phase and an auxiliary voltage. The relay will automatically select the specific voltages to be used by the synchrocheck element in accordance with the following table.

NO.	V1 OR V2 (SOURCE Y)	V2 OR V1 (SOURCE Z)	AUTO-SELECTED COMBINATION		AUTO-SELECTED VOLTAGE
			SOURCE Y	SOURCE Z	
1	Phase VTs and Auxiliary VT	Phase VTs and Auxiliary VT	Phase	Phase	VAB
2	Phase VTs and Auxiliary VT	Phase VT	Phase	Phase	VAB
3	Phase VT	Phase VT	Phase	Phase	VAB
4	Phase VT and Auxiliary VT	Auxiliary VT	Phase	Auxiliary	V auxiliary (as set for Source z)
5	Auxiliary VT	Auxiliary VT	Auxiliary	Auxiliary	V auxiliary (as set for selected sources)

The voltages V1 and V2 will be matched automatically so that the corresponding voltages from the two sources will be used to measure conditions. A phase to phase voltage will be used if available in both sources; if one or both of the Sources have only an auxiliary voltage, this voltage will be used. For example, if an auxiliary voltage is programmed to VAG, the synchrocheck element will automatically select VAG from the other source. If the comparison is required on a specific voltage, the user can externally connect that specific voltage to auxiliary voltage terminals and then use this "Auxiliary Voltage" to check the synchronism conditions.

If using a single CT/VT module with both phase voltages and an auxiliary voltage, ensure that only the auxiliary voltage is programmed in one of the sources to be used for synchrocheck.



Exception: Synchronism cannot be checked between Delta connected phase VTs and a Wye connected auxiliary voltage.

- The relay measures frequency and Volts/Hz from an input on a given source with priorities as established by the configuration of input channels to the source. The relay will use the phase channel of a three-phase set of voltages if programmed as part of that source. The relay will use the auxiliary voltage channel only if that channel is programmed as part of the Source and a three-phase set is not.

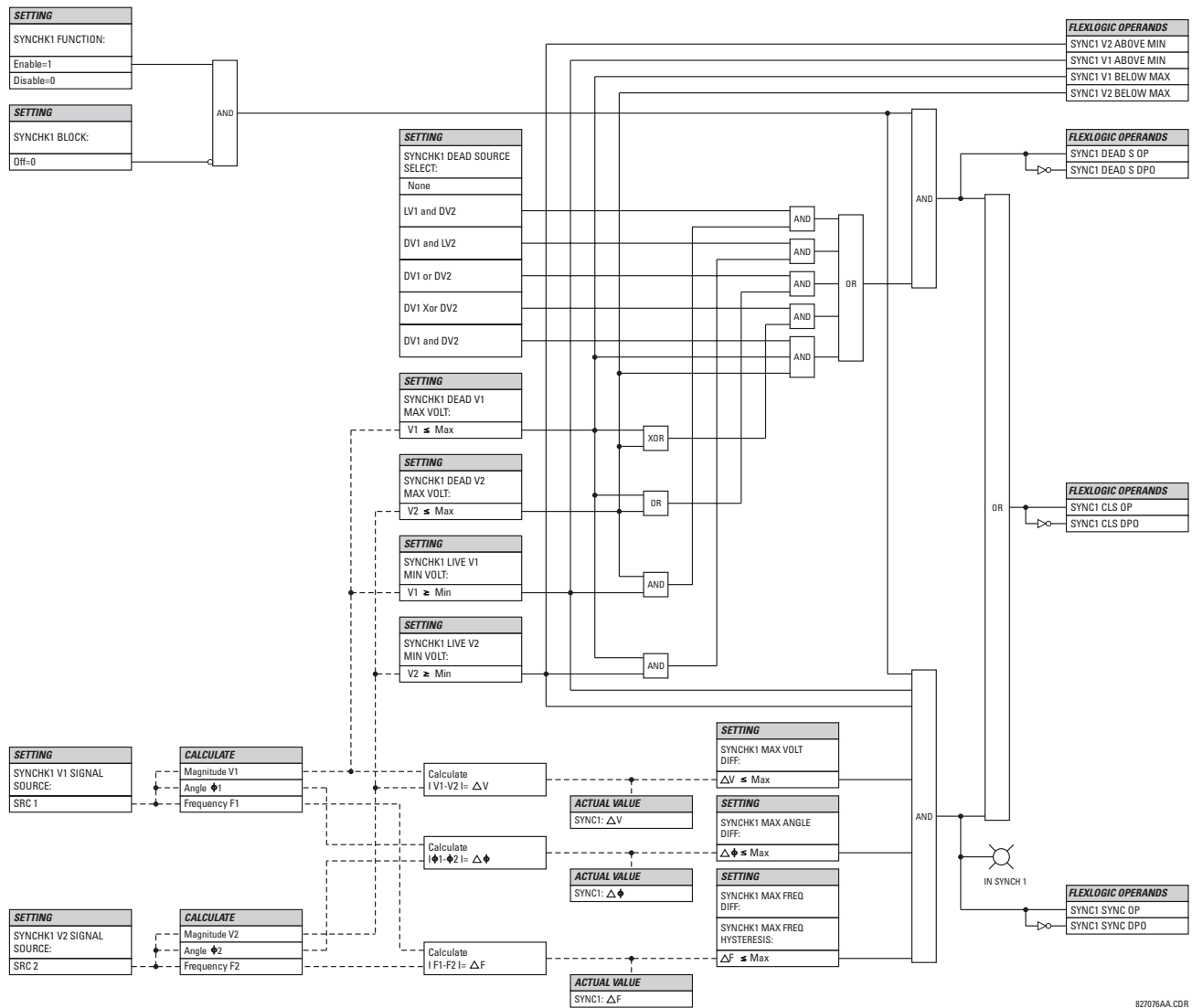


Figure 5-57: SYNCHROCHECK SCHEME LOGIC

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5.6.7 DIGITAL ELEMENTS

PATH: SETTINGS ⇌ CONTROL ELEMENTS ⇌ DIGITAL ELEMENTS ⇌ DIGITAL ELEMENT 1(48)

DIGITAL ELEMENT 1	DIGITAL ELEMENT 1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	DIG ELEM 1 NAME: Dig Element 1	Range: 16 alphanumeric characters
MESSAGE	DIG ELEM 1 INPUT: Off	Range: FlexLogic™ operand
MESSAGE	DIG ELEM 1 PICKUP DELAY: 0.000 s	Range: 0.000 to 999999.999 s in steps of 0.001
MESSAGE	DIG ELEM 1 RESET DELAY: 0.000 s	Range: 0.000 to 999999.999 s in steps of 0.001
MESSAGE	DIG ELEMENT 1 PICKUP LED: Enabled	Range: Disabled, Enabled
MESSAGE	DIG ELEM 1 BLOCK: Off	Range: FlexLogic™ operand
MESSAGE	DIGITAL ELEMENT 1 TARGET: Self-reset	Range: Self-reset, Latched, Disabled
MESSAGE	DIGITAL ELEMENT 1 EVENTS: Disabled	Range: Disabled, Enabled

There are 48 identical digital elements available, numbered 1 to 48. A digital element can monitor any FlexLogic™ operand and present a target message and/or enable events recording depending on the output operand state. The digital element settings include a name which will be referenced in any target message, a blocking input from any selected FlexLogic™ operand, and a timer for pickup and reset delays for the output operand.

- **DIGITAL ELEMENT 1 INPUT:** Selects a FlexLogic™ operand to be monitored by the digital element.
- **DIGITAL ELEMENT 1 PICKUP DELAY:** Sets the time delay to pickup. If a pickup delay is not required, set to "0".
- **DIGITAL ELEMENT 1 RESET DELAY:** Sets the time delay to reset. If a reset delay is not required, set to "0".
- **DIGITAL ELEMENT 1 PICKUP LED:** This setting enables or disabled the digital element pickup LED. When set to "Disabled", the operation of the pickup LED is blocked.

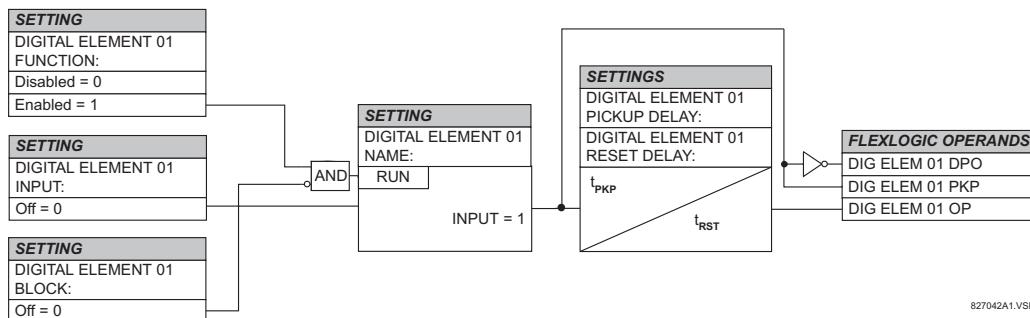


Figure 5-58: DIGITAL ELEMENT SCHEME LOGIC

CIRCUIT MONITORING APPLICATIONS:

Some versions of the digital input modules include an active voltage monitor circuit connected across Form-A contacts. The voltage monitor circuit limits the trickle current through the output circuit (see technical specifications for Form-A).

As long as the current through the Voltage Monitor is above a threshold (see technical specifications for Form-A), the FlexLogic™ operand "Cont Op # VOn" will be set (# represents the output contact number). If the output circuit has a high resistance or the DC current is interrupted, the trickle current will drop below the threshold and the FlexLogic™ operand "Cont Op # VOff" will be set. Consequently, the state of these operands can be used as indicators of the integrity of the circuits in which Form-A contacts are inserted.

EXAMPLE 1: BREAKER TRIP CIRCUIT INTEGRITY MONITORING

In many applications it is desired to monitor the breaker trip circuit integrity so problems can be detected before a trip operation is required. The circuit is considered to be healthy when the voltage monitor connected across the trip output contact detects a low level of current, well below the operating current of the breaker trip coil. If the circuit presents a high resistance, the trickle current will fall below the monitor threshold and an alarm would be declared.

In most breaker control circuits, the trip coil is connected in series with a breaker auxiliary contact which is open when the breaker is open (see diagram below). To prevent unwanted alarms in this situation, the trip circuit monitoring logic must include the breaker position.

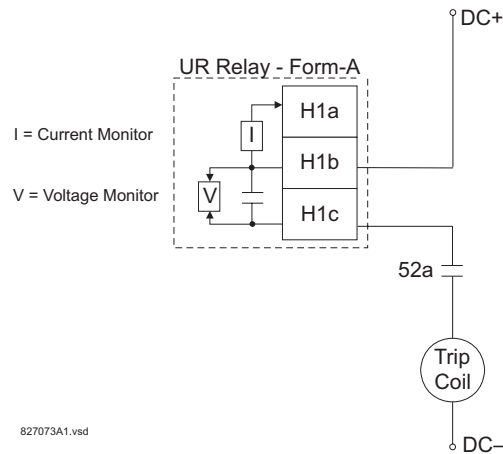


Figure 5–59: TRIP CIRCUIT EXAMPLE 1

Assume the output contact H1 is a trip contact. Using the contact output settings, this output will be given an ID name, e.g. "Cont Op 1". Assume a 52a breaker auxiliary contact is connected to contact input H7a to monitor breaker status. Using the contact input settings, this input will be given an ID name, e.g. "Cont Ip 1" and will be set "On" when the breaker is closed. Using Digital Element 1 to monitor the breaker trip circuit, the settings will be:

DIGITAL ELEMENT 1	DIGITAL ELEMENT 1
	FUNCTION: Enabled
MESSAGE	DIG ELEM 1 NAME: Bkr Trip Cct Out
MESSAGE	DIG ELEM 1 INPUT: Cont Op 1 VOff
MESSAGE	DIG ELEM 1 PICKUP DELAY: 0.200 s
MESSAGE	DIG ELEM 1 RESET DELAY: 0.100 s
MESSAGE	DIG ELEM 1 BLOCK: Cont Ip 1 Off
MESSAGE	DIGITAL ELEMENT 1 TARGET: Self-reset
MESSAGE	DIGITAL ELEMENT 1 EVENTS: Enabled



The PICKUP DELAY setting should be greater than the operating time of the breaker to avoid nuisance alarms.

NOTE

EXAMPLE 2: BREAKER TRIP CIRCUIT INTEGRITY MONITORING

If it is required to monitor the trip circuit continuously, independent of the breaker position (open or closed), a method to maintain the monitoring current flow through the trip circuit when the breaker is open must be provided (as shown in the figure below). This can be achieved by connecting a suitable resistor (see figure below) across the auxiliary contact in the trip circuit. In this case, it is not required to supervise the monitoring circuit with the breaker position – the BLOCK setting is selected to “Off”. In this case, the settings will be:

DIGITAL ELEMENT 1	DIGITAL ELEMENT 1
	FUNCTION: Enabled
MESSAGE	DIG ELEM 1 NAME: Bkr Trip Cct Out
MESSAGE	DIG ELEM 1 INPUT: Cont Op 1 Voff
MESSAGE	DIG ELEM 1 PICKUP DELAY: 0.200 s
MESSAGE	DIG ELEM 1 RESET DELAY: 0.100 s
MESSAGE	DIG ELEM 1 BLOCK: Off
MESSAGE	DIGITAL ELEMENT 1 TARGET: Self-reset
MESSAGE	DIGITAL ELEMENT 1 EVENTS: Enabled

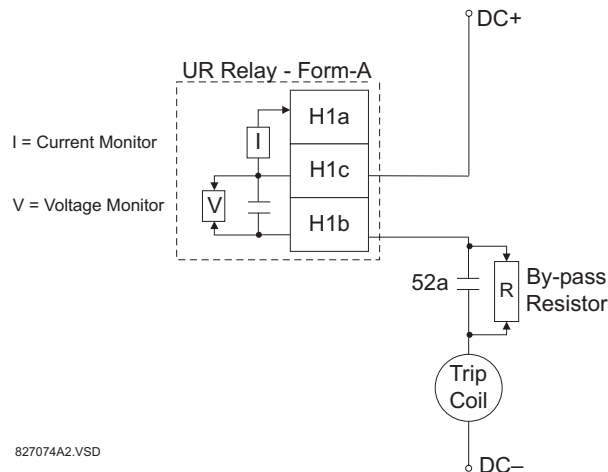


Table 5–10: VALUES OF RESISTOR ‘R’

POWER SUPPLY (V DC)	RESISTANCE (OHMS)	POWER (WATTS)
24	1000	2
30	5000	2
48	10000	2
110	25000	5
125	25000	5
250	50000	5

Figure 5–60: TRIP CIRCUIT EXAMPLE 2

5.6.8 DIGITAL COUNTERS

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ DIGITAL COUNTERS ⇒ COUNTER 1(8)

■ COUNTER 1	◀▶	COUNTER 1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	COUNTER 1 NAME: Counter 1	Range: 12 alphanumeric characters
MESSAGE	▲▼	COUNTER 1 UNITS:	Range: 6 alphanumeric characters
MESSAGE	▲▼	COUNTER 1 PRESET: 0	Range: -2,147,483,648 to +2,147,483,647
MESSAGE	▲▼	COUNTER 1 COMPARE: 0	Range: -2,147,483,648 to +2,147,483,647
MESSAGE	▲▼	COUNTER 1 UP: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	COUNTER 1 DOWN: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	COUNTER 1 BLOCK: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	CNT1 SET TO PRESET: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	COUNTER 1 RESET: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	COUNT1 FREEZE/RESET: Off	Range: FlexLogic™ operand
MESSAGE	▲	COUNT1 FREEZE/COUNT: Off	Range: FlexLogic™ operand

There are 8 identical digital counters, numbered from 1 to 8. A digital counter counts the number of state transitions from Logic 0 to Logic 1. The counter is used to count operations such as the pickups of an element, the changes of state of an external contact (e.g. breaker auxiliary switch), or pulses from a watt-hour meter.

- **COUNTER 1 UNITS:** Assigns a label to identify the unit of measure pertaining to the digital transitions to be counted. The units label will appear in the corresponding actual values status.
- **COUNTER 1 PRESET:** Sets the count to a required preset value before counting operations begin, as in the case where a substitute relay is to be installed in place of an in-service relay, or while the counter is running.
- **COUNTER 1 COMPARE:** Sets the value to which the accumulated count value is compared. Three FlexLogic™ output operands are provided to indicate if the present value is 'more than (HI)', 'equal to (EQL)', or 'less than (LO)' the set value.
- **COUNTER 1 UP:** Selects the FlexLogic™ operand for incrementing the counter. If an enabled UP input is received when the accumulated value is at the limit of +2,147,483,647 counts, the counter will rollover to -2,147,483,648.
- **COUNTER 1 DOWN:** Selects the FlexLogic™ operand for decrementing the counter. If an enabled DOWN input is received when the accumulated value is at the limit of -2,147,483,648 counts, the counter will rollover to +2,147,483,647.
- **COUNTER 1 BLOCK:** Selects the FlexLogic™ operand for blocking the counting operation. All counter operands are blocked.

- # 5

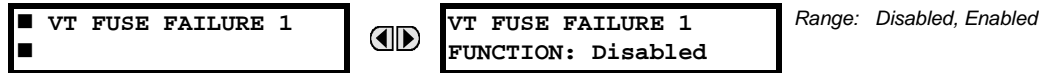


Figure 5–61: DIGITAL COUNTER SCHEME LOGIC

5.6.9 MONITORING ELEMENTS

a) VT FUSE FAILURE

PATH: SETTINGS ⇄ CONTROL ELEMENTS ⇄ MONITORING ELEMENTS ⇄ VT FUSE FAILURE 1(6)



Every signal source includes a fuse failure scheme.

The VT fuse failure detector can be used to raise an alarm and/or block elements that may operate incorrectly for a full or partial loss of AC potential caused by one or more blown fuses. Some elements that might be blocked (via the BLOCK input) are distance, voltage restrained overcurrent, and directional current.

There are two classes of fuse failure that may occur:

- Class A: Loss of one or two phases.
- Class B: Loss of all three phases.

Different means of detection are required for each class. An indication of Class A failures is a significant level of negative sequence voltage, whereas an indication of Class B failures is when positive sequence current is present and there is an insignificant amount of positive sequence voltage. These noted indications of fuse failure could also be present when faults are present on the system, so a means of detecting faults and inhibiting fuse failure declarations during these events is provided. Once the fuse failure condition is declared, it will be sealed-in until the cause that generated it disappears.

An additional condition is introduced to inhibit a fuse failure declaration when the monitored circuit is de-energized; positive sequence voltage and current are both below threshold levels.

The VT FUSE FAILURE 1(6) FUNCTION setting enables/disables the fuse failure feature for each source.

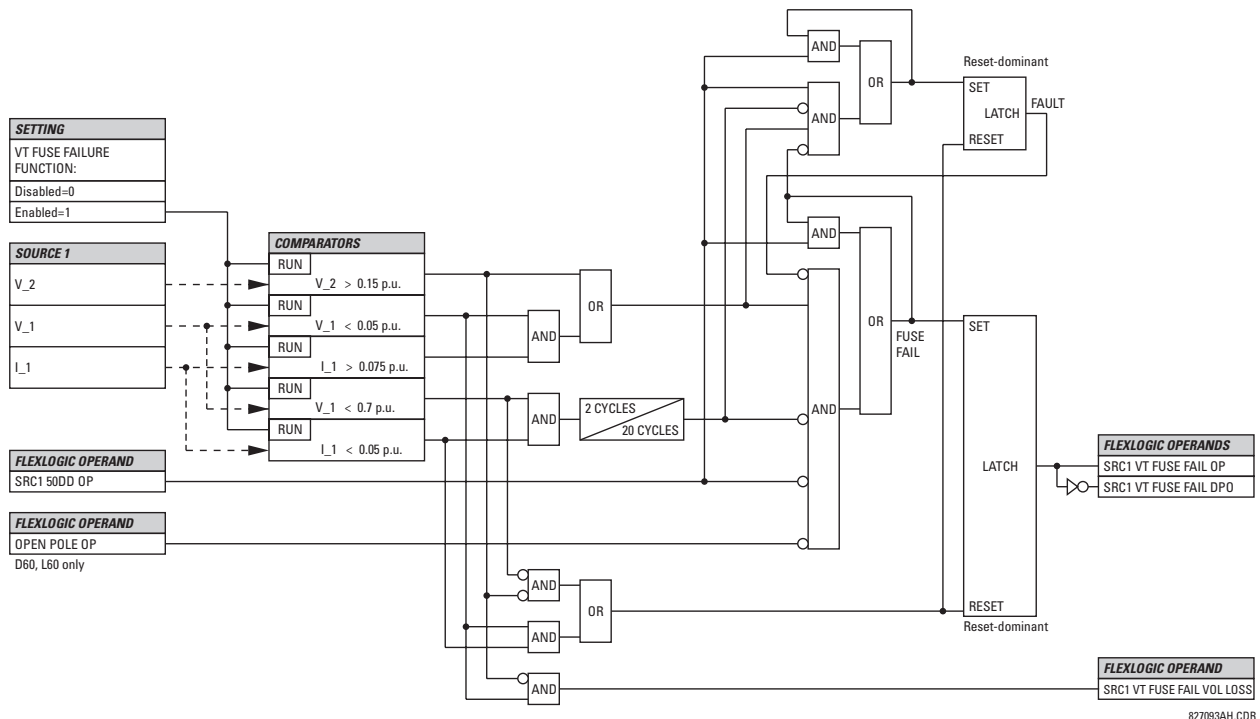


Figure 5-62: VT FUSE FAIL SCHEME LOGIC

5.6.10 FREQUENCY RATE OF CHANGE

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ FREQUENCY RATE OF CHANGE ⇒ FREQUENCY RATE OF CHANGE 1(4)

■ FREQUENCY RATE ■ OF CHANGE 1		FREQ RATE 1 FUNCTION: Disabled	Range: Disabled, Enabled
	MESSAGE	FREQ RATE 1 SOURCE: SRC 1	Range: SRC 1, SRC 2
	MESSAGE	FREQ RATE 1 TREND: Decreasing	Range: Increasing, Decreasing, Bi-directional
	MESSAGE	FREQ RATE 1 PICKUP: 0.50 Hz/sec	Range: 0.10 to 15.00 Hz/sec in steps of 0.01
	MESSAGE	FREQ RATE 1 OV SUPV PICKUP: 0.700 pu	Range: 0.100 to 3.000 pu in steps of 0.001
	MESSAGE	FREQ RATE 1 OC SUPV PICKUP: 0.200 pu	Range: 0.000 to 30.000 pu in steps of 0.001
	MESSAGE	FREQ RATE 1 MIN FREQUENCY: 45.00 Hz	Range: 20.00 to 80.00 Hz in steps of 0.01
	MESSAGE	FREQ RATE 1 MAX FREQUENCY: 65.00 Hz	Range: 20.00 to 80.00 Hz in steps of 0.01
	MESSAGE	FREQ RATE 1 PICKUP DELAY: 0.000 s	Range: 0 to 65.535 s in steps of 0.001
	MESSAGE	FREQ RATE 1 RESET DELAY: 0.000 s	Range: 0 to 65.535 s in steps of 0.001
	MESSAGE	FREQ RATE 1 BLOCK: Off	Range: FlexLogic™ operand
	MESSAGE	FREQ RATE 1 TARGET: Self-Reset	Range: Self-Reset, Latched, Disabled
	MESSAGE	FREQ RATE 1 EVENTS: Disabled	Range: Disabled, Enabled

Four (4) independent Rate of Change of Frequency elements are available. The element responds to rate of change of frequency with voltage, current and frequency supervision.

- **FREQ RATE 1 TREND:** This setting allows configuring the element to respond to increasing or decreasing frequency, or to frequency change in either direction.
- **FREQ RATE 1 PICKUP:** This setting specifies an intended df/dt pickup threshold. For applications monitoring a decreasing trend, set **FREQ RATE 1 TREND** to “Decreasing” and specify the pickup threshold accordingly. The operating condition is: $-df/dt > \text{Pickup}$.

For applications monitoring an increasing trend, set **FREQ RATE 1 TREND** to “Increasing” and specify the pickup threshold accordingly. The operating condition is: $df/dt > \text{Pickup}$.

For applications monitoring rate of change of frequency in any direction set **FREQ RATE 1 TREND** to “Bi-Directional” and specify the pickup threshold accordingly. The operating condition is: $\text{abs}(df/dt) > \text{Pickup}$

- **FREQ RATE 1 OV SUPV PICKUP:** This setting defines minimum voltage level required for operation of the element. The supervising function responds to the positive-sequence voltage. Overvoltage supervision should be used to prevent operation under specific system conditions such as faults.
- **FREQ RATE 1 OC SUPV PICKUP:** This setting defines minimum current level required for operation of the element. The supervising function responds to the positive-sequence current. Typical application includes load shedding. Set the pickup threshold to zero if no overcurrent supervision is required.

- **FREQ RATE 1 MIN FREQUENCY:** This setting defines minimum frequency level required for operation of the element. The setting may be used to effectively block the feature based on frequency. For example, if the intent is to monitor an increasing trend but only if the frequency is already above certain level, this setting should be set to the required frequency level.
- **FREQ RATE 1 MAX FREQUENCY:** This setting defines maximum frequency level required for operation of the element. The setting may be used to effectively block the feature based on frequency. For example, if the intent is to monitor a decreasing trend but only if the frequency is already below certain level (such as for load shedding), this setting should be set to the required frequency level.

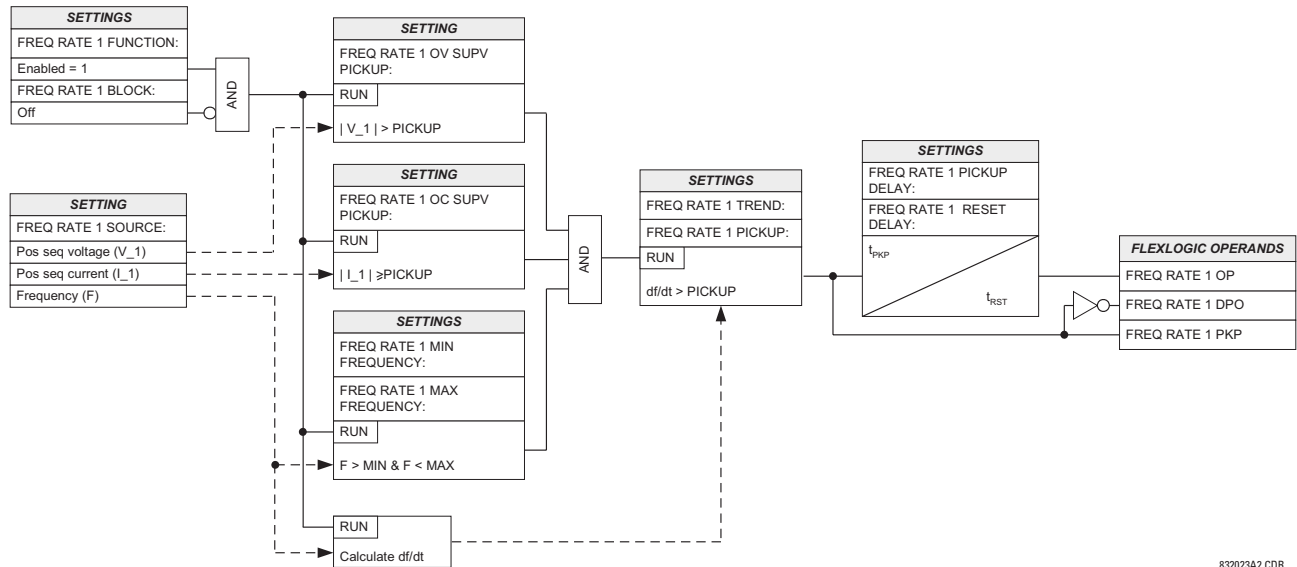


Figure 5–63: FREQUENCY RATE OF CHANGE SCHEME LOGIC

5.6.11 DIGITIZERS

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ DIGITIZERS ⇒ DIGITIZER 1(5)

<div>■ DIGITIZER 1</div> <div>MESSAGE</div> <div>MESSAGE</div>	<div>◀▶</div>	<div>DIGITIZER 1</div> <div>FUNCTION: Disabled</div>	Range: Disabled, Enabled
	<div>▲▼</div>	<div>DIGITIZER 1 SOURCE:</div> <div>Off</div>	Range: Off, any FlexAnalog parameter
	<div>▲</div>	<div>DIGITIZER 1</div> <div>LIMIT: 1.000 pu</div>	Range: 0.050 to 90.000 pu in steps of 0.001

There are five (5) digitizer elements. The Digitizer truncates the input signal to user-specified symmetrical limits and digitizes the outcome with an 8-bit resolution. The basic operation of the element is described by the following equations:

$$x_{truncated} = \begin{cases} -LIMIT, & \text{if } x < -LIMIT \\ LIMIT, & \text{if } x > LIMIT \\ x, & \text{otherwise} \end{cases} \quad (\text{EQ 5.11})$$

$$N_{out} = \text{round}\left(\frac{x_{truncated}}{LIMIT} \times 127\right) \quad (\text{EQ 5.12})$$

where: x is an input signal specified by the **DIGITIZER 1(5) SOURCE** setting

LIMIT is defined by the **DIGITIZER 1(5) LIMIT** setting

round is an operand that rounds up to the nearest integer

N_{out} is an integer output of the digitizer (from -127 to 127)

The output characteristic of the Digitizer element is illustrated below:

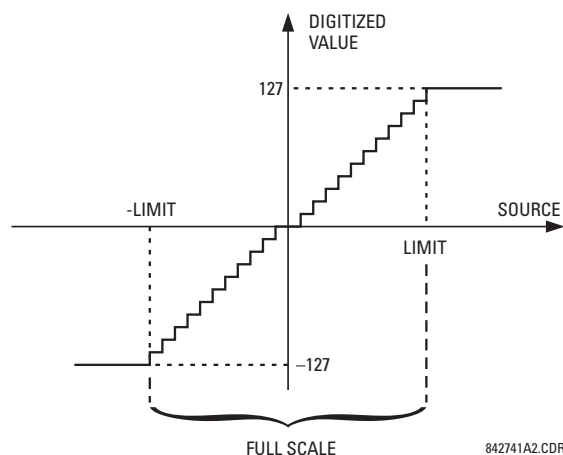


Figure 5-64: DIGITIZER OUTPUT CHARACTERISTIC

The output integer number is represented by eight (8) FlexLogic™ operands encoding the input value per the following convention:

$$N_{out} = (-1)^{b_7} \times (2^6 b_6 + 2^5 b_5 + 2^4 b_4 + 2^3 b_3 + 2^2 b_2 + 2^1 b_1 + 2^0 b_0) \quad (\text{EQ 5.13})$$

The output operands can be configured to drive relay outputs, such as remote outputs, direct outputs, contact outputs, or any combination of these, in order to relay the analog quantity to other UR-series IEDs for further processing.

The digitizer averages the signal prior to digitization for increased accuracy. The extra filtering is in effect only under steady-state conditions or during slow changes of the input value. When the input signal changes significantly, the filtering is removed to maintain the response speed of the element.

For critical applications, users should ensure the validity of the digitizer input signal to prevent an undesired response of the relaying system. For example, configuring the VT Fuse Failure condition when the digitizer uses the voltage signal.

The digitized output value is available as an actual value for testing, troubleshooting and special applications (refer to the **ACTUAL VALUES** ⇒ **METERING** ⇒ **DIGITIZERS** values for details).

- **DIGITIZER 1 SOURCE:** This setting specifies an internal analog value to be digitized. A variety of analog actual values (FlexAnalog parameters), such as power, current amplitude, voltage amplitude, power factor, etc., can be configured as the digitizer source. Refer to Appendix A for a complete list of FlexAnalog parameters available for the N60.
- **DIGITIZER 1 LIMIT:** This setting allows setting symmetrical limits for the input signal. The input signal is truncated to an interval from $-\text{LIMIT}$ to $+\text{LIMIT}$ before the effective digitization. The base units are defined in the exact manner as FlexElements™ (see the FlexElements™ section for details).

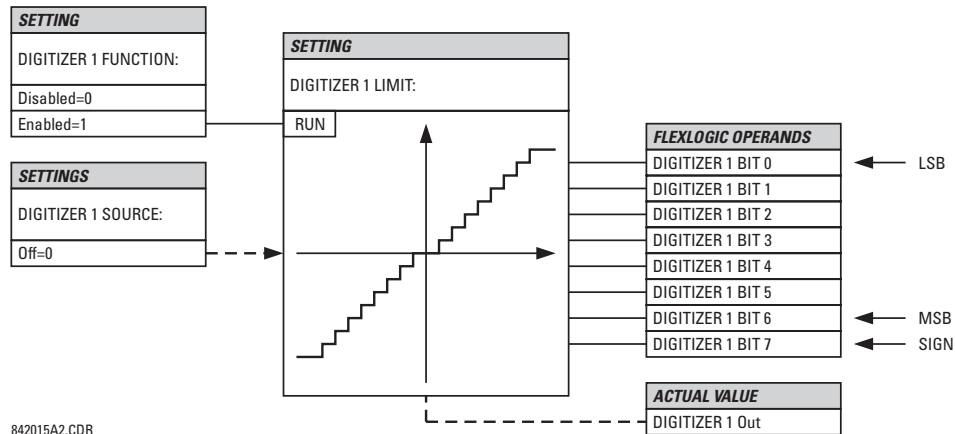


Figure 5-65: DIGITIZER LOGIC

APPLICATION EXAMPLE 1:

A three phase active power on a 13.8kV system measured via UR source 1 is to be sent via Direct Outputs, bits 20 through 27, to a remote location. The following settings are applied on the relay: CT ratio = 1200:5, VT ratio = 120, and VT secondary = 66.4 V. The nominal current is 800 A primary and the nominal power factor is 0.90. The power is to be monitored in both importing and exporting directions and allow for 20% overload as compared with the nominal.

- Nominal three-phase power: $P = \sqrt{3} \times 13.8 \text{ kV} \times 0.8 \text{ kA} \times 0.9 = 17.21 \text{ MW}$.
- Three-phase power with the 20% overload margin: $P_{max} = 1.2 \times 17.21 \text{ MW} = 20.65 \text{ MW}$.
- UR base unit for power: $P_{BASE} = \text{VT ratio} \times \text{VT secondary} \times \text{CT primary} = 120 \times 66.4 \text{ V} \times 1.2 \text{ kA} = 9.56 \text{ MW}$ (see the FlexElements section for further details).
- Maximum power to be monitored (pu): $20.65 \text{ MW} / 9.56 \text{ MW} = 2.160 \text{ pu}$.

The following settings should be applied: **DIGITIZER 1 SOURCE:** "SRC 1" and **DIGITIZER 1 LIMIT:** "2.160 pu".

Using the above settings, the output represents power with the scale of 127 per 20.65 MW; for example, 1 count of a digitizer output represent $20.65 \text{ MW} / 127 = 162.6 \text{ kW}$.

The Direct Outputs should be configured as follows:

- | | |
|---|---|
| DIRECT OUT 20 OPERAND: "DIGITIZER 1 BIT 0" | DIRECT OUT 24 OPERAND: "DIGITIZER 1 BIT 4" |
| DIRECT OUT 21 OPERAND: "DIGITIZER 1 BIT 1" | DIRECT OUT 25 OPERAND: "DIGITIZER 1 BIT 5" |
| DIRECT OUT 22 OPERAND: "DIGITIZER 1 BIT 2" | DIRECT OUT 26 OPERAND: "DIGITIZER 1 BIT 6" |
| DIRECT OUT 23 OPERAND: "DIGITIZER 1 BIT 3" | DIRECT OUT 27 OPERAND: "DIGITIZER 1 BIT 7" |

On the receiving location, bits 20 through 27 should be configured to drive eight Direct Inputs. The latter should be configured accordingly to use the incoming information (typically via the 8-Bit Comparator feature).

The following table illustrates this application for four sample power values.

POWER	N _{out}	B7... B0
15.00 MW	92	01011100
20.00 MW	123	01111011
-5.00 MW	-31	10011111
-12.00 MW	-74	11001010

APPLICATION EXAMPLE 2:

The phase A current (true RMS value) is to be sent via Remote Outputs (MMS GOOSE) to several remote IEDs (DNA points 14 through 20). The CT ratio is 5000:5 and the maximum load current is 4200 A. The current should be monitored allowing for 50% overload.

- Phase current with the 50% overload margin: $I_{max} = 1.5 \times 4.2 \text{ kA} = 6.3 \text{ kA}$.
- UR base unit for current: $I_{BASE} = 5 \text{ kA}$ (see FlexElements section for further details).
- Minimum current to be monitored (pu): $6.3 \text{ kA} / 5 \text{ kA} = 1.26 \text{ pu}$

The following settings should be applied: **DIGITIZER 1 SOURCE**: "SRC 1 Ia RMS" and **DIGITIZER 1 LIMIT**: "1.260 pu"

Using the above settings, the output represents current magnitude with the scale of 127 per 6.3kA; for example, 1 count of a digitizer output represents $6.3 \text{ kA} / 127 = 49.6 \text{ A}$.

The Remote Outputs should be configured as follows:

DNA- 14 OPERAND: "DIGITIZER 1 BIT 0"

DNA- 15 OPERAND: "DIGITIZER 1 BIT 1"

DNA- 16 OPERAND: "DIGITIZER 1 BIT 2"

DNA- 17 OPERAND: "DIGITIZER 1 BIT 3"

DNA- 18 OPERAND: "DIGITIZER 1 BIT 4"

DNA- 19 OPERAND: "DIGITIZER 1 BIT 5"

DNA- 20 OPERAND: "DIGITIZER 1 BIT 6"

As the input signal (true RMS) is always positive, the sign bit could be ignored and only 7 points need to be exchanged. On the receiving location, incoming bits 14 through 20 should be configured to drive Remote Inputs. The latter together with the FlexLogic™ constant "Off" for the sign bit should be configured accordingly to use the incoming information (typically via the 8-Bit Comparator feature).

The following table illustrates this application for four sample current values.

CURRENT	N _{out}	B7... B0
1.00 kA	20	00010100
2.50 kA	50	00110010
4.20 kA	85	01010101
6.00 kA	121	01111001

5.6.12 8-BIT COMPARATORS

PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ 8-BIT COMPARATORS ⇒ 8-BIT COMP 1(6)

<div> <div>8-BIT</div> <div>COMP 1</div> </div>		<div> <div>8BIT COMP 1</div> <div>FUNCTION: Disabled</div> </div>	Range: Disabled, Enabled
MESSAGE	<div> <div>8BIT COMP 1 ARG A0:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
MESSAGE	<div> <div>8BIT COMP 1 ARG A1:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
↓			
MESSAGE	<div> <div>8BIT COMP 1 ARG A7:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
MESSAGE	<div> <div>8BIT COMP 1 ARG B0:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
MESSAGE	<div> <div>8BIT COMP 1 ARG B1:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
↓			
MESSAGE	<div> <div>8BIT COMP 1 ARG B7:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
MESSAGE	<div> <div>8BIT COMP 1 INPUT</div> <div>MODE: Signed</div> </div>	Range: Signed, Absolute	
MESSAGE	<div> <div>8BIT COMP 1</div> <div>DIRECTION: Over</div> </div>	Range: Over, Under	
MESSAGE	<div> <div>8BIT COMP 1 ADD/SUB:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
MESSAGE	<div> <div>8BIT COMP 1 PICKUP:</div> <div>10000.00</div> </div>	Range: -25400.00 to 25400.00 in steps of 0.01	
MESSAGE	<div> <div>8BIT COMP 1</div> <div>HYSTERESIS: 500.00</div> </div>	Range: 0 to 5000.00 in steps of 0.01	
MESSAGE	<div> <div>8BIT COMP 1 PICKUP</div> <div>DELAY: 0.000 s</div> </div>	Range: 0 to 65.353 s in steps of 0.001	
MESSAGE	<div> <div>8BIT COMP 1 RESET</div> <div>DELAY: 0.000 s</div> </div>	Range: 0 to 65.353 s in steps of 0.001	
MESSAGE	<div> <div>8BIT COMP 1 SCALE</div> <div>FACTOR: 1.00</div> </div>	Range: 0.01 to 100.00 in steps of 0.01	
MESSAGE	<div> <div>8BIT COMP 1 BLOCK:</div> <div>Off</div> </div>	Range: FlexLogic™ operand	
MESSAGE	<div> <div>8BIT COMP 1 TARGET:</div> <div>Self-reset</div> </div>	Range: Self-reset, Latched, Disabled	
MESSAGE	<div> <div>8BIT COMP 1 EVENTS:</div> <div>Disabled</div> </div>	Range: Disabled, Enabled	

This feature allows simple manipulations on 8-bit numbers. In particular two numbers can be added or subtracted depending on a user-programmable dynamic condition. User-programmable threshold, hysteresis, direction of comparison, pickup and dropout time delays can be applied.

The two input arguments are user-configurable as collections of FlexLogic™ operands using the binary encoding convention. This allows bringing analog values from remote locations via communication-based inputs/outputs to facilitate telemetry and other advanced applications. The two values to be added or subtracted should have a common scaling and should be of the same type. For example, adding voltage and current will not yield meaningful results.

Typically this element is applied in conjunction with the Digitizer feature.

- **8BIT COMP 1 ARG A0 to 8BIT COMP 1 ARG A7:** These settings specify FlexLogic™ operands that provide an 8-bit representation of the first argument, A , of the comparator. The **8BIT COMP 1 ARG A0** setting represents the least significant bit, while the **8BIT COMP 1 ARG A6** setting represents the most significant bit. The **8BIT COMP 1 ARG A7** setting represents the sign bit (asserted for negative values). In other words, the following convention is used:

$$A = (-1)^{a_7} \times (2^6 a_6 + 2^5 a_5 + 2^4 a_4 + 2^3 a_3 + 2^2 a_2 + 2^1 a_1 + 2^0 a_0) \quad (\text{EQ 5.14})$$

Care must be taken to avoid race conditions for the used bits (FlexLogic™ operands). If the 8 bits are not changing simultaneously (for example, when some of the bits arrive via contact inputs and the others arrive via direct inputs) a race condition may occur, leading to severe over- or under-estimation of the resulting 8-bit number. For example, assume the integer value changes from 15 to 16, i.e. from 00001111 to 00010000, but transiently the A4 bit asserts before the A3 through A0 bits de-assert. As a result, a value of 00011111, or 31, is produced, resulting in almost 100% overestimation compared to a true value of 16.

To prevent problems, pickup and dropout time delays must be applied to avoid misoperation, or all 8 bits must be configured to change simultaneously (for example, applied to the relay via the same communications).

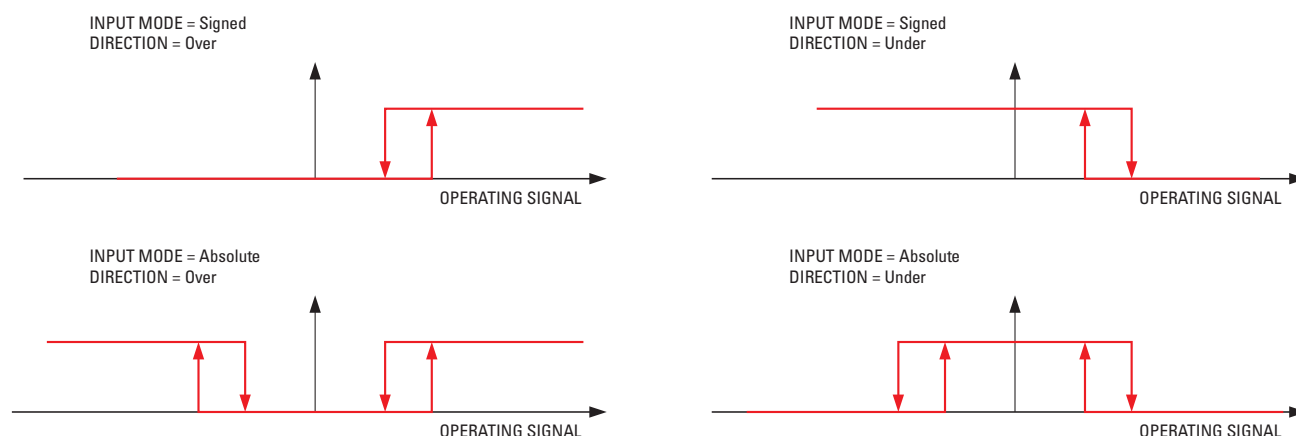
- **BIT COMP 1 ARG B0 to 8BIT COMP 1 ARG B7:** These settings specify FlexLogic™ operands that provide an 8-bit representation of the second argument, B , of the comparator. The **8BIT COMP 1 ARG B0** setting represents the least significant bit, while the **8BIT COMP 1 ARG B6** setting represents the most significant bit. The **8BIT COMP 1 ARG B7** setting is the sign bit (asserted for negative values). In other words the following convention is used:

$$B = (-1)^{b_7} \times (2^6 b_6 + 2^5 b_5 + 2^4 b_4 + 2^3 b_3 + 2^2 b_2 + 2^1 b_1 + 2^0 b_0) \quad (\text{EQ 5.15})$$

- **8BIT COMP 1 INPUT MODE:** This setting specifies whether a signed or absolute value should be used for comparison with the pickup threshold. This setting applies to the effective operating signal (that is, either $A - B$ or $A + B$) and not to the individual inputs. The figure below illustrates an effective operating characteristic resulting from this setting.

The **8BIT COMP 1 Out** actual value, as well as the **8BIT COMP1 BIT0** through **8BIT COMP1 BIT7** FlexLogic™ operands, are derived without reference to this setting.

- **8BIT COMP 1 DIRECTION:** This setting specifies if the element should operate if the effective operating signal is above ("Over") or below ("Under") the threshold as illustrated in the following figure.

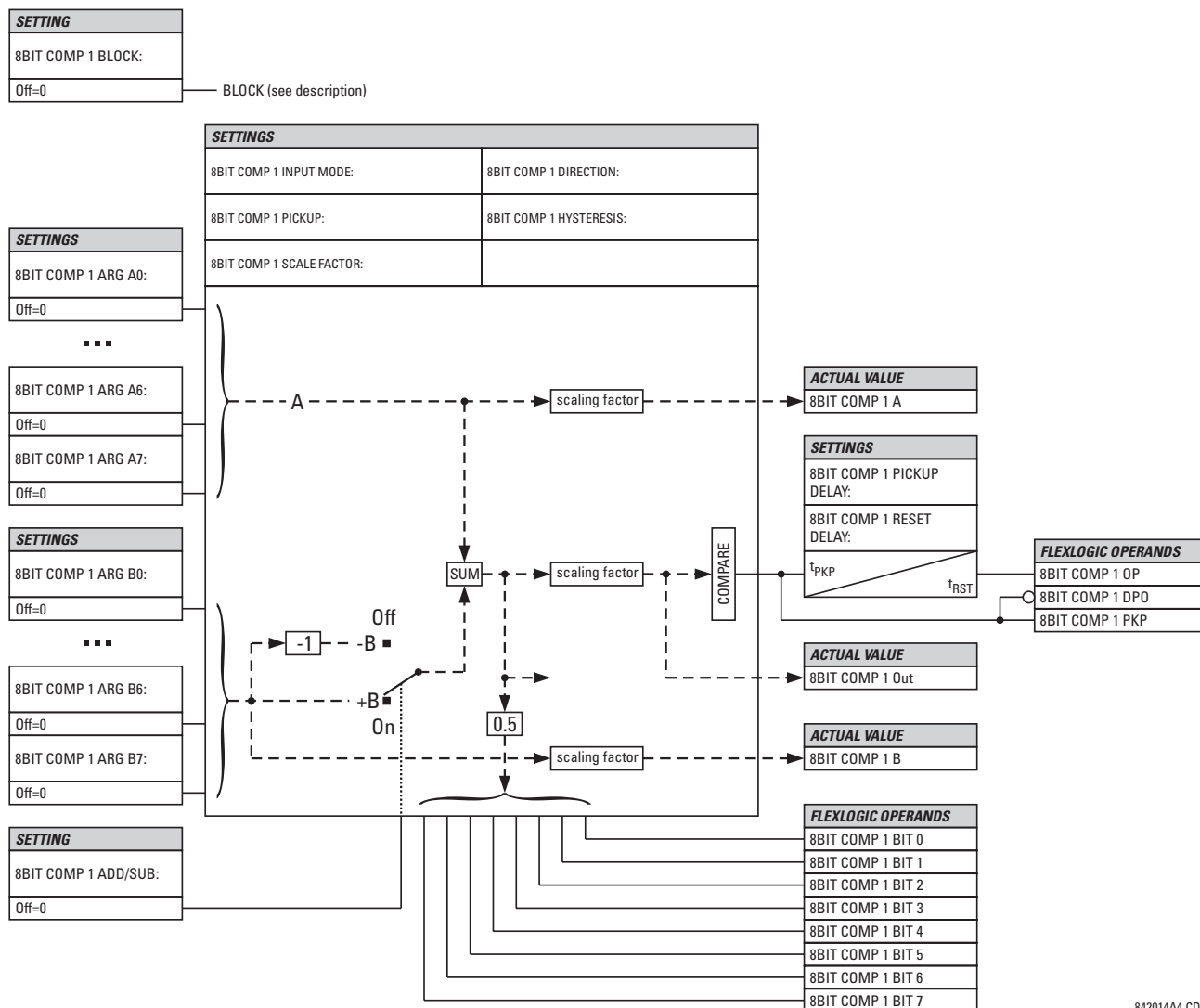


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Figure 5-66: OPERATING CHARACTERISTICS OF THE 8-BIT COMPARATOR

- **8BIT COMP 1 ADD/SUB:** This setting specifies if the two arguments, *A* and *B*, should be added or subtracted to form the effective operating signal. If the FlexLogic™ operand configured under this setting is logic 0 (“Off”), the operating signal is $A - B$. If the FlexLogic™ operand configured under this setting is logic 1 (“On”), the operating signal is $A + B$. The element will switch between adding and subtracting instantly, without any additional delay.
- **8BIT COMP 1 PICKUP:** This setting specifies the pickup threshold for the comparator. This setting applies to the value scaled via the **8BIT COMP 1 SCALE FACTOR** setting.
- **8BIT COMP 1 HYSTERESIS:** Specifies the width of hysteresis for the comparator. The following logic applies:
 If **8BIT COMP 1 DIRECTION** = “Over”, then Dropout := **8BIT COMP 1 PICKUP** – **8BIT COMP 1 HYSTERESIS**
 If **8BIT COMP 1 DIRECTION** = “Under”, then Dropout := **8BIT COMP 1 PICKUP** + **8BIT COMP 1 HYSTERESIS**
- **8BIT COMP 1 PICKUP DELAY:** Specifies a pickup time delay for the 8BIT COMP 1 OP FlexLogic™ operand.
- **8BIT COMP 1 RESET DELAY:** Specifies a reset time delay for the 8BIT COMP 1 OP FlexLogic™ operand.
- **8BIT COMP 1 SCALE FACTOR:** This setting allows re-scaling the two input arguments and the effective operating quantity. The same scaling factor applies to all three actual values: **8BIT COMP 1 A**, **8BIT COMP 1 B** and **8BIT COMP 1 OUT**. The scaling enables easier application, testing and troubleshooting. Also, it facilitates telemetry applications.
- **8BIT COMP 1 BLOCK:** This setting specifies a FlexLogic™ operand for blocking the feature based on user-programmable conditions. When the blocking input is asserted, the element resets its timers, de-asserts the 8BIT COMP 1 PKP and 8BIT COMP 1 OP operands (if asserted), clears self-reset targets, logs a ‘blocked’ event if Events are enabled, and becomes inactive. When unblocked, the element will start operating instantly. If exposed to pickup conditions for an extended period of time and unblocked, the element will pickup and start timing out at the moment of unblocking.

Typical applications of the **8BIT COMP 1 BLOCK** setting include conditions when the input signals are not reliable. For example, remote power may be digitized using the Digitizer feature and sent via Remote I/Os to be used locally by the 8-Bit Comparator. The VT Fuse Fail at the remote location, or the Remote Device Offline indication may be used to block this element.



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Figure 5-67: 8-BIT COMPARATOR LOGIC

APPLICATION EXAMPLE:

A three phase active power measured via SRC 1 on the UR-1 IED is to be sent to a remote location UR-2 via the Direct Inputs/Outputs feature. Upon reception the remote power is to be added to a three phase active power measured locally by the UR-2 IED via SRC 4. The application should work accurately under an overload of 25% and a power factor of 1.0 at either location.

An alarm should be established at the UR-2 location if the total power is above 2500 MW for 50 ms. The alarm should be cleared if the power is below 2000 MW for 1 second.

The following settings and conditions are given.

- UR-1: Nominal system voltage = 345 kV, SRC 1 CT ratio = 2000:5, Normal load current = 1.8 kA.
- UR-2: Nominal system voltage = 220 kV, SRC 4 CT ratio = 3000:5, Normal load current = 2.5 kA.

The application requires the following features:

For UR-1: Digitizer to prepare the measured power for sending to the UR-2 IED, and Direct Outputs to send the digitized power to the UR-2 IED.

For UR-2: • Digitizer to prepare the measured power for adding to the remote power,
Direct Inputs to receive the remote power from the UR-1 IED,
8-Bit Comparator to sum the power values and apply a threshold, and
User-definable display messages for telemetry (optional).

The maximum active power at the UR-1 location is: $P = \sqrt{3} \times 345 \text{ kV} \times 1.25 \times 1.8 \text{ kA} \times 1.0 = 1345 \text{ MW}$

The maximum active power at the UR-2 location is: $P = \sqrt{3} \times 220 \text{ kV} \times 1.25 \times 2.5 \text{ kA} \times 1.0 = 1191 \text{ MW}$

For the two power values to be subsequently added, a common base must be selected for digitization. For this example, an arbitrary value of 1500 MW is selected as a base and maximum value for digitization. Consequently, 1 count of digitized power at either location is worth $1500 \text{ MW} / 127 = 11.811 \text{ MW}$ (refer to the Digitizers section in this chapter for details).

For the Digitizer at UR-1:

1. UR base unit for power given by $P_{BASE} = 345 \text{ kV} \times 2 \text{ kA} = 690 \text{ MW}$ (refer to the FlexElements™ section for details).
2. The maximum power to be monitored (in pu) given by $1500 \text{ MW} / 690 \text{ MW} = 2.174 \text{ pu}$.
3. The following settings should be applied:

DIGITIZER 1 SOURCE: "SRC 1 P" and **DIGITIZER 1 LIMIT** = "2.174 pu"

For the Digitizer at UR-2:

1. UR base unit for power given by $P_{BASE} = 220 \text{ kV} \times 3 \text{ kA} = 660 \text{ MW}$ (refer to the FlexElements™ section for details).
2. The maximum power to be monitored (in pu) given by $1500 \text{ MW} / 660 \text{ MW} = 2.273 \text{ pu}$.
3. The following settings should be applied:

DIGITIZER 1 SOURCE: "SRC 4 P" and **DIGITIZER 1 LIMIT** = "2.273 pu"

For the Direct Outputs at UR-1:

1. Assume the UR-1 IED is labeled as Direct Device 1 (**DIRECT OUTPUT DEVICE ID** = "1").
2. Assume bits 1 to 8 are used for sending the required power to the UR-2 IED. The following settings should be applied:

DIRECT OUT 1 OPERAND: "DIGITIZER 1 BIT 0"	DIRECT OUT 5 OPERAND: "DIGITIZER 1 BIT 4"
DIRECT OUT 2 OPERAND: "DIGITIZER 1 BIT 1"	DIRECT OUT 6 OPERAND: "DIGITIZER 1 BIT 5"
DIRECT OUT 3 OPERAND: "DIGITIZER 1 BIT 2"	DIRECT OUT 7 OPERAND: "DIGITIZER 1 BIT 6"
DIRECT OUT 4 OPERAND: "DIGITIZER 1 BIT 3"	DIRECT OUT 8 OPERAND: "DIGITIZER 1 BIT 7"

For the Direct Inputs at UR-2:

1. Apply the following settings (Direct Inputs 10 through 17 are used) to receive the digitized power from the UR-1 IED:

DIRECT INPUT 10 DEVICE ID: "1"	DIRECT INPUT 10 BIT NUMBER: "1"
DIRECT INPUT 11 DEVICE ID: "1"	DIRECT INPUT 11 BIT NUMBER: "2"
DIRECT INPUT 12 DEVICE ID: "1"	DIRECT INPUT 12 BIT NUMBER: "3"
DIRECT INPUT 13 DEVICE ID: "1"	DIRECT INPUT 13 BIT NUMBER: "4"
DIRECT INPUT 14 DEVICE ID: "1"	DIRECT INPUT 14 BIT NUMBER: "5"
DIRECT INPUT 15 DEVICE ID: "1"	DIRECT INPUT 15 BIT NUMBER: "6"
DIRECT INPUT 16 DEVICE ID: "1"	DIRECT INPUT 16 BIT NUMBER: "7"
DIRECT INPUT 17 DEVICE ID: "1"	DIRECT INPUT 17 BIT NUMBER: "8"

2. The remote power will be available as the DIRECT INPUT 10 On (the least significant bit) through DIRECT INPUT 16 On (the most significant bit) and DIRECT INPUT 17 On (the sign bit) FlexLogic™ operands.

For the 8-Bit Comparator at UR-2:

1. Remote power from UR-1 IED received on Direct Inputs is the first argument for the comparator. As such, the following settings should be applied:

8 BIT COMP 1 ARG A0: "DIRECT INPUT 1 On"	8 BIT COMP 1 ARG A4: "DIRECT INPUT 5 On"
8 BIT COMP 1 ARG A1: "DIRECT INPUT 2 On"	8 BIT COMP 1 ARG A5: "DIRECT INPUT 6 On"
8 BIT COMP 1 ARG A2: "DIRECT INPUT 3 On"	8 BIT COMP 1 ARG A6: "DIRECT INPUT 7 On"
8 BIT COMP 1 ARG A3: "DIRECT INPUT 4 On"	8 BIT COMP 1 ARG A7: "DIRECT INPUT 8 On"

2. Local power from the UR-2 IED available internally is the second argument for the comparator. Thus, the following settings should be applied:

8 BIT COMP 1 ARG B0: "DIGITIZER 1 BIT 0"
8 BIT COMP 1 ARG B1: "DIGITIZER 1 BIT 1"
8 BIT COMP 1 ARG B2: "DIGITIZER 1 BIT 2"
8 BIT COMP 1 ARG B3: "DIGITIZER 1 BIT 3"

8 BIT COMP 1 ARG B4: "DIGITIZER 1 BIT 4"
8 BIT COMP 1 ARG B5: "DIGITIZER 1 BIT 5"
8 BIT COMP 1 ARG B6: "DIGITIZER 1 BIT 6"
8 BIT COMP 1 ARG B7: "DIGITIZER 1 BIT 7"

3. The two power values are to be added, therefore set **8BIT COMP 1 ADD/SUB** to "On".
4. The alarm should be established when the total power in *either* direction (import or export) is above the threshold, therefore set **8BIT COMP 1 INPUT MODE** to "Absolute".
5. The alarm should also be established when the total power is *above* the threshold, therefore set the **8BIT COMP 1 DIRECTION** to "Over".
6. The digitized power values should be scaled to meaningful values. Assuming that MW are to be used for comparison and display, set **8BIT COMP 1 SCALE FACTOR** to "11.81". In this way, the **8BIT COMP 1 A**, **8BIT COMP 1 B**, and **8BIT COMP 1 Out** actual values represent the remote (UR-1), local (UR-2), and total power values in MW.
7. The alarm should also be established when the total power is greater than 2500 MW. As such, set **8BIT COMP 1 PICKUP** to "2500.00".
8. The alarm should be cleared when the total power is less than 2000 MW. As such, the hysteresis setting should be $2500 - 2000 = 500$ MW. Set **8BIT COMP 1 HYSTERESIS** to "500".
9. Using the timing requirements, set **8BIT COMP 1 PICKUP DELAY** to "0.050" and **8BIT COMP 1 RESET DELAY** to "1.000". The **8BIT COMP 1 OP FlexLogic™** operand should be used to set the alarm.
10. For monitoring and telemetry purposes, the digitized power values should be scaled to meaningful values. Assuming MW to be displayed, set **8BIT COMP 1 SCALE FACTOR** to "11.81". In this way, the actual values **8BIT COMP 1 A**, **8BIT COMP 1 B** and **8BIT COMP 1 Out** will represent the remote (UR-1), local (UR-2), and total power values in MW.

For the User-definable displays at UR-2 (optional):

To display the remote and total power values the following screen may be set up:

DISP 1 TOP LINE: "REMOTE MW: ~"
DISP 1 BOTTOM LINE: "TOTAL MW: ~"
DISP 1 ITEM 1: "8100" (Modbus register address of **8BIT COMP 1 A**)
DISP 1 ITEM 2: "8104" (Modbus register address of **8BIT COMP 1 Out**)

Analysis of metering errors at UR-2:

The remote and local power values are measured with the maximum error of $\pm 1\%$ of reading (at power factors above 0.8). The digitization (round up) error is 0.2% of full scale, or $0.002 \times 2 \times 1500 \text{ MW} = 6 \text{ MW}$. Consequently,

- Remote power is measured at UR-2 with a worst-case error of $\pm (1\% \text{ of reading} + 6 \text{ MW})$.
- Total power is measured at UR-2 with a worst-case error of $\pm (1\% \text{ of UR-1 reading} + 1\% \text{ of UR-2 reading} + 12 \text{ MW})$.

For example, with a reading of 1020 MW at UR-1 and 800 MW at UR-2, the remote power is measured at UR-2 with the worst-case error of $\pm(0.01 \times 1020 \text{ MW} + 6 \text{ MW}) = \pm 16.2 \text{ MW}$, or $\pm 1.6\%$. The reading for remote power at UR-2 is the product of the digitized value sent from UR-1 and received at UR-2 and the scale factor setting; that is,

$$\begin{aligned} \text{Total Power at UR-1} &= \left(\text{round}\left(\frac{1020 \text{ MW}}{1500 \text{ MW}}\right) \times 127 \right) \times 11.81 \text{ MW} \\ &= 86 \times 11.81 \text{ MW} = 1015.7 \text{ MW} \end{aligned} \quad (\text{EQ 5.16})$$

Therefore, the reading for the remote power at UR-2 is 1015.7 MW, versus 1020 MW at the point of origin (UR-1).

The total power is measured at UR-2 with the worst-case error of $\pm (0.01 \times 1020 \text{ MW} + 0.01 \times 800 \text{ MW} + 12 \text{ MW}) = \pm 30.2 \text{ MW}$, or $\pm 1.7\%$. The reading for the total power at UR-2 is the sum of the digitized value sent from UR-1 and received at UR-2 and the digitized value at UR-2 multiplied by the scaling factor; that is,

$$\begin{aligned} \text{Total Power at UR-2} &= \left(\text{round}\left(\frac{1020 \text{ MW}}{1500 \text{ MW}}\right) \times 127 \right) + \text{round}\left(\frac{800 \text{ MW}}{1500 \text{ MW}}\right) \times 127 \times 11.81 \text{ MW} \\ &= (86 + 68) \times 11.81 \text{ MW} = 1818.7 \text{ MW} \end{aligned} \quad (\text{EQ 5.17})$$

Therefore, the reading for the total power at UR-2 is 1818.7 MW, versus a true total of $1020 \text{ MW} + 800 \text{ MW} = 1820 \text{ MW}$.

5.6.13 8-BIT SWITCHES

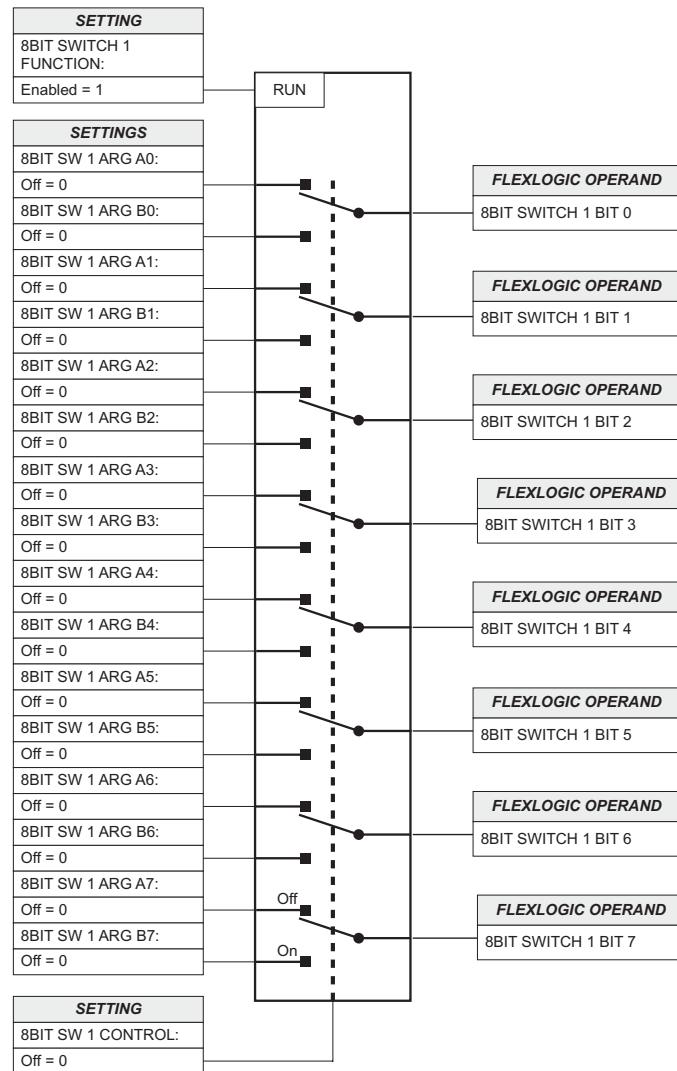
PATH: SETTINGS ⇒ CONTROL ELEMENTS ⇒ 8-BIT SWITCHES ⇒ 8-BIT SWITCH 1(6)

<div> <div>8-BIT</div> <div>SWITCH 1</div> </div>	<div> <div>8BIT SWITCH 1</div> <div>FUNCTION: Disabled</div> </div>	Range: Disabled, Enabled
MESSAGE	<div>8BIT SW 1 ARG A0:</div> <div>Off</div>	Range: FlexLogic™ operand
MESSAGE	<div>8BIT SW 1 ARG A1:</div> <div>Off</div>	Range: FlexLogic™ operand
	↓	
MESSAGE	<div>8BIT SW 1 ARG A7:</div> <div>Off</div>	Range: FlexLogic™ operand
MESSAGE	<div>8BIT SW 1 ARG B0:</div> <div>Off</div>	Range: FlexLogic™ operand
MESSAGE	<div>8BIT SW 1 ARG B1:</div> <div>Off</div>	Range: FlexLogic™ operand
	↓	
MESSAGE	<div>8BIT SW 1 ARG B7:</div> <div>Off</div>	Range: FlexLogic™ operand
MESSAGE	<div>8BIT SW 1 CONTROL:</div> <div>Off</div>	Range: FlexLogic™ operand

This feature allows switching between two input arguments defined by 8 bits each. The bits are specified by FlexLogic™ operands. The feature could be viewed as an integrated two-position switch for 8 logic signals.

Typically this element is applied in conjunction with the Digitizer and 8-bit Comparator features.

- **8BIT SW 1 ARG A0 to 8BIT SW 1 ARG A7:** These settings specify FlexLogic™ operands that constitute the first (A) input of the switch. These operands are routed to the output operands if the control input is in the "Off" position.
- **8BIT SW 1 ARG B0 to 8BIT SW 1 ARG B7:** These settings specify FlexLogic™ operands that constitute the second (B) input of the switch. These operands are routed to the output operands if the control input is in the "On" position.
- **8BIT SW 1 CONTROL:** This setting specifies FlexLogic™ operands to control the routing between the A and B inputs of the switch. If the control operand is in the "Off" state, the first (A) input is switched to the output. If the control operand is in the "On" state, the second (B) input is switched to the output. The switching takes place instantaneously.

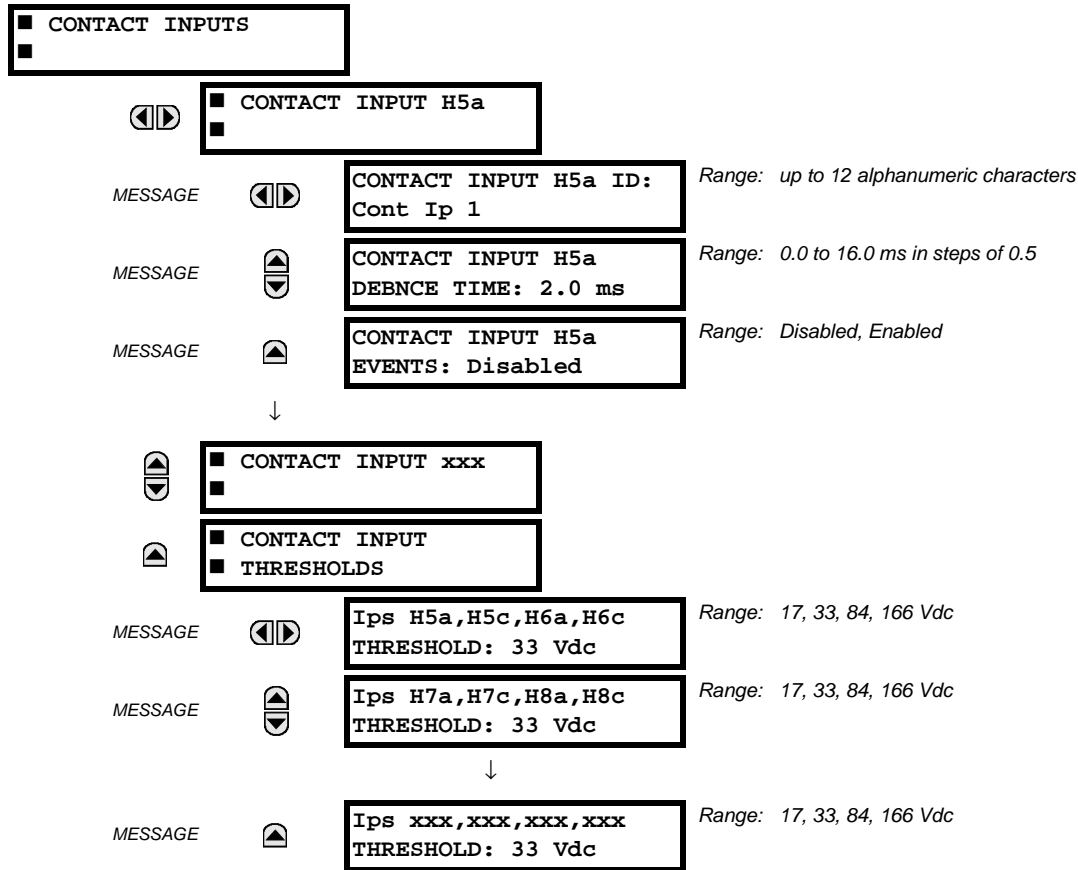


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Figure 5–68: 8-BIT SWITCH LOGIC

5.7.1 CONTACT INPUTS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ CONTACT INPUTS



The contact inputs menu contains configuration settings for each contact input as well as voltage thresholds for each group of four contact inputs. Upon startup, the relay processor determines (from an assessment of the installed modules) which contact inputs are available and then display settings for only those inputs.

An alphanumeric ID may be assigned to a contact input for diagnostic, setting, and event recording purposes. The **CONTACT IP X On** (Logic 1) FlexLogic™ operand corresponds to contact input “X” being closed, while **CONTACT IP X Off** corresponds to contact input “X” being open. The **CONTACT INPUT DEBNCE TIME** defines the time required for the contact to overcome ‘contact bouncing’ conditions. As this time differs for different contact types and manufacturers, set it as a maximum contact debounce time (per manufacturer specifications) plus some margin to ensure proper operation. If **CONTACT INPUT EVENTS** is set to “Enabled”, every change in the contact input state will trigger an event.

A raw status is scanned for all Contact Inputs synchronously at the constant rate of 0.5 ms as shown in the figure below. The DC input voltage is compared to a user-settable threshold. A new contact input state must be maintained for a user-settable debounce time in order for the N60 to validate the new contact state. In the figure below, the debounce time is set at 2.5 ms; thus the 6th sample in a row validates the change of state (mark no. 1 in the diagram). Once validated (debounced), the contact input asserts a corresponding FlexLogic™ operand and logs an event as per user setting.

A time stamp of the first sample in the sequence that validates the new state is used when logging the change of the contact input into the Event Recorder (mark no. 2 in the diagram).

Protection and control elements, as well as FlexLogic™ equations and timers, are executed eight times in a power system cycle. The protection pass duration is controlled by the frequency tracking mechanism. The FlexLogic™ operand reflecting the debounced state of the contact is updated at the protection pass following the validation (marks no. 3 and 4 on the figure below). The update is performed at the beginning of the protection pass so all protection and control functions, as well as FlexLogic™ equations, are fed with the updated states of the contact inputs.

The FlexLogic™ operand response time to the contact input change is equal to the debounce time setting plus up to one protection pass (variable and depending on system frequency if frequency tracking enabled). If the change of state occurs just after a protection pass, the recognition is delayed until the subsequent protection pass; that is, by the entire duration of the protection pass. If the change occurs just prior to a protection pass, the state is recognized immediately. Statistically a delay of half the protection pass is expected. Owing to the 0.5 ms scan rate, the time resolution for the input contact is below 1msec.

For example, 8 protection passes per cycle on a 60 Hz system correspond to a protection pass every 2.1 ms. With a contact debounce time setting of 3.0 ms, the FlexLogic™ operand-assert time limits are: $3.0 + 0.0 = 3.0$ ms and $3.0 + 2.1 = 5.1$ ms. These time limits depend on how soon the protection pass runs after the debouncing time.

Regardless of the contact debounce time setting, the contact input event is time-stamped with a 1 μ s accuracy using the time of the first scan corresponding to the new state (mark no. 2 below). Therefore, the time stamp reflects a change in the DC voltage across the contact input terminals that was not accidental as it was subsequently validated using the debounce timer. Keep in mind that the associated FlexLogic™ operand is asserted/de-asserted later, after validating the change.

The debounce algorithm is symmetrical: the same procedure and debounce time are used to filter the LOW-HIGH (marks no.1, 2, 3, and 4 in the figure below) and HIGH-LOW (marks no. 5, 6, 7, and 8 below) transitions.

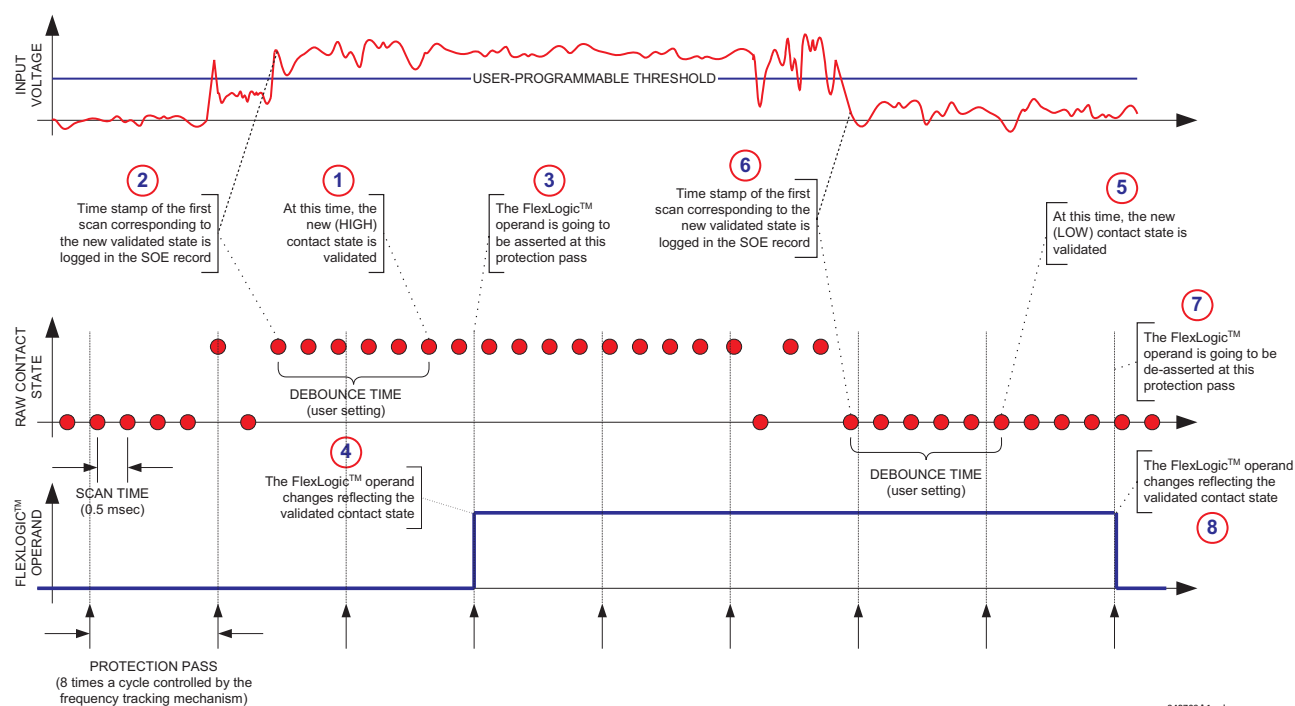


Figure 5-69: INPUT CONTACT DEBOUNCING MECHANISM AND TIME-STAMPING SAMPLE TIMING

Contact inputs are isolated in groups of four to allow connection of wet contacts from different voltage sources for each group. The **CONTACT INPUT THRESHOLDS** determine the minimum voltage required to detect a closed contact input. This value should be selected according to the following criteria: 17 for 24 V sources, 33 for 48 V sources, 84 for 110 to 125 V sources and 166 for 250 V sources.

For example, to use contact input H5a as a status input from the breaker 52b contact to seal-in the trip relay and record it in the Event Records menu, make the following settings changes:

CONTACT INPUT H5A ID: "Breaker Closed (52b)"

CONTACT INPUT H5A EVENTS: "Enabled"

Note that the 52b contact is closed when the breaker is open and open when the breaker is closed.

5.7.2 VIRTUAL INPUTS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ VIRTUAL INPUTS ⇒ VIRTUAL INPUT 1(64)

VIRTUAL INPUT 1	◀▶	VIRTUAL INPUT 1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	⬆⬇	VIRTUAL INPUT 1 ID: Virt Ip 1	Range: Up to 12 alphanumeric characters
MESSAGE	⬆⬇	VIRTUAL INPUT 1 TYPE: Latched	Range: Self-Reset, Latched
MESSAGE	⬆	VIRTUAL INPUT 1 EVENTS: Disabled	Range: Disabled, Enabled

There are 64 virtual inputs that can be individually programmed to respond to input signals from the keypad (Commands menu) and communications protocols. All virtual input operands are defaulted to OFF = 0 unless the appropriate input signal is received. **Virtual input states are preserved through a control power loss.**

If the **VIRTUAL INPUT x FUNCTION** is to “Disabled”, the input will be forced to 'Off' (Logic 0) regardless of any attempt to alter the input. If set to “Enabled”, the input operates as shown on the logic diagram and generates output FlexLogic™ operands in response to received input signals and the applied settings.

There are two types of operation: Self-Reset and Latched. If **VIRTUAL INPUT x TYPE** is “Self-Reset”, when the input signal transits from OFF = 0 to ON = 1, the output operand will be set to ON = 1 for only one evaluation of the FlexLogic™ equations and then return to OFF = 0. If set to “Latched”, the virtual input sets the state of the output operand to the same state as the most recent received input, ON = 1 or OFF = 0.



The “Self-Reset” operating mode generates the output operand for a single evaluation of the FlexLogic™ equations. If the operand is to be used anywhere other than internally in a FlexLogic™ equation, it will likely have to be lengthened in time. A FlexLogic™ timer with a delayed reset can perform this function.

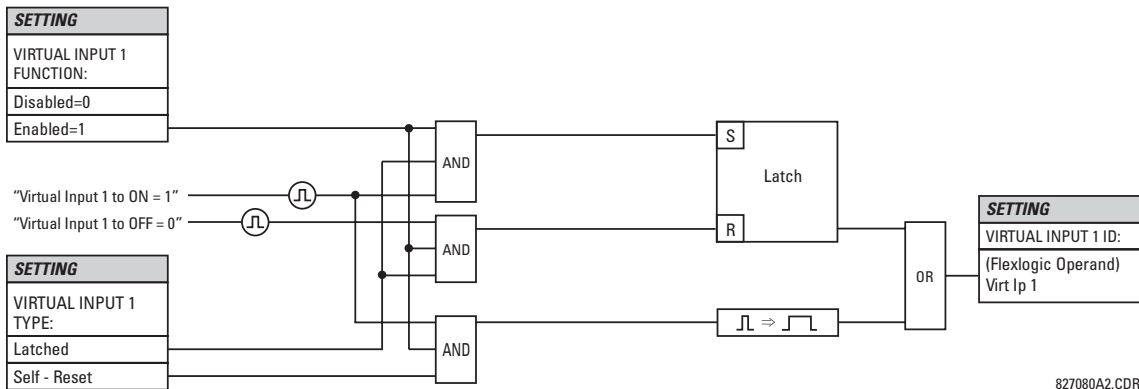


Figure 5-70: VIRTUAL INPUTS SCHEME LOGIC

5.7.3 CONTACT OUTPUTS

a) DIGITAL OUTPUTS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ CONTACT OUTPUTS ⇒ CONTACT OUTPUT H1

■ CONTACT OUTPUT H1	◀▶	CONTACT OUTPUT H1 ID Cont Op 1	Range: Up to 12 alphanumeric characters
MESSAGE	▲▼	OUTPUT H1 OPERATE: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	OUTPUT H1 SEAL-IN: Off	Range: FlexLogic™ operand
MESSAGE	▲	CONTACT OUTPUT H1 EVENTS: Enabled	Range: Disabled, Enabled

Upon startup of the relay, the main processor will determine from an assessment of the modules installed in the chassis which contact outputs are available and present the settings for only these outputs.

An ID may be assigned to each contact output. The signal that can **OPERATE** a contact output may be any FlexLogic™ operand (virtual output, element state, contact input, or virtual input). An additional FlexLogic™ operand may be used to **SEAL-IN** the relay. Any change of state of a contact output can be logged as an Event if programmed to do so.

For example, the trip circuit current is monitored by providing a current threshold detector in series with some Form-A contacts (see the trip circuit example in the *Digital Elements* section). The monitor will set a flag (see the specifications for Form-A). The name of the FlexLogic™ operand set by the monitor, consists of the output relay designation, followed by the name of the flag; e.g. 'Cont Op 1 IOn' or 'Cont Op 1 IOff'.

In most breaker control circuits, the trip coil is connected in series with a breaker auxiliary contact used to interrupt current flow after the breaker has tripped, to prevent damage to the less robust initiating contact. This can be done by monitoring an auxiliary contact on the breaker which opens when the breaker has tripped, but this scheme is subject to incorrect operation caused by differences in timing between breaker auxiliary contact change-of-state and interruption of current in the trip circuit. The most dependable protection of the initiating contact is provided by directly measuring current in the tripping circuit, and using this parameter to control resetting of the initiating relay. This scheme is often called 'trip seal-in'.

This can be realized in the N60 using the 'Cont Op 1 IOn' FlexLogic™ operand to seal-in the contact output as follows:

CONTACT OUTPUT H1 ID: "Cont Op 1"
 OUTPUT H1 OPERATE: any suitable FlexLogic™ operand
 OUTPUT H1 SEAL-IN: "Cont Op 1 IOn"
 CONTACT OUTPUT H1 EVENTS: "Enabled"

b) LATCHING OUTPUTS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ CONTACT OUTPUTS ⇒ CONTACT OUTPUT H1a

■ CONTACT OUTPUT H1a	◀▶	OUTPUT H1a ID L-Cont Op 1	Range: Up to 12 alphanumeric characters
MESSAGE	▲▼	OUTPUT H1a OPERATE: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	OUTPUT H1a RESET: Off	Range: FlexLogic™ operand
MESSAGE	▲▼	OUTPUT H1a TYPE: Operate-dominant	Range: Operate-dominant, Reset-dominant
MESSAGE	▲	OUTPUT H1a EVENTS: Disabled	Range: Disabled, Enabled

The N60 latching output contacts are mechanically bi-stable and controlled by two separate (open and close) coils. As such they retain their position even if the relay is not powered up. The relay recognizes all latching output contact cards and populates the setting menu accordingly. On power up, the relay reads positions of the latching contacts from the hardware before executing any other functions of the relay (such as protection and control features or FlexLogic™).

The latching output modules, either as a part of the relay or as individual modules, are shipped from the factory with all latching contacts opened. It is highly recommended to double-check the programming and positions of the latching contacts when replacing a module.

Since the relay asserts the output contact and reads back its position, it is possible to incorporate self-monitoring capabilities for the latching outputs. If any latching outputs exhibits a discrepancy, the **LATCHING OUTPUT ERROR** self-test error is declared. The error is signaled by the LATCHING OUT ERROR FlexLogic™ operand, event, and target message.

- **OUTPUT H1a OPERATE:** This setting specifies a FlexLogic™ operand to operate the 'close coil' of the contact. The relay will seal-in this input to safely close the contact. Once the contact is closed and the **RESET** input is logic 0 (off), any activity of the **OPERATE** input, such as subsequent chattering, will not have any effect. With both the **OPERATE** and **RESET** inputs active (logic 1), the response of the latching contact is specified by the **OUTPUT H1a TYPE** setting.
- **OUTPUT H1a RESET:** This setting specifies a FlexLogic™ operand to operate the 'trip coil' of the contact. The relay will seal-in this input to safely open the contact. Once the contact is opened and the **OPERATE** input is logic 0 (off), any activity of the **RESET** input, such as subsequent chattering, will not have any effect. With both the **OPERATE** and **RESET** inputs active (logic 1), the response of the latching contact is specified by the **OUTPUT H1a TYPE** setting.
- **OUTPUT H1a TYPE:** This setting specifies the contact response under conflicting control inputs; that is, when both the **OPERATE** and **RESET** signals are applied. With both control inputs applied simultaneously, the contact will close if set to "Operate-dominant" and will open if set to "Reset-dominant".

Application Example 1:

A latching output contact H1a is to be controlled from two user-programmable pushbuttons (buttons number 1 and 2). The following settings should be applied.

Program the Latching Outputs by making the following changes in the **SETTINGS** ⇒ **INPUTS/OUTPUTS** ⇒ **CONTACT OUTPUTS** ⇒ **CONTACT OUTPUT H1a** menu (assuming an H4L module):

OUTPUT H1a OPERATE: "PUSHBUTTON 1 ON"
OUTPUT H1a RESET: "PUSHBUTTON 2 ON"

Program the pushbuttons by making the following changes in the **PRODUCT SETUP** ⇒ **USER-PROGRAMMABLE PUSHBUTTONS** ⇒ **USER PUSHBUTTON 1** and **USER PUSHBUTTON 2** menus:

PUSHBUTTON 1 FUNCTION: "Self-reset" **PUSHBUTTON 2 FUNCTION:** "Self-reset"
PUSHBTN 1 DROP-OUT TIME: "0.00 s" **PUSHBTN 2 DROP-OUT TIME:** "0.00 s"

Application Example 2:

A relay, having two latching contacts H1a and H1c, is to be programmed. The H1a contact is to be a Type-a contact, while the H1c contact is to be a Type-b contact (Type-a means closed after exercising the operate input; Type-b means closed after exercising the reset input). The relay is to be controlled from virtual outputs: VO1 to operate and VO2 to reset.

Program the Latching Outputs by making the following changes in the **SETTINGS** ⇒ **INPUTS/OUTPUTS** ⇒ **CONTACT OUTPUTS** ⇒ **CONTACT OUTPUT H1a** and **CONTACT OUTPUT H1c** menus (assuming an H4L module):

OUTPUT H1a OPERATE: "VO1" **OUTPUT H1c OPERATE:** "VO2"
OUTPUT H1a RESET: "VO2" **OUTPUT H1c RESET:** "VO1"

Since the two physical contacts in this example are mechanically separated and have individual control inputs, they will not operate at exactly the same time. A discrepancy in the range of a fraction of a maximum operating time may occur. Therefore, a pair of contacts programmed to be a multi-contact relay will not guarantee any specific sequence of operation (such as make before break). If required, the sequence of operation must be programmed explicitly by delaying some of the control inputs as shown in the next application example.

Application Example 3:

A make before break functionality must be added to the preceding example. An overlap of 20 ms is required to implement this functionality as described below:

Write the following FlexLogic™ equation (EnerVista UR Setup example shown):

FLEXLOGIC ENTRY	TYPE	SYNTAX
View Graphic	View	View
FlexLogic Entry 1	Read Virtual Outputs On	Virt Op 1 On (VO1)
FlexLogic Entry 2	TIMER	Timer 1
FlexLogic Entry 3	Write Virtual Output[Assign]	= Virt Op 3 (VO3)
FlexLogic Entry 4	Read Virtual Outputs On	Virt Op 2 On (VO2)
FlexLogic Entry 5	TIMER	Timer 2
FlexLogic Entry 6	Write Virtual Output[Assign]	= Virt Op 4 (VO4)
FlexLogic Entry 7	End of List	

Both timers (Timer 1 and Timer 2) should be set to 20 ms pickup and 0 ms dropout.

Program the Latching Outputs by making the following changes in the **SETTINGS** ⇒ **INPUTS/OUTPUTS** ⇒ **CONTACT OUTPUTS** ⇒ **CONTACT OUTPUT H1a** and **CONTACT OUTPUT H1c** menus (assuming an H4L module):

OUTPUT H1a OPERATE: "VO1"

OUTPUT H1a RESET: "VO4"

OUTPUT H1c OPERATE: "VO2"

OUTPUT H1c RESET: "VO3"

Application Example 4:

A latching contact H1a is to be controlled from a single virtual output VO1. The contact should stay closed as long as VO1 is high, and should stay opened when VO1 is low. Program the relay as follows.

Write the following FlexLogic™ equation (EnerVista UR Setup example shown):

FLEXLOGIC ENTRY	TYPE	SYNTAX
View Graphic	View	View
FlexLogic Entry 1	Read Virtual Outputs On	Virt Op 1 On (VO1)
FlexLogic Entry 2	NOT	1 Input
FlexLogic Entry 3	Write Virtual Output[Assign]	= Virt Op 2 (VO2)
FlexLogic Entry 4	End of List	

Program the Latching Outputs by making the following changes in the **SETTINGS** ⇒ **INPUTS/OUTPUTS** ⇒ **CONTACT OUTPUTS** ⇒ **CONTACT OUTPUT H1a** menu (assuming an H4L module):

OUTPUT H1a OPERATE: "VO1"

OUTPUT H1a RESET: "VO2"

5.7.4 VIRTUAL OUTPUTS

PATH: **SETTINGS** ⇒ **INPUTS/OUTPUTS** ⇒ **VIRTUAL OUTPUTS** ⇒ **VIRTUAL OUTPUT 1(96)**

VIRTUAL OUTPUT 1	VIRTUAL OUTPUT 1 ID Virt Op 1	Range: Up to 12 alphanumeric characters
MESSAGE	VIRTUAL OUTPUT 1 EVENTS: Disabled	Range: Disabled, Enabled

There are 96 virtual outputs that may be assigned via FlexLogic™. If not assigned, the output will be forced to 'OFF' (Logic 0). An ID may be assigned to each virtual output. Virtual outputs are resolved in each pass through the evaluation of the FlexLogic™ equations. Any change of state of a virtual output can be logged as an event if programmed to do so.

For example, if Virtual Output 1 is the trip signal from FlexLogic™ and the trip relay is used to signal events, the settings would be programmed as follows:

VIRTUAL OUTPUT 1 ID: "Trip"

VIRTUAL OUTPUT 1 EVENTS: "Disabled"

5.7.5 REMOTE DEVICES

a) REMOTE INPUTS/OUTPUTS OVERVIEW

Remote inputs and outputs provide a means of exchanging digital state information between Ethernet-networked devices. The IEC 61850 GSSE (Generic Substation State Event) and GOOSE (Generic Object Oriented Substation Event) standards are used.



The IEC 61850 specification requires that communications between devices be implemented on Ethernet. For UR-series relays, Ethernet communications is provided only all CPU modules except type 9E.

The sharing of digital point state information between GSSE/GOOSE equipped relays is essentially an extension to Flex-Logic™, allowing distributed FlexLogic™ by making operands available to/from devices on a common communications network. In addition to digital point states, GSSE/GOOSE messages identify the originator of the message and provide other information required by the communication specification. All devices listen to network messages and capture data only from messages that have originated in selected devices.

IEC 61850 GSSE messages are compatible with UCA GOOSE messages and contain a fixed set of digital points. IEC 61850 GOOSE messages can, in general, contain any configurable data items. When used by the remote input/output feature, IEC 61850 GOOSE messages contain the same data as GSSE messages.

Both GSSE and GOOSE messages are designed to be short, reliable, and high priority. GOOSE messages have additional advantages over GSSE messages due to their support of VLAN (virtual LAN) and Ethernet priority tagging functionality. The GSSE message structure contains space for 128 bit pairs representing digital point state information. The IEC 61850 specification provides 32 “DNA” bit pairs that represent the state of two pre-defined events and 30 user-defined events. All remaining bit pairs are “UserSt” bit pairs, which are status bits representing user-definable events. The N60 implementation provides 32 of the 96 available UserSt bit pairs.

The IEC 61850 specification includes features that are used to cope with the loss of communication between transmitting and receiving devices. Each transmitting device will send a GSSE/GOOSE message upon a successful power-up, when the state of any included point changes, or after a specified interval (the *default update* time) if a change-of-state has not occurred. The transmitting device also sends a ‘hold time’ which is set greater than three times the programmed default time required by the receiving device.

Receiving devices are constantly monitoring the communications network for messages they require, as recognized by the identification of the originating device carried in the message. Messages received from remote devices include the message *time allowed to live*. The receiving relay sets a timer assigned to the originating device to this time interval, and if it has not received another message from this device at time-out, the remote device is declared to be non-communicating, so it will use the programmed default state for all points from that specific remote device. If a message is received from a remote device before the *time allowed to live* expires, all points for that device are updated to the states contained in the message and the hold timer is restarted. The status of a remote device, where “Offline” indicates non-communicating, can be displayed.

The remote input/output facility provides for 64 remote inputs and 64 remote outputs.

b) LOCAL DEVICES: ID OF DEVICE FOR TRANSMITTING GSSE MESSAGES

In a N60 relay, the device ID that identifies the originator of the message is programmed in the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **INSTALLATION** ⇒ **RELAY NAME** setting.

c) REMOTE DEVICES: ID OF DEVICE FOR RECEIVING GSSE MESSAGES

PATH: SETTINGS ⇒ **INPUTS/OUTPUTS** ⇒ **REMOTE DEVICES** ⇒ **REMOTE DEVICE 1(16)**

<div> <div>■ REMOTE DEVICE 1</div> <div>■</div> </div>	<div> <div>◀▶</div> <div> <div>REMOTE DEVICE 1 ID:</div> <div>Remote Device 1</div> </div> </div>	Range: up to 20 alphanumeric characters
MESSAGE	<div> <div>▲▼</div> <div> <div>REMOTE DEVICE 1</div> <div>VLAN ID: 0</div> </div> </div>	Range: 0 to 4095 in steps of 1
MESSAGE	<div> <div>▲</div> <div> <div>REMOTE DEVICE 1</div> <div>ETYPE APPID: 0</div> </div> </div>	Range: 0 to 16383 in steps of 1

Sixteen remote devices, numbered from 1 to 16, can be selected for setting purposes. A receiving relay must be programmed to capture messages from only those originating remote devices of interest. This setting is used to select specific remote devices by entering (bottom row) the exact identification (ID) assigned to those devices.

The **REMOTE DEVICE 1(16) VLAN ID** and **REMOTE DEVICE 1(16) ETYPE APPID** settings are only used with GOOSE messages; they are not applicable to GSSE messages. The **REMOTE DEVICE 1(16) VLAN ID** setting identifies the virtual LAN on which the remote device is sending the GOOSE message. The **REMOTE DEVICE 1(16) ETYPE APPID** setting identifies the Ethernet application identification in the GOOSE message. These settings should match the corresponding settings on the sending device.

5.7.6 REMOTE INPUTS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ REMOTE INPUTS ⇒ REMOTE INPUT 1(64)

<div> <div>■ REMOTE INPUT 1</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div> </div>	<div>◀▶</div>	<div>REMOTE INPUT 1 ID:</div> <div>Remote Ip 1</div>	Range: up to 12 alphanumeric characters
	<div>▲▼</div>	<div>REMOTE IN 1 DEVICE:</div> <div>Remote Device 1</div>	Range: 1 to 16 inclusive
	<div>▲▼</div>	<div>REMOTE IN 1 BIT PAIR:</div> <div>None</div>	Range: None, DNA-1 to DNA-32, UserSt-1 to UserSt-32
	<div>▲▼</div>	<div>REMOTE IN 1 DEFAULT STATE:</div> <div>Off</div>	Range: On, Off, Latest/On, Latest/Off
	<div>▲</div>	<div>REMOTE IN 1 EVENTS:</div> <div>Disabled</div>	Range: Disabled, Enabled

Remote Inputs which create FlexLogic™ operands at the receiving relay, are extracted from GSSE/GOOSE messages originating in remote devices. The relay provides 64 remote inputs, each of which can be selected from a list consisting of 64 selections: DNA-1 through DNA-32 and UserSt-1 through UserSt-32. The function of DNA inputs is defined in the IEC 61850 specification and is presented in the IEC 61850 DNA Assignments table in the *Remote Outputs* section. The function of UserSt inputs is defined by the user selection of the FlexLogic™ operand whose state is represented in the GSSE/GOOSE message. A user must program a DNA point from the appropriate FlexLogic™ operand.

Remote Input 1 must be programmed to replicate the logic state of a specific signal from a specific remote device for local use. This programming is performed via the three settings shown above.

The **REMOTE INPUT 1 ID** setting allows the user to assign descriptive text to the remote input. The **REMOTE IN 1 DEVICE** setting selects the number (1 to 16) of the remote device which originates the required signal, as previously assigned to the remote device via the setting **REMOTE DEVICE *NN* ID** (see the *Remote Devices* section). **REMOTE IN 1 BIT PAIR** selects the specific bits of the GSSE/GOOSE message required.

The **REMOTE IN 1 DEFAULT STATE** setting selects the logic state for this point if the local relay has just completed startup or the remote device sending the point is declared to be non-communicating. The following choices are available:

- Setting **REMOTE IN 1 DEFAULT STATE** to “On” value defaults the input to Logic 1.
- Setting **REMOTE IN 1 DEFAULT STATE** to “Off” value defaults the input to Logic 0.
- Setting **REMOTE IN 1 DEFAULT STATE** to “Latest/On” freezes the input in case of lost communications. If the latest state is not known, such as after relay power-up but before the first communication exchange, the input will default to Logic 1. When communication resumes, the input becomes fully operational.
- Setting **REMOTE IN 1 DEFAULT STATE** to “Latest/Off” freezes the input in case of lost communications. If the latest state is not known, such as after relay power-up but before the first communication exchange, the input will default to Logic 0. When communication resumes, the input becomes fully operational.



For additional information on GSSE/GOOSE messaging, refer to the *Remote Devices* section in this chapter.

5.7.7 REMOTE OUTPUTS

a) DNA BIT PAIRS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ REMOTE OUTPUTS DNA BIT PAIRS ⇒ REMOTE OUPUTS DNA- 1(32) BIT PAIR

<input checked="" type="checkbox"/> REMOTE OUTPUTS <input checked="" type="checkbox"/> DNA- 1 BIT PAIR	<div>◀▶</div> <div>MESSAGE ▲</div>	DNA- 1 OPERAND: Off	Range: FlexLogic™ operand
		DNA- 1 EVENTS: Disabled	Range: Disabled, Enabled

Remote outputs (1 to 32) are FlexLogic™ operands inserted into GSSE/GOOSE messages that are transmitted to remote devices on a LAN. Each digital point in the message must be programmed to carry the state of a specific FlexLogic™ operand. The above operand setting represents a specific DNA function (as shown in the following table) to be transmitted.

Table 5–11: IEC 61850 DNA ASSIGNMENTS

DNA	IEC 61850 DEFINITION	FLEXLOGIC™ OPERAND
1	Test	IEC 61850 TEST MODE
2	ConfRev	IEC 61850 CONF REV

b) USERST BIT PAIRS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ REMOTE OUTPUTS UserSt BIT PAIRS ⇒ REMOTE OUTPUTS UserSt- 1(32) BIT PAIR

<input checked="" type="checkbox"/> REMOTE OUTPUTS <input checked="" type="checkbox"/> UserSt- 1 BIT PAIR	<div>◀▶</div> <div>MESSAGE ▲</div>	UserSt- 1 OPERAND: Off	Range: FlexLogic™ operand
		UserSt- 1 EVENTS: Disabled	Range: Disabled, Enabled

Remote outputs 1 to 32 originate as GSSE/GOOSE messages to be transmitted to remote devices. Each digital point in the message must be programmed to carry the state of a specific FlexLogic™ operand. The setting above is used to select the operand which represents a specific UserSt function (as selected by the user) to be transmitted.

The following setting represents the time between sending GSSE/GOOSE messages when there has been no change of state of any selected digital point. This setting is located in the **PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ IEC 61850 PROTOCOL ⇒ GSSE/GOOSE CONFIGURATION** settings menu.

DEFAULT GSSE/GOOSE UPDATE TIME: 60 s	Range: 1 to 60 s in steps of 1
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The following setting determines whether remote input/output data is transported using IEC 61850 GSSE or IEC 61850 GOOSE messages. If GOOSE is selected, the VLAN and APPID settings should be set accordingly. If GSSE is selected, the VLAN and APPID settings are not relevant. This setting is located in the **PRODUCT SETUP ⇒ COMMUNICATIONS ⇒ IEC 61850 PROTOCOL ⇒ GSSE/GOOSE CONFIGURATION** menu.

REMOTE I/O TRANSFER METHOD: GSSE	Range: GOOSE, GSSE, None
-------------------------------------	--------------------------



For more information on GSSE/GOOSE messaging, refer to *Remote Inputs/Outputs Overview* in the *Remote Devices* section.

5.7.8 RESETTING

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ RESETTING

<div>■ RESETTING</div> <div>■</div>	◀▶	<div>RESET OPERAND:</div> <div>Off</div>	Range: FlexLogic™ operand
-------------------------------------	----	--	---------------------------

Some events can be programmed to latch the faceplate LED event indicators and the target message on the display. Once set, the latching mechanism will hold all of the latched indicators or messages in the set state after the initiating condition has cleared until a RESET command is received to return these latches (not including FlexLogic™ latches) to the reset state. The RESET command can be sent from the faceplate Reset button, a remote device via a communications channel, or any programmed operand.

When the RESET command is received by the relay, two FlexLogic™ operands are created. These operands, which are stored as events, reset the latches if the initiating condition has cleared. The three sources of RESET commands each create the RESET OP FlexLogic™ operand. Each individual source of a RESET command also creates its individual operand RESET OP (PUSHBUTTON), RESET OP (COMMS) or RESET OP (OPERAND) to identify the source of the command. The setting shown above selects the operand that will create the RESET OP (OPERAND) operand.

5.7.9 DIRECT INPUTS/OUTPUTS

a) DIRECT INPUTS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ DIRECT INPUTS ⇒ DIRECT INPUT 1(64)

<div>■ DIRECT INPUT 1</div> <div>■</div>	◀▶	<div>DIRECT INPUT 1</div> <div>NAME: Dir Ip 1</div>	Range: up to 12 alphanumeric characters
MESSAGE	▲▼	<div>DIRECT INPUT 1</div> <div>DEVICE ID: 1</div>	Range: 1 to 16
MESSAGE	▲▼	<div>DIRECT INPUT 1</div> <div>BIT NUMBER: 1</div>	Range: 1 to 64
MESSAGE	▲▼	<div>DIRECT INPUT 1</div> <div>DEFAULT STATE: Off</div>	Range: On, Off, Latest/On, Latest/Off
MESSAGE	▲	<div>DIRECT INPUT 1</div> <div>EVENTS: Disabled</div>	Range: Enabled, Disabled

These settings specify how the direct input information is processed. The **DIRECT INPUT 1 NAME** setting allows the user to assign a descriptive name to the direct input. The **DIRECT INPUT 1 DEVICE ID** represents the source of this direct input. The specified direct input is driven by the device identified here.

The **DIRECT INPUT 1 BIT NUMBER** is the bit number to extract the state for this direct input. Direct Input x is driven by the bit identified here as **DIRECT INPUT 1 BIT NUMBER**. This corresponds to the direct output number of the sending device.

The **DIRECT INPUT 1 DEFAULT STATE** represents the state of the direct input when the associated direct device is offline. The following choices are available:

- Setting **DIRECT INPUT 1 DEFAULT STATE** to “On” value defaults the input to Logic 1.
- Setting **DIRECT INPUT 1 DEFAULT STATE** to “Off” value defaults the input to Logic 0.
- Setting **DIRECT INPUT 1 DEFAULT STATE** to “Latest/On” freezes the input in case of lost communications. If the latest state is not known, such as after relay power-up but before the first communication exchange, the input will default to Logic 1. When communication resumes, the input becomes fully operational.
- Setting **DIRECT INPUT 1 DEFAULT STATE** to “Latest/Off” freezes the input in case of lost communications. If the latest state is not known, such as after relay power-up but before the first communication exchange, the input will default to Logic 0. When communication resumes, the input becomes fully operational.

b) DIRECT OUTPUTS

PATH: SETTINGS ⇒ INPUTS/OUTPUTS ⇒ DIRECT OUTPUTS ⇒ DIRECT OUTPUT 1(64)

<div> <div>■</div> <div>DIRECT OUTPUT 1</div> </div> <div>MESSAGE</div> <div>MESSAGE</div>	<div> <div>◀▶</div> <div>DIRECT OUT 1 NAME: Dir Out 1</div> </div> <div> <div>▲▼</div> <div>DIRECT OUT 1 OPERAND: Off</div> </div> <div> <div>▲</div> <div>DIRECT OUTPUT 1 EVENTS: Disabled</div> </div>	<div>Range: up to 12 alphanumeric characters</div> <div>Range: FlexLogic™ operand</div> <div>Range: Enabled, Disabled</div>
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The **DIRECT OUT 1 NAME** setting allows the user to assign a descriptive name to the direct output. The **DIR OUT 1 OPERAND** is the FlexLogic™ operand that determines the state of this direct output.

c) APPLICATION EXAMPLES

The examples introduced in the earlier *Direct Inputs/Outputs* section (part of the *Product Setup* section) direct inputs/outputs are continued below to illustrate usage of the direct inputs and outputs.

EXAMPLE 1: EXTENDING INPUT/OUTPUT CAPABILITIES OF A N60 RELAY

Consider an application that requires additional quantities of digital inputs and/or output contacts and/or lines of programmable logic that exceed the capabilities of a single UR-series chassis. The problem is solved by adding an extra UR-series IED, such as the C30, to satisfy the additional inputs/outputs and programmable logic requirements. The two IEDs are connected via single-channel digital communication cards as shown below.

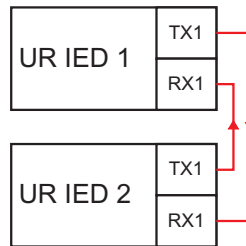


Figure 5–71: INPUT/OUTPUT EXTENSION VIA DIRECT INPUTS/OUTPUTS

Assume Contact Input 1 from UR IED 2 is to be used by UR IED 1. The following settings should be applied (Direct Input 5 and bit number 12 are used, as an example):

UR IED 1: **DIRECT INPUT 5 DEVICE ID** = "2"
DIRECT INPUT 5 BIT NUMBER = "12"

UR IED 2: **DIRECT OUT 12 OPERAND** = "Cont Ip 1 On"

The Cont Ip 1 On operand of UR IED 2 is now available in UR IED 1 as DIRECT INPUT 5 ON.

EXAMPLE 2: INTERLOCKING BUSBAR PROTECTION

A simple interlocking busbar protection scheme can be accomplished by sending a blocking signal from downstream devices, say 2, 3 and 4, to the upstream device that monitors a single incomer of the busbar, as shown in the figure below.

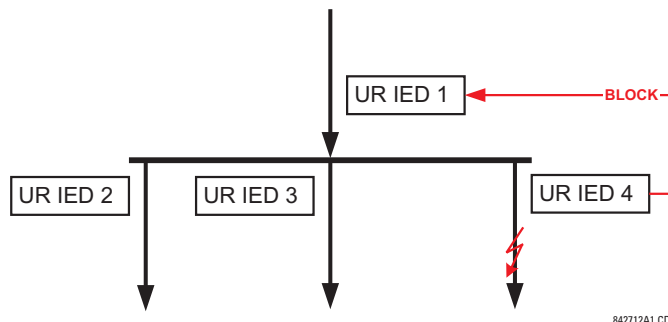


Figure 5–72: SAMPLE INTERLOCKING BUSBAR PROTECTION SCHEME

Assume that Phase Instantaneous Overcurrent 1 is used by Devices 2, 3, and 4 to block Device 1. If not blocked, Device 1 would trip the bus upon detecting a fault and applying a short coordination time delay.

The following settings should be applied (assume Bit 3 is used by all 3 devices to send the blocking signal and Direct Inputs 7, 8, and 9 are used by the receiving device to monitor the three blocking signals):

UR IED 2: **DIRECT OUT 3 OPERAND:** "PHASE IOC1 OP"

UR IED 3: **DIRECT OUT 3 OPERAND:** "PHASE IOC1 OP"

UR IED 4: **DIRECT OUT 3 OPERAND:** "PHASE IOC1 OP"

UR IED 1: **DIRECT INPUT 7 DEVICE ID:** "2"

DIRECT INPUT 7 BIT NUMBER: "3"

DIRECT INPUT 7 DEFAULT STATE: select "On" for security, select "Off" for dependability

DIRECT INPUT 8 DEVICE ID: "3"

DIRECT INPUT 8 BIT NUMBER: "3"

DIRECT INPUT 8 DEFAULT STATE: select "On" for security, select "Off" for dependability

DIRECT INPUT 9 DEVICE ID: "4"

DIRECT INPUT 9 BIT NUMBER: "3"

DIRECT INPUT 9 DEFAULT STATE: select "On" for security, select "Off" for dependability

Now the three blocking signals are available in UR IED 1 as DIRECT INPUT 7 ON, DIRECT INPUT 8 ON, and DIRECT INPUT 9 ON. Upon losing communications or a device, the scheme is inclined to block (if any default state is set to "On"), or to trip the bus on any overcurrent condition (all default states set to "Off").

EXAMPLE 2: PILOT-AIDED SCHEMES

Consider a three-terminal line protection application shown in the figure below.

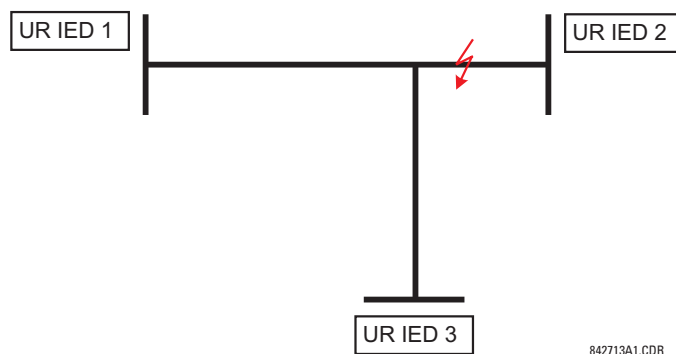


Figure 5-73: THREE-TERMINAL LINE APPLICATION

Assume the Hybrid Permissive Overreaching Transfer Trip (Hybrid POTT) scheme is applied using the architecture shown below. The scheme output operand HYB POTT TX1 is used to key the permission.

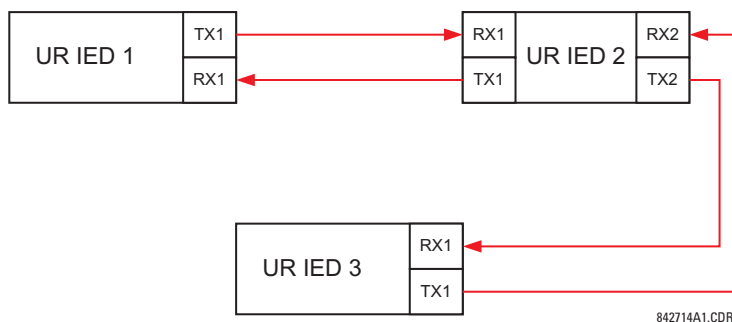


Figure 5-74: SINGLE-CHANNEL OPEN-LOOP CONFIGURATION

In the above architecture, Devices 1 and 3 do not communicate directly. Therefore, Device 2 must act as a 'bridge'. The following settings should be applied:

- UR IED 1: **DIRECT OUT 2 OPERAND:** "HYB POTT TX1"
DIRECT INPUT 5 DEVICE ID: "2"
DIRECT INPUT 5 BIT NUMBER: "2" (this is a message from IED 2)
DIRECT INPUT 6 DEVICE ID: "2"
DIRECT INPUT 6 BIT NUMBER: "4" (effectively, this is a message from IED 3)
- UR IED 3: **DIRECT OUT 2 OPERAND:** "HYB POTT TX1"
DIRECT INPUT 5 DEVICE ID: "2"
DIRECT INPUT 5 BIT NUMBER: "2" (this is a message from IED 2)
DIRECT INPUT 6 DEVICE ID: "2"
DIRECT INPUT 6 BIT NUMBER: "3" (effectively, this is a message from IED 1)
- UR IED 2: **DIRECT INPUT 5 DEVICE ID:** "1"
DIRECT INPUT 5 BIT NUMBER: "2"
DIRECT INPUT 6 DEVICE ID: "3"
DIRECT INPUT 6 BIT NUMBER: "2"
DIRECT OUT 2 OPERAND: "HYB POTT TX1"
DIRECT OUT 3 OPERAND: "DIRECT INPUT 5" (forward a message from 1 to 3)
DIRECT OUT 4 OPERAND: "DIRECT INPUT 6" (forward a message from 3 to 1)

Signal flow between the three IEDs is shown in the figure below:

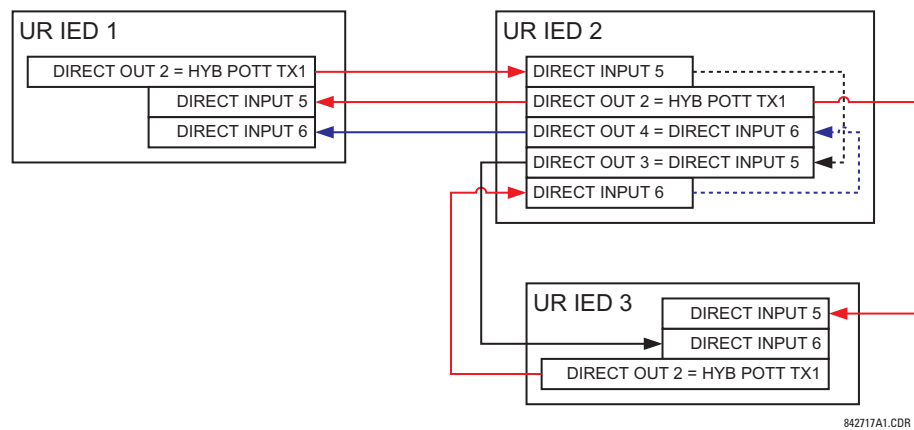


Figure 5–75: SIGNAL FLOW FOR DIRECT INPUT/OUTPUT EXAMPLE 3

In three-terminal applications, both the remote terminals must grant permission to trip. Therefore, at each terminal, Direct Inputs 5 and 6 should be ANDed in FlexLogic™ and the resulting operand configured as the permission to trip (**HYB POTT RX1** setting).

5.8.1 DCMA INPUTS

PATH: SETTINGS ⇒ TRANSDUCER I/O ⇒ DCMA INPUTS ⇒ DCMA INPUT F1(W8)

■ DCMA INPUT F1	◀▶	DCMA INPUT F1 FUNCTION: Disabled	Range: Disabled, Enabled
MESSAGE	▲▼	DCMA INPUT F1 ID: DCMA Ip 1	Range: up to 20 alphanumeric characters
MESSAGE	▲▼	DCMA INPUT F1 UNITS: μ A	Range: 6 alphanumeric characters
MESSAGE	▲▼	DCMA INPUT F1 RANGE: 0 to -1 mA	Range: 0 to -1 mA, 0 to +1 mA, -1 to +1 mA, 0 to 5 mA, 0 to 10mA, 0 to 20 mA, 4 to 20 mA
MESSAGE	▲▼	DCMA INPUT F1 MIN VALUE: 0.000	Range: -9999.999 to +9999.999 in steps of 0.001
MESSAGE	▲▼	DCMA INPUT F1 MAX VALUE: 0.000	Range: -9999.999 to +9999.999 in steps of 0.001

Hardware and software is provided to receive signals from external transducers and convert these signals into a digital format for use as required. The relay will accept inputs in the range of -1 to +20 mA DC, suitable for use with most common transducer output ranges; all inputs are assumed to be linear over the complete range. Specific hardware details are contained in Chapter 3.

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Before the dcma input signal can be used, the value of the signal measured by the relay must be converted to the range and quantity of the external transducer primary input parameter, such as DC voltage or temperature. The relay simplifies this process by internally scaling the output from the external transducer and displaying the actual primary parameter.

dcma input channels are arranged in a manner similar to CT and VT channels. The user configures individual channels with the settings shown here.

The channels are arranged in sub-modules of two channels, numbered from 1 through 8 from top to bottom. On power-up, the relay will automatically generate configuration settings for every channel, based on the order code, in the same general manner that is used for CTs and VTs. Each channel is assigned a slot letter followed by the row number, 1 through 8 inclusive, which is used as the channel number. The relay generates an actual value for each available input channel.

Settings are automatically generated for every channel available in the specific relay as shown above for the first channel of a type 5F transducer module installed in slot F.

The function of the channel may be either "Enabled" or "Disabled". If "Disabled", no actual values are created for the channel. An alphanumeric "ID" is assigned to each channel; this ID will be included in the channel actual value, along with the programmed units associated with the parameter measured by the transducer, such as volts, °C, megawatts, etc. This ID is also used to reference the channel as the input parameter to features designed to measure this type of parameter. The **DCMA INPUT F1 RANGE** setting specifies the mA DC range of the transducer connected to the input channel.

The **DCMA INPUT F1 MIN VALUE** and **DCMA INPUT F1 MAX VALUE** settings are used to program the span of the transducer in primary units. For example, a temperature transducer might have a span from 0 to 250°C; in this case the **DCMA INPUT F1 MIN VALUE** value is "0" and the **DCMA INPUT F1 MAX VALUE** value is "250". Another example would be a watts transducer with a span from -20 to +180 MW; in this case the **DCMA INPUT F1 MIN VALUE** value would be "-20" and the **DCMA INPUT F1 MAX VALUE** value "180". Intermediate values between the min and max values are scaled linearly.

5.8.2 RTD INPUTS

PATH: SETTINGS ⇒ TRANSDUCER I/O ⇒ RTD INPUTS ⇒ RTD INPUT F1(W8)

<div>■ RTD INPUT F1</div> <div>MESSAGE</div> <div>MESSAGE</div>	<div>◀▶</div> <div>▲▼</div> <div>▲</div>	RTD INPUT F1 FUNCTION: Disabled	Range: Disabled, Enabled
		RTD INPUT F1 ID: RTD Ip 1	Range: Up to 20 alphanumeric characters
		RTD INPUT F1 TYPE: 100Ω Nickel	Range: 100Ω Nickel, 10Ω Copper, 100Ω Platinum, 120Ω Nickel

Hardware and software is provided to receive signals from external resistance temperature detectors and convert these signals into a digital format for use as required. These channels are intended to be connected to any of the RTD types in common use. Specific hardware details are contained in Chapter 3.

RTD input channels are arranged in a manner similar to CT and VT channels. The user configures individual channels with the settings shown here.

The channels are arranged in sub-modules of two channels, numbered from 1 through 8 from top to bottom. On power-up, the relay will automatically generate configuration settings for every channel, based on the order code, in the same general manner that is used for CTs and VTs. Each channel is assigned a slot letter followed by the row number, 1 through 8 inclusive, which is used as the channel number. The relay generates an actual value for each available input channel.

Settings are automatically generated for every channel available in the specific relay as shown above for the first channel of a type 5C transducer module installed in slot F.

The function of the channel may be either “Enabled” or “Disabled”. If “Disabled”, there will not be an actual value created for the channel. An alphanumeric ID is assigned to the channel; this ID will be included in the channel actual values. It is also used to reference the channel as the input parameter to features designed to measure this type of parameter. Selecting the type of RTD connected to the channel configures the channel.

Actions based on RTD overtemperature, such as trips or alarms, are done in conjunction with the FlexElements™ feature. In FlexElements™, the operate level is scaled to a base of 100°C. For example, a trip level of 150°C is achieved by setting the operate level at 1.5 pu. FlexElement™ operands are available to FlexLogic™ for further interlocking or to operate an output contact directly.

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5.8.3 DCMA OUTPUTS

PATH: SETTINGS ⇒ TRANSDUCER I/O ⇒ DCMA OUTPUTS ⇒ DCMA OUTPUT F1(W8)

<div>■ DCMA OUTPUT F1</div> <div>MESSAGE</div> <div>MESSAGE</div> <div>MESSAGE</div>	<div>◀▶</div> <div>▲▼</div> <div>▲</div> <div>▲</div>	DCMA OUTPUT F1 SOURCE: Off	Range: Off, any analog actual value parameter
		DCMA OUTPUT F1 RANGE: -1 to 1 mA	Range: -1 to 1 mA, 0 to 1 mA, 4 to 20 mA
		DCMA OUTPUT F1 MIN VAL: 0.000 pu	Range: -90.000 to 90.000 pu in steps of 0.001
		DCMA OUTPUT F1 MAX VAL: 1.000 pu	Range: -90.000 to 90.000 pu in steps of 0.001

Hardware and software is provided to generate dcmA signals that allow interfacing with external equipment. Specific hardware details are contained in Chapter 3. The dcmA output channels are arranged in a manner similar to transducer input or CT and VT channels. The user configures individual channels with the settings shown below.

The channels are arranged in sub-modules of two channels, numbered 1 through 8 from top to bottom. On power-up, the relay automatically generates configuration settings for every channel, based on the order code, in the same manner used for CTs and VTs. Each channel is assigned a slot letter followed by the row number, 1 through 8 inclusive, which is used as the channel number.

Both the output range and a signal driving a given output are user-programmable via the following settings menu (an example for channel M5 is shown).

The relay checks the driving signal (x in equations below) for the minimum and maximum limits, and subsequently re-scales so the limits defined as **MIN VAL** and **MAX VAL** match the output range of the hardware defined as **RANGE**. The following equation is applied:

$$I_{out} = \begin{cases} I_{min} & \text{if } x < \text{MIN VAL} \\ I_{max} & \text{if } x > \text{MAX VAL} \\ k(x - \text{MIN VAL}) + I_{min} & \text{otherwise} \end{cases} \quad (\text{EQ 5.18})$$

where: x is a driving signal specified by the **SOURCE** setting

I_{min} and I_{max} are defined by the **RANGE** setting

k is a scaling constant calculated as:

$$k = \frac{I_{max} - I_{min}}{\text{MAX VAL} - \text{MIN VAL}} \quad (\text{EQ 5.19})$$

The feature is intentionally inhibited if the **MAX VAL** and **MIN VAL** settings are entered incorrectly, e.g. when **MAX VAL** – **MIN VAL** < 0.1 pu. The resulting characteristic is illustrated in the following figure.

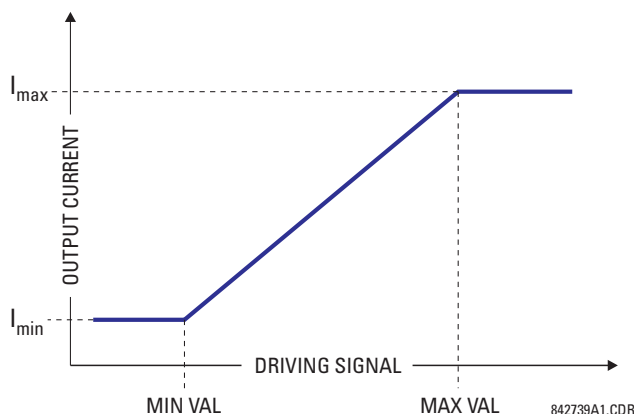


Figure 5-76: DCMA OUTPUT CHARACTERISTIC

The dcmA output settings are described below.

- **DCMA OUTPUT F1 SOURCE:** This setting specifies an internal analog value to drive the analog output. Actual values (FlexAnalog parameters) such as power, current amplitude, voltage amplitude, power factor, etc. can be configured as sources driving dcmA outputs. Refer to Appendix A for a complete list of FlexAnalog parameters.
- **DCMA OUTPUT F1 RANGE:** This setting allows selection of the output range. Each dcmA channel may be set independently to work with different ranges. The three most commonly used output ranges are available.
- **DCMA OUTPUT F1 MIN VAL:** This setting allows setting the minimum limit for the signal that drives the output. This setting is used to control the mapping between an internal analog value and the output current (see the following examples). The setting is entered in per-unit values. The base units are defined in the same manner as the FlexElement™ base units.
- **DCMA OUTPUT F1 MAX VAL:** This setting allows setting the maximum limit for the signal that drives the output. This setting is used to control the mapping between an internal analog value and the output current (see the following examples). The setting is entered in per-unit values. The base units are defined in the same manner as the FlexElement™ base units.



The **DCMA OUTPUT F1 MIN VAL** and **DCMA OUTPUT F1 MAX VAL** settings are ignored for power factor base units (i.e. if the **DCMA OUTPUT F1 SOURCE** is set to FlexAnalog value based on power factor measurement).

Three application examples are described below.

EXAMPLE 1:

A three phase active power on a 13.8 kV system measured via UR-series relay source 1 is to be monitored by the dcma H1 output of the range of –1 to 1 mA. The following settings are applied on the relay: CT ratio = 1200:5, VT secondary 115, VT connection is delta, and VT ratio = 120. The nominal current is 800 A primary and the nominal power factor is 0.90. The power is to be monitored in both importing and exporting directions and allow for 20% overload compared to the nominal.

The nominal three-phase power is:

$$P = \sqrt{3} \times 13.8 \text{ kV} \times 0.8 \text{ kA} \times 0.9 = 17.21 \text{ MW} \quad (\text{EQ 5.20})$$

The three-phase power with 20% overload margin is:

$$P_{max} = 1.2 \times 17.21 \text{ MW} = 20.65 \text{ MW} \quad (\text{EQ 5.21})$$

The base unit for power (refer to the FlexElements section in this chapter for additional details) is:

$$P_{BASE} = 115 \text{ V} \times 120 \times 1.2 \text{ kA} = 16.56 \text{ MW} \quad (\text{EQ 5.22})$$

The minimum and maximum power values to be monitored (in pu) are:

$$\text{minimum power} = \frac{-20.65 \text{ MW}}{16.56 \text{ MW}} = -1.247 \text{ pu}, \quad \text{maximum power} = \frac{20.65 \text{ MW}}{16.56 \text{ MW}} = 1.247 \text{ pu} \quad (\text{EQ 5.23})$$

The following settings should be entered:

DCMA OUTPUT H1 SOURCE: "SRC 1 P"
DCMA OUTPUT H1 RANGE: "–1 to 1 mA"
DCMA OUTPUT H1 MIN VAL: "–1.247 pu"
DCMA OUTPUT H1 MAX VAL: "1.247 pu"

With the above settings, the output will represent the power with the scale of 1 mA per 20.65 MW. The worst-case error for this application can be calculated by superimposing the following two sources of error:

- $\pm 0.5\%$ of the full scale for the analog output module, or $\pm 0.005 \times (1 - (-1)) \times 20.65 \text{ MW} = \pm 0.207 \text{ MW}$
- $\pm 1\%$ of reading error for the active power at power factor of 0.9

For example at the reading of 20 MW, the worst-case error is $0.01 \times 20 \text{ MW} + 0.207 \text{ MW} = 0.407 \text{ MW}$.

EXAMPLE 2:

The phase A current (true RMS value) is to be monitored via the H2 current output working with the range from 4 to 20 mA. The CT ratio is 5000:5 and the maximum load current is 4200 A. The current should be monitored from 0 A upwards, allowing for 50% overload.

The phase current with the 50% overload margin is:

$$I_{max} = 1.5 \times 4.2 \text{ kA} = 6.3 \text{ kA} \quad (\text{EQ 5.24})$$

The base unit for current (refer to the FlexElements section in this chapter for additional details) is:

$$I_{BASE} = 5 \text{ kA} \quad (\text{EQ 5.25})$$

The minimum and maximum power values to be monitored (in pu) are:

$$\text{minimum current} = \frac{0 \text{ kA}}{5 \text{ kA}} = 0 \text{ pu}, \quad \text{maximum current} = \frac{6.3 \text{ kA}}{5 \text{ kA}} = 1.26 \text{ pu} \quad (\text{EQ 5.26})$$

The following settings should be entered:

DCMA OUTPUT H2 SOURCE: "SRC 1 Ia RMS"
DCMA OUTPUT H2 RANGE: "4 to 20 mA"
DCMA OUTPUT H2 MIN VAL: "0.000 pu"
DCMA OUTPUT H2 MAX VAL: "1.260 pu"

The worst-case error for this application could be calculated by superimposing the following two sources of error:

- $\pm 0.5\%$ of the full scale for the analog output module, or $\pm 0.005 \times (20 - 4) \times 6.3 \text{ kA} = \pm 0.504 \text{ kA}$
- $\pm 0.25\%$ of reading or $\pm 0.1\%$ of rated (whichever is greater) for currents between 0.1 and 2.0 of nominal

For example, at the reading of 4.2 kA, the worst-case error is $\max(0.0025 \times 4.2 \text{ kA}, 0.001 \times 5 \text{ kA}) + 0.504 \text{ kA} = 0.515 \text{ kA}$.

EXAMPLE 3:

A positive-sequence voltage on a 400 kV system measured via Source 2 is to be monitored by the dcMA H3 output with a range of 0 to 1 mA. The VT secondary setting is 66.4 V, the VT ratio setting is 6024, and the VT connection setting is "Delta". The voltage should be monitored in the range from 70% to 110% of nominal.

The minimum and maximum positive-sequence voltages to be monitored are:

$$V_{min} = 0.7 \times \frac{400 \text{ kV}}{\sqrt{3}} = 161.66 \text{ kV}, \quad V_{max} = 1.1 \times \frac{400 \text{ kV}}{\sqrt{3}} = 254.03 \text{ kV} \quad (\text{EQ 5.27})$$

The base unit for voltage (refer to the FlexElements section in this chapter for additional details) is:

$$V_{BASE} = 0.0664 \text{ kV} \times 6024 = 400 \text{ kV} \quad (\text{EQ 5.28})$$

The minimum and maximum voltage values to be monitored (in pu) are:

$$\text{minimum voltage} = \frac{161.66 \text{ kV}}{400 \text{ kV}} = 0.404 \text{ pu}, \quad \text{maximum voltage} = \frac{254.03 \text{ kV}}{400 \text{ kV}} = 0.635 \text{ pu} \quad (\text{EQ 5.29})$$

The following settings should be entered:

DCMA OUTPUT H3 SOURCE: "SRC 2 V_1 mag"

DCMA OUTPUT H3 RANGE: "0 to 1 mA"

DCMA OUTPUT H3 MIN VAL: "0.404 pu"

DCMA OUTPUT H3 MAX VAL: "0.635 pu"

The limit settings differ from the expected 0.7 pu and 1.1 pu because the relay calculates the positive-sequence quantities scaled to the phase-to-ground voltages, even if the VTs are connected in "Delta" (refer to the *Metering Conventions* section in Chapter 6), while at the same time the VT nominal voltage is 1 pu for the settings. Consequently the settings required in this example differ from naturally expected by the factor of $\sqrt{3}$.

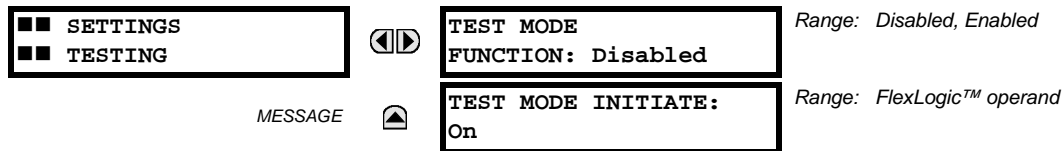
The worst-case error for this application could be calculated by superimposing the following two sources of error:

- $\pm 0.5\%$ of the full scale for the analog output module, or $\pm 0.005 \times (1 - 0) \times 254.03 \text{ kV} = \pm 1.27 \text{ kV}$
- $\pm 0.5\%$ of reading

For example, under nominal conditions, the positive-sequence reads 230.94 kV and the worst-case error is $0.005 \times 230.94 \text{ kV} + 1.27 \text{ kV} = 2.42 \text{ kV}$.

5.9.1 TEST MODE

PATH: SETTINGS ⇌ TESTING ⇌ TEST MODE



The relay provides test settings to verify that functionality using simulated conditions for contact inputs and outputs. The Test Mode is indicated on the relay faceplate by a flashing Test Mode LED indicator.

To initiate the Test mode, the **TEST MODE FUNCTION** setting must be “Enabled” and the **TEST MODE INITIATE** setting must be set to Logic 1. In particular:

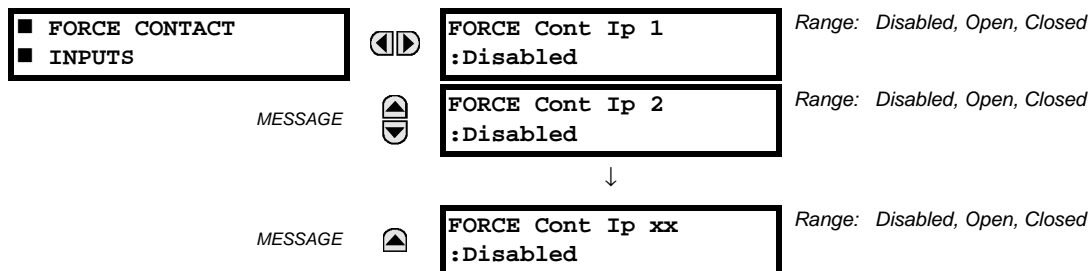
- To initiate Test Mode through relay settings, set **TEST MODE INITIATE** to “On”. The Test Mode starts when the **TEST MODE FUNCTION** setting is changed from “Disabled” to “Enabled”.
- To initiate Test Mode through a user-programmable condition, such as FlexLogic™ operand (pushbutton, digital input, communication-based input, or a combination of these), set **TEST MODE FUNCTION** to “Enabled” and set **TEST MODE INITIATE** to the desired operand. The Test Mode starts when the selected operand assumes a Logic 1 state.

When in Test Mode, the N60 remains fully operational, allowing for various testing procedures. In particular, the protection and control elements, FlexLogic™, and communication-based inputs and outputs function normally.

The only difference between the normal operation and the Test Mode is the behavior of the input and output contacts. The former can be forced to report as open or closed or remain fully operational; the latter can be forced to open, close, freeze, or remain fully operational. The response of the digital input and output contacts to the Test Mode is programmed individually for each input and output using the Force Contact Inputs and Force Contact Outputs test functions described in the following sections.

5.9.2 FORCE CONTACT INPUTS

PATH: SETTINGS ⇌ TESTING ⇌ FORCE CONTACT INPUTS



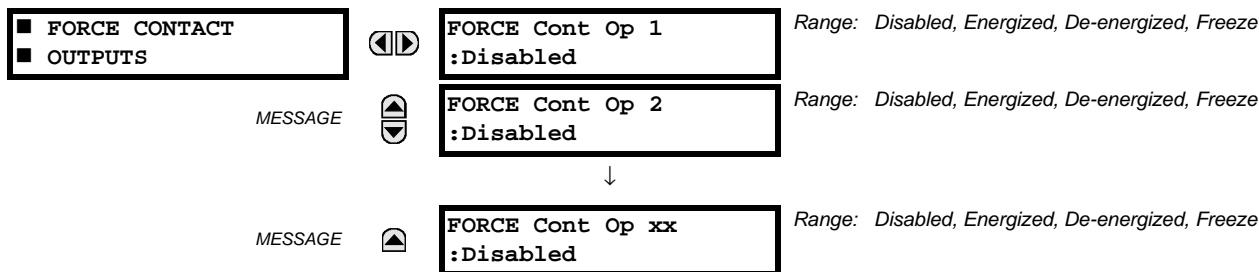
The relay digital inputs (contact inputs) could be pre-programmed to respond to the Test Mode in the following ways:

- If set to “Disabled”, the input remains fully operational. It is controlled by the voltage across its input terminals and can be turned on and off by external circuitry. This value should be selected if a given input must be operational during the test. This includes, for example, an input initiating the test, or being a part of a user pre-programmed test sequence.
- If set to “Open”, the input is forced to report as opened (Logic 0) for the entire duration of the Test Mode regardless of the voltage across the input terminals.
- If set to “Closed”, the input is forced to report as closed (Logic 1) for the entire duration of the Test Mode regardless of the voltage across the input terminals.

The Force Contact Inputs feature provides a method of performing checks on the function of all contact inputs. Once enabled, the relay is placed into Test Mode, allowing this feature to override the normal function of contact inputs. The Test Mode LED will be On, indicating that the relay is in Test Mode. The state of each contact input may be programmed as “Disabled”, “Open”, or “Closed”. All contact input operations return to normal when all settings for this feature are disabled.

5.9.3 FORCE CONTACT OUTPUTS

PATH: SETTINGS ⇒ TESTING ⇒ FORCE CONTACT OUTPUTS



The relay contact outputs can be pre-programmed to respond to the Test Mode.

If set to “Disabled”, the contact output remains fully operational. If operates when its control operand is Logic 1 and will resets when its control operand is Logic 0. If set to “Energize”, the output will close and remain closed for the entire duration of the Test Mode, regardless of the status of the operand configured to control the output contact. If set to “De-energize”, the output will open and remain opened for the entire duration of the Test Mode regardless of the status of the operand configured to control the output contact. If set to “Freeze”, the output retains its position from before entering the Test Mode, regardless of the status of the operand configured to control the output contact.

These settings are applied two ways. First, external circuits may be tested by energizing or de-energizing contacts. Second, by controlling the output contact state, relay logic may be tested and undesirable effects on external circuits avoided.

Example 1: Initiating a Test from User-Programmable Pushbutton 1

The Test Mode should be initiated from User-Programmable Pushbutton 1. The pushbutton will be programmed as “Latched” (pushbutton pressed to initiate the test, and pressed again to terminate the test). During the test, Digital Input 1 should remain operational, Digital Inputs 2 and 3 should open, and Digital Input 4 should close. Also, Contact Output 1 should freeze, Contact Output 2 should open, Contact Output 3 should close, and Contact Output 4 should remain fully operational. The required settings are shown below.

To enable User-Programmable Pushbutton 1 to initiate the Test mode, make the following changes in the **SETTINGS ⇒ TESTING ⇒ TEST MODE** menu:

TEST MODE FUNCTION: “Enabled” and **TEST MODE INITIATE:** “PUSHBUTTON 1 ON”

Make the following changes to configure the Contact I/Os. In the **SETTINGS ⇒ TESTING ⇒ FORCE CONTACT INPUTS** and **FORCE CONTACT OUTPUTS** menus, set:

FORCE Cont Ip 1: “Disabled”, **FORCE Cont Ip 2:** “Open”, **FORCE Cont Ip 3:** “Open”, and **FORCE Cont Ip 4:** “Closed”

FORCE Cont Op 1: “Freeze”, **FORCE Cont Op 2:** “De-energized”, **FORCE Cont Op 3:** “Open”, and **FORCE Cont Op 4:** “Disabled”

Example 2: Initiating a Test from User-Programmable Pushbutton 1 or through Remote Input 1

The Test should be initiated locally from User-Programmable Pushbutton 1 or remotely through Remote Input 1. Both the pushbutton and the remote input will be programmed as “Latched”. The required settings are shown below.

Write the following FlexLogic™ equation (EnerVista UR Setup example shown):





































FlexLogic Equation Editor // D60 with Pushbuttons.urs : C:\Pro...		
FLEXLOGIC ENTRY	TYPE	SYNTAX
View Graphic	View	View
FlexLogic Entry 1	Remote Inputs On	Remote I/P 1 ON
FlexLogic Entry 2	Protection Element	PUSHBUTTON 1 ON
FlexLogic Entry 3	OR	2 Input
FlexLogic Entry 4	Write Virtual Output[Assign]	= Virt Op 1 (VO1)
FlexLogic Entry 5	End of List	

Set the User Programmable Pushbutton as latching by changing **SETTINGS ⇒ PRODUCT SETUP ⇒ USER-PROGRAMMABLE PUSHBUTTONS ⇒ USER PUSHBUTTON 1 ⇒ PUSHBUTTON 1 FUNCTION** to “Latched”. To enable either Pushbutton 1 or Remote Input 1 to initiate the Test mode, make the following changes in the **SETTINGS ⇒ TESTING ⇒ TEST MODE** menu:

TEST MODE FUNCTION: “Enabled” and **TEST MODE INITIATE:** “VO1”

5.9.4 PHASE MEASUREMENT UNIT TEST VALUES

PATH: SETTINGS ⇒ TESTING ⇒ PMU TEST VALUES

■ PMU ■ TEST VALUES		PMU 1 TEST	Range: Enabled, Disabled
		FUNCTION: Disabled	
MESSAGE	 	PMU 1 VA TEST MAGNITUDE: 500.00 kV	Range: 0.00 to 700.00 kV in steps of 0.01
MESSAGE	 	PMU 1 VA TEST ANGLE: 0.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 VB TEST MAGNITUDE: 500.00 kV	Range: 0.00 to 700.00 kV in steps of 0.01
MESSAGE	 	PMU 1 VB TEST ANGLE: -120.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 VC TEST MAGNITUDE: 500.00 kV	Range: 0.00 to 700.00 kV in steps of 0.01
MESSAGE	 	PMU 1 VC TEST ANGLE: 120.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 VX TEST MAGNITUDE: 500.00 kV	Range: 0.00 to 700.00 kV in steps of 0.01
MESSAGE	 	PMU 1 VX TEST ANGLE: 0.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 IA TEST MAGNITUDE: 1.000 kA	Range: 0.000 to 9.999 kA in steps of 0.001
MESSAGE	 	PMU 1 IA TEST ANGLE: -10.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 IB TEST MAGNITUDE: 1.000 kA	Range: 0.000 to 9.999 kA in steps of 0.001
MESSAGE	 	PMU 1 IB TEST ANGLE: -130.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 IC TEST MAGNITUDE: 1.000 kA	Range: 0.000 to 9.999 kA in steps of 0.001
MESSAGE	 	PMU 1 IC TEST ANGLE: 110.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 IG TEST MAGNITUDE: 0.000 kA	Range: 0.000 to 9.999 kA in steps of 0.001
MESSAGE	 	PMU 1 IG TEST ANGLE: 0.00°	Range: -180.00 to 180.00° in steps of 0.05
MESSAGE	 	PMU 1 TEST FREQUENCY: 60.000 Hz	Range: 20.000 to 60.000 Hz in steps of 0.001
MESSAGE		PMU 1 TEST df/dt: 0.000 Hz/s	Range: -10.000 to 10.000 Hz/s in steps of 0.001

The relay must be in test mode to use the PMU test mode. That is, the **TESTING ⇒ TEST MODE FUNCTION** setting must be "Enabled" and the **TESTING ⇒ TEST MODE INITIATE** initiating signal must be "On".

During the PMU test mode, the physical channels (VA, VB, VC, VX, IA, IB, IC, and IG), frequency, and rate of change of frequency are substituted with user values, while the symmetrical components are calculated from the physical channels. The test values are not explicitly marked in the outgoing data frames. When required, it is recommended to use the user-programmable digital channels to signal the C37.118 client that test values are being sent in place of the real measurements.

6.1.1 ACTUAL VALUES MAIN MENU

<div> <div> <div>■ ■ ACTUAL VALUES</div> <div>■ ■ STATUS</div> </div> <div>▼</div> </div>	◀▶	<div> <div>■ CONTACT INPUTS</div> <div>■</div> </div>	See page 6-3.
	▲▼	<div> <div>■ VIRTUAL INPUTS</div> <div>■</div> </div>	See page 6-3.
	▲▼	<div> <div>■ REMOTE INPUTS</div> <div>■</div> </div>	See page 6-3.
	▲▼	<div> <div>■ CONTACT OUTPUTS</div> <div>■</div> </div>	See page 6-4.
	▲▼	<div> <div>■ VIRTUAL OUTPUTS</div> <div>■</div> </div>	See page 6-4.
	▲▼	<div> <div>■ REMOTE DEVICES</div> <div>■ STATUS</div> </div>	See page 6-4.
	▲▼	<div> <div>■ REMOTE DEVICES</div> <div>■ STATISTICS</div> </div>	See page 6-5.
	▲▼	<div> <div>■ DIGITAL COUNTERS</div> <div>■</div> </div>	See page 6-5.
	▲▼	<div> <div>■ SELECTOR SWITCHES</div> <div>■</div> </div>	See page 6-5.
	▲▼	<div> <div>■ FLEX STATES</div> <div>■</div> </div>	See page 6-5.
	▲▼	<div> <div>■ ETHERNET</div> <div>■</div> </div>	See page 6-6.
	▲▼	<div> <div>■ DIRECT INPUTS</div> <div>■</div> </div>	See page 6-6.
	▲	<div> <div>■ DIRECT DEVICES</div> <div>■ STATUS</div> </div>	See page 6-7.
<div> <div>▲</div> <div> <div>■ ■ ACTUAL VALUES</div> <div>■ ■ METERING</div> </div> <div>▼</div> </div>	◀▶	<div> <div>■ SOURCE SRC 1</div> <div>■</div> </div>	See page 6-11.
	▲▼	<div> <div>■ SOURCE SRC 2</div> <div>■</div> </div>	
	▲▼	<div> <div>■ SOURCE SRC 3</div> <div>■</div> </div>	
	▲▼	<div> <div>■ SOURCE SRC 4</div> <div>■</div> </div>	
	▲▼	<div> <div>■ SOURCE SRC 5</div> <div>■</div> </div>	
	▲▼	<div> <div>■ SOURCE SRC 6</div> <div>■</div> </div>	
	▲▼	<div> <div>■ SENSITIVE</div> <div>■ DIRECTIONAL POWER</div> </div>	See page 6-14.
	▲▼	<div> <div>■ SYNCHROCHECK</div> <div>■</div> </div>	See page 6-15.

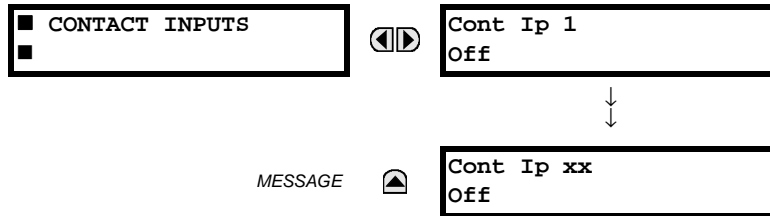
	▲▼	■ TRACKING FREQUENCY ■	See page 6-15.
	▲▼	■ FLEXELEMENTS ■	See page 6-15.
	▲▼	■ DIGITIZERS ■	See page 6-16.
	▲▼	■ 8-BIT COMPARATORS ■	See page 6-16.
	▲▼	■ PHASOR MEASUREMENT ■ UNIT	See page 6-16.
	▲▼	■ TRANSDUCER I/O ■ DCMA INPUTS	See page 6-17.
	▲	■ TRANSDUCER I/O ■ RTD INPUTS	See page 6-17.
▲		■ ACTUAL VALUES ■ RECORDS	
▼			
	◀▶	■ EVENT RECORDS ■	See page 6-18.
	▲▼	■ OSCILLOGRAPHY ■	See page 6-18.
	▲▼	■ DATA LOGGER ■	See page 6-18.
	▲▼	■ PMU ■ RECORDS	See page 6-19.
	▲	■ MAINTENANCE ■	See page 6-19.
6		■ ACTUAL VALUES ■ PRODUCT INFO	
	◀▶	■ MODEL INFORMATION ■	See page 6-20.
	▲	■ FIRMWARE REVISIONS ■	See page 6-20.



For status reporting, 'On' represents Logic 1 and 'Off' represents Logic 0.

6.2.1 CONTACT INPUTS

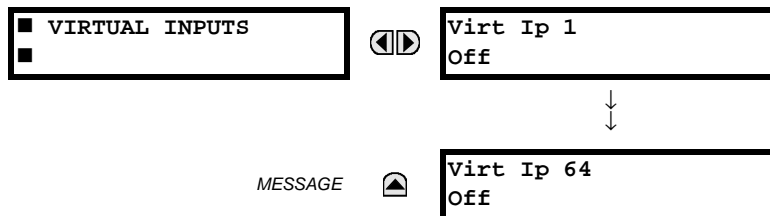
PATH: ACTUAL VALUES ⇒ STATUS ⇒ CONTACT INPUTS



The present status of the contact inputs is shown here. The first line of a message display indicates the ID of the contact input. For example, 'Cont Ip 1' refers to the contact input in terms of the default name-array index. The second line of the display indicates the logic state of the contact input.

6.2.2 VIRTUAL INPUTS

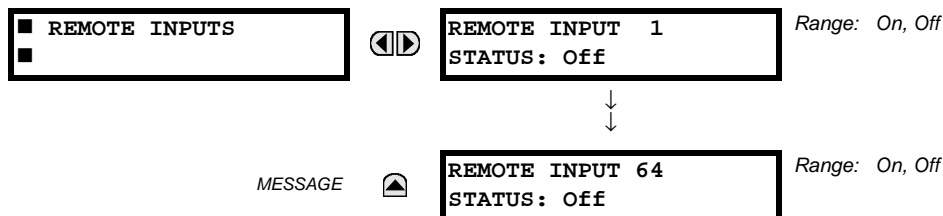
PATH: ACTUAL VALUES ⇒ STATUS ⇒ VIRTUAL INPUTS



The present status of the 64 virtual inputs is shown here. The first line of a message display indicates the ID of the virtual input. For example, 'Virt Ip 1' refers to the virtual input in terms of the default name. The second line of the display indicates the logic state of the virtual input.

6.2.3 REMOTE INPUTS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ REMOTE INPUTS

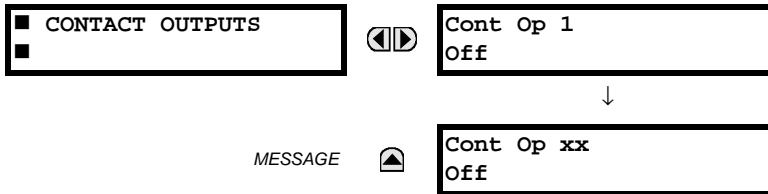


The present state of the 64 remote inputs is shown here.

The state displayed will be that of the remote point unless the remote device has been established to be "Offline" in which case the value shown is the programmed default state for the remote input.

6.2.4 CONTACT OUTPUTS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ CONTACT OUTPUTS



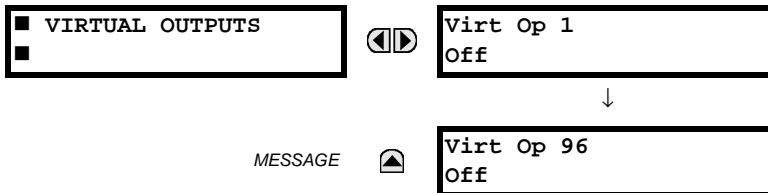
The present state of the contact outputs is shown here. The first line of a message display indicates the ID of the contact output. For example, 'Cont Op 1' refers to the contact output in terms of the default name-array index. The second line of the display indicates the logic state of the contact output.



For Form-A outputs, the state of the voltage(V) and/or current(I) detectors will show as: Off, VOff, IOff, On, VOn, and/or IOn. For Form-C outputs, the state will show as Off or On.

6.2.5 VIRTUAL OUTPUTS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ VIRTUAL OUTPUTS

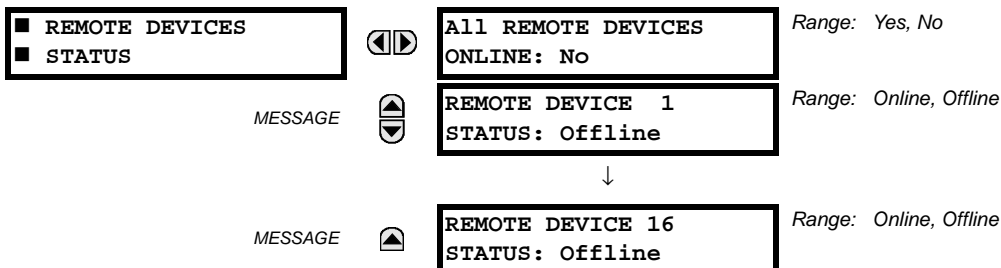


The present state of up to 96 virtual outputs is shown here. The first line of a message display indicates the ID of the virtual output. For example, 'Virt Op 1' refers to the virtual output in terms of the default name-array index. The second line of the display indicates the logic state of the virtual output, as calculated by the FlexLogic™ equation for that output.

6.2.6 REMOTE DEVICES

a) STATUS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ REMOTE DEVICES STATUS



The present state of up to 16 programmed Remote Devices is shown here. The **ALL REMOTE DEVICES ONLINE** message indicates whether or not all programmed Remote Devices are online. If the corresponding state is "No", then at least one required Remote Device is not online.

b) STATISTICS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ REMOTE DEVICES STATISTICS ⇒ REMOTE DEVICE 1(16)

<div> <div>REMOTE DEVICE 1</div> <div>MESSAGE</div> </div>	<div> <div>REMOTE DEVICE 1</div> <div>StNum: 0</div> </div> <div> <div>REMOTE DEVICE 1</div> <div>SqNum: 0</div> </div>
--	---

Statistical data (2 types) for up to 16 programmed Remote Devices is shown here.

The **StNum** number is obtained from the indicated Remote Device and is incremented whenever a change of state of at least one DNA or UserSt bit occurs. The **SqNum** number is obtained from the indicated Remote Device and is incremented whenever a GSSE message is sent. This number will rollover to zero when a count of 4,294,967,295 is incremented.

6.2.7 DIGITAL COUNTERS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ DIGITAL COUNTERS ⇒ DIGITAL COUNTERS Counter 1(8)

<div> <div>DIGITAL COUNTERS</div> <div>Counter 1</div> </div>	<div> <div>Counter 1</div> <div>ACCUM: 0</div> </div> <div> <div>Counter 1</div> <div>FROZEN: 0</div> </div> <div> <div>Counter 1</div> <div>FROZEN: YYYY/MM/DD HH:MM:SS</div> </div> <div> <div>Counter 1</div> <div>MICROS: 0</div> </div>
---	--

The present status of the 8 digital counters is shown here. The status of each counter, with the user-defined counter name, includes the accumulated and frozen counts (the count units label will also appear). Also included, is the date/time stamp for the frozen count. The **Counter n MICROS** value refers to the microsecond portion of the time stamp.

6.2.8 SELECTOR SWITCHES

PATH: ACTUAL VALUES ⇒ STATUS ⇒ SELECTOR SWITCHES

<div> <div>SELECTOR SWITCHES</div> <div>MESSAGE</div> </div>	<div> <div>SELECTOR SWITCH 1</div> <div>POSITION: 0 / 7</div> </div> <div> <div>SELECTOR SWITCH 2</div> <div>POSITION: 0 / 7</div> </div>	<div>Range: Current Position / 7</div> <div>Range: Current Position / 7</div>
--	---	---

The display shows both the current position and the full range. The current position only (an integer from 0 through 7) is the actual value.

6.2.9 FLEX STATES

PATH: ACTUAL VALUES ⇒ STATUS ⇒ FLEX STATES

<div> <div>FLEX STATES</div> <div>MESSAGE</div> </div>	<div> <div>PARAM 1: Off</div> <div>Off</div> </div> <div> <div>PARAM 256: Off</div> <div>Off</div> </div>	<div>Range: Off, On</div> <div>Range: Off, On</div>
--	---	---

There are 256 FlexState bits available. The second line value indicates the state of the given FlexState bit.

6.2.10 ETHERNET

PATH: ACTUAL VALUES ⇒ STATUS ⇒ ⚡ ETHERNET

<div>ETHERNET</div> <div>MESSAGE</div>	<div>⏮⏭</div> <div>⏮</div>	ETHERNET PRI LINK STATUS: OK	Range: Fail, OK
		ETHERNET SEC LINK STATUS: OK	Range: Fail, OK

These values indicate the status of the primary and secondary Ethernet links.

6.2.11 DIRECT INPUTS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ ⚡ DIRECT INPUTS

<div><div>■ DIRECT INPUTS</div><div>■</div></div>	<div>◀▶</div>	<div>AVG MSG RETURN</div> <div>TIME CH1: 0 ms</div>
<div>MESSAGE</div>	<div>▲▼</div>	<div>UNRETURNED MSG</div> <div>COUNT CH1: 0</div>
<div>MESSAGE</div>	<div>▲▼</div>	<div>CRC FAIL COUNT</div> <div>CH1: 0</div>
<div>MESSAGE</div>	<div>▲▼</div>	<div>AVG MSG RETURN</div> <div>TIME CH2: 0 ms</div>
<div>MESSAGE</div>	<div>▲▼</div>	<div>UNRETURNED MSG</div> <div>COUNT CH2: 0</div>
<div>MESSAGE</div>	<div>▲▼</div>	<div>CRC FAIL COUNT</div> <div>CH2: 0</div>
<div>MESSAGE</div>	<div>▲▼</div>	<div>DIRECT INPUT 1:</div> <div>On</div>
<div>↓</div>		
<div>MESSAGE</div>	<div>▲</div>	<div>DIRECT INPUT 64:</div> <div>On</div>

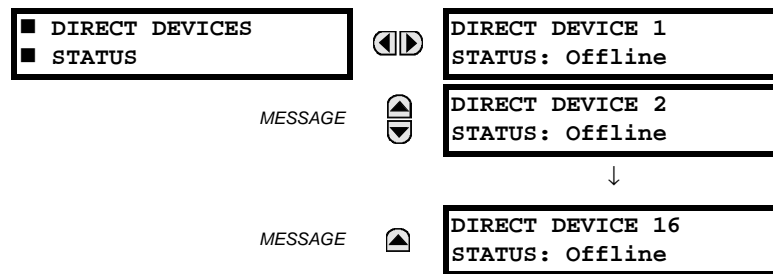
The **AVERAGE MSG RETURN TIME** is the time taken for direct output messages to return to the sender in a direct input/output ring configuration (this value is not applicable for non-ring configurations). This is a rolling average calculated for the last 10 messages. There are two return times for dual-channel communications modules.

The **UNRETURNED MSG COUNT** values (one per communications channel) count the direct output messages that do not make the trip around the communications ring. The **CRC FAIL COUNT** values (one per communications channel) count the direct output messages that have been received but fail the CRC check. High values for either of these counts may indicate on a problem with wiring, the communication channel, or the relay(s). The **UNRETURNED MSG COUNT** and **CRC FAIL COUNT** values can be cleared using the **CLEAR DIRECT I/O COUNTERS** command.

The **DIRECT INPUT x** values represent the state of the x-th direct input.

6.2.12 DIRECT DEVICES STATUS

PATH: ACTUAL VALUES ⇒ STATUS ⇒ DIRECT DEVICES STATUS



These actual values represent the state of direct devices 1 through 16.

6.3.1 METERING CONVENTIONS

a) UR CONVENTION FOR MEASURING POWER AND ENERGY

The following figure illustrates the conventions established for use in UR-series relays.

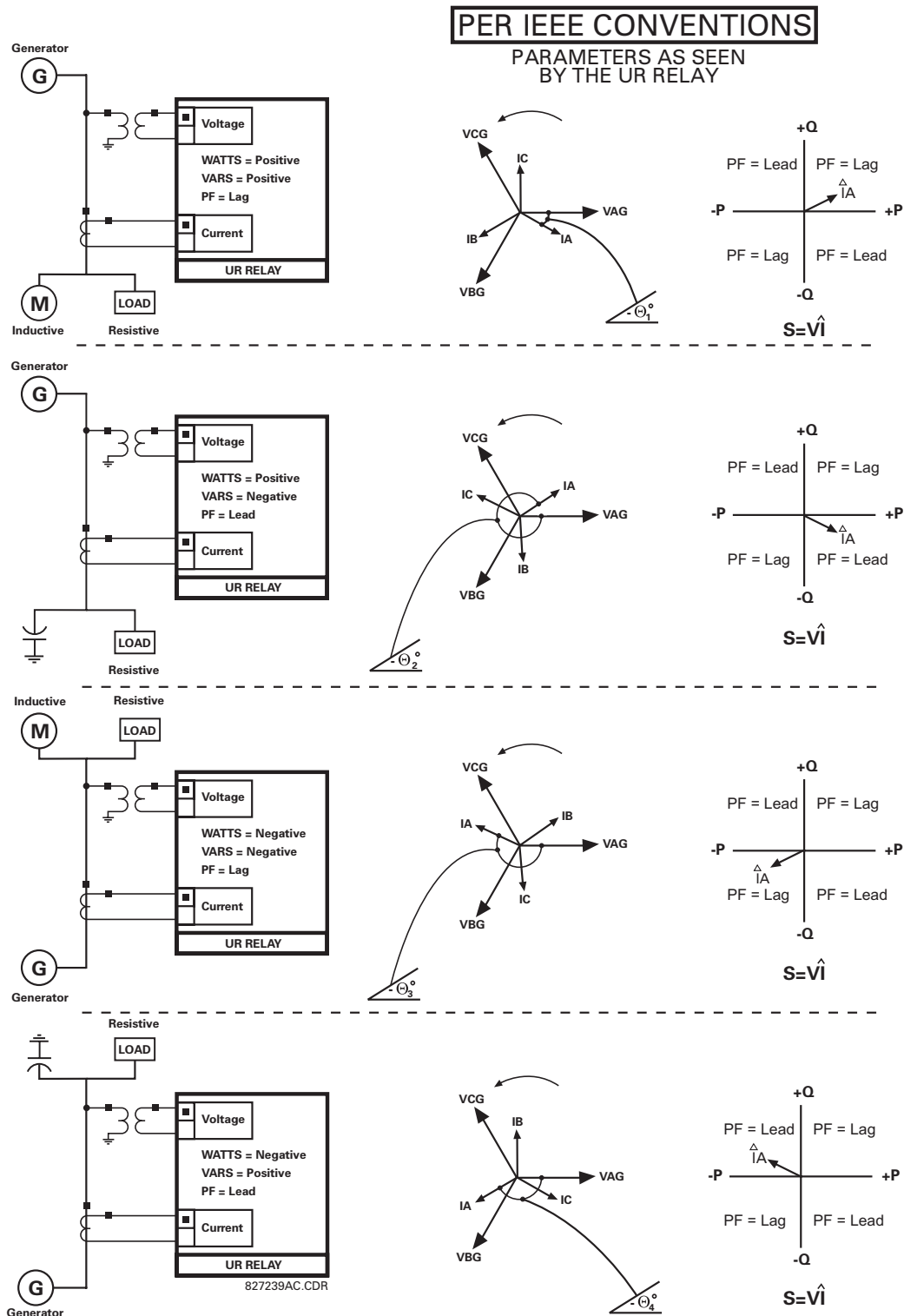


Figure 6-1: FLOW DIRECTION OF SIGNED VALUES FOR WATTS AND VARS

b) UR CONVENTION FOR MEASURING PHASE ANGLES

All phasors calculated by UR-series relays and used for protection, control and metering functions are rotating phasors that maintain the correct phase angle relationships with each other at all times.

For display and oscillography purposes, all phasor angles in a given relay are referred to an AC input channel pre-selected by the **SETTINGS** ⇒ **SYSTEM SETUP** ⇒ **POWER SYSTEM** ⇒ **FREQUENCY AND PHASE REFERENCE** setting. This setting defines a particular to be used as the reference.

If the AC signal pre-selected by the relay upon configuration is not measurable, the phase angles are not referenced. The phase angles are assigned as positive in the leading direction, and are presented as negative in the lagging direction, to more closely align with power system metering conventions. This is illustrated below.

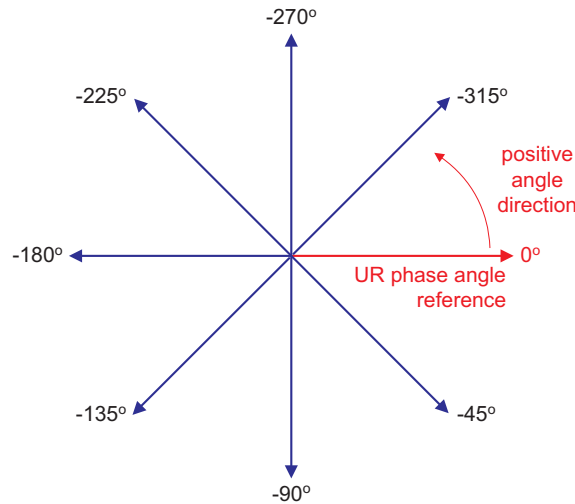


Figure 6-2: UR PHASE ANGLE MEASUREMENT CONVENTION

c) UR CONVENTION FOR MEASURING SYMMETRICAL COMPONENTS

The UR-series of relays calculate voltage symmetrical components for the power system phase A line-to-neutral voltage, and symmetrical components of the currents for the power system phase A current. Owing to the above definition, phase angle relations between the symmetrical currents and voltages stay the same irrespective of the connection of instrument transformers. This is important for setting directional protection elements that use symmetrical voltages.

For display and oscillography purposes the phase angles of symmetrical components are referenced to a common reference as described in the previous sub-section.

WYE-Connected Instrument Transformers:

- ABC phase rotation:

$$V_{-0} = \frac{1}{3}(V_{AG} + V_{BG} + V_{CG})$$

$$V_{-1} = \frac{1}{3}(V_{AG} + aV_{BG} + a^2V_{CG})$$

$$V_{-2} = \frac{1}{3}(V_{AG} + a^2V_{BG} + aV_{CG})$$

- ACB phase rotation:

$$V_{-0} = \frac{1}{3}(V_{AG} + V_{BG} + V_{CG})$$

$$V_{-1} = \frac{1}{3}(V_{AG} + a^2V_{BG} + aV_{CG})$$

$$V_{-2} = \frac{1}{3}(V_{AG} + aV_{BG} + a^2V_{CG})$$

The above equations apply to currents as well.

DELTA-Connected Instrument Transformers:

- ABC phase rotation:

$$V_0 = N/A$$

$$V_1 = \frac{1\angle-30^\circ}{3\sqrt{3}}(V_{AB} + aV_{BC} + a^2V_{CA})$$

$$V_2 = \frac{1\angle30^\circ}{3\sqrt{3}}(V_{AB} + a^2V_{BC} + aV_{CA})$$

- ACB phase rotation:

$$V_0 = N/A$$

$$V_1 = \frac{1\angle30^\circ}{3\sqrt{3}}(V_{AB} + a^2V_{BC} + aV_{CA})$$

$$V_2 = \frac{1\angle-30^\circ}{3\sqrt{3}}(V_{AB} + aV_{BC} + a^2V_{CA})$$

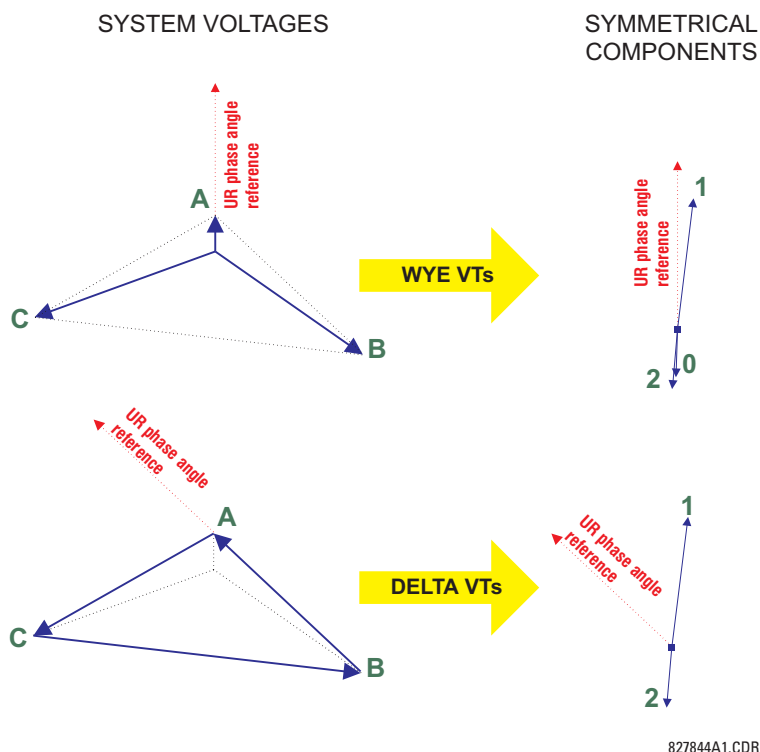
The zero-sequence voltage is not measurable under the Delta connection of instrument transformers and is defaulted to zero. The table below shows an example of symmetrical components calculations for the ABC phase rotation.

Table 6–1: SYMMETRICAL COMPONENTS CALCULATION EXAMPLE

SYSTEM VOLTAGES, SEC. V *						VT CONN.	RELAY INPUTS, SEC. V			SYMM. COMP, SEC. V		
V _{AG}	V _{BG}	V _{CG}	V _{AB}	V _{BC}	V _{CA}		F5AC	F6AC	F7AC	V ₀	V ₁	V ₂
13.9 ∠0°	76.2 ∠-125°	79.7 ∠-250°	84.9 ∠-313°	138.3 ∠-97°	85.4 ∠-241°	WYE	13.9 ∠0°	76.2 ∠-125°	79.7 ∠-250°	19.5 ∠-192°	56.5 ∠-7°	23.3 ∠-187°
UNKNOWN (only V ₁ and V ₂ can be determined)			84.9 ∠0°	138.3 ∠-144°	85.4 ∠-288°	DELTA	84.9 ∠0°	138.3 ∠-144°	85.4 ∠-288°	N/A	56.5 ∠-54°	23.3 ∠-234°

- * The power system voltages are phase-referenced – for simplicity – to V_{AG} and V_{AB}, respectively. This, however, is a relative matter. It is important to remember that the UR displays are always referenced as specified under **SETTINGS**
⇒ **SYSTEM SETUP** ⇒ **POWER SYSTEM** ⇒ **FREQUENCY AND PHASE REFERENCE**.

The example above is illustrated in the following figure.



827844A1.CDR

Figure 6–3: MEASUREMENT CONVENTION FOR SYMMETRICAL COMPONENTS

6.3.2 SOURCES

PATH: ACTUAL VALUES ⇒ METERING ⇒ SOURCE SRC 1 ⇒



Because energy values are accumulated, these values should be recorded and then reset immediately prior to changing CT or VT characteristics.

<input checked="" type="checkbox"/> PHASE CURRENT <input checked="" type="checkbox"/> SRC 1		<div>◀▶</div> <div>SRC 1 RMS Ia: 0.000 b: 0.000 c: 0.000 A</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 RMS Ia: 0.000 A</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 RMS Ib: 0.000 A</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 RMS Ic: 0.000 A</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 RMS In: 0.000 A</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 PHASOR Ia: 0.000 A 0.0°</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 PHASOR Ib: 0.000 A 0.0°</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 PHASOR Ic: 0.000 A 0.0°</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 PHASOR In: 0.000 A 0.0°</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 ZERO SEQ I0: 0.000 A 0.0°</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 POS SEQ I1: 0.000 A 0.0°</div>
MESSAGE	▲	<div>▲</div> <div>SRC 1 NEG SEQ I2: 0.000 A 0.0°</div>
▲		
<input checked="" type="checkbox"/> GROUND CURRENT <input checked="" type="checkbox"/> SRC 1		<div>◀▶</div> <div>SRC 1 RMS Ig: 0.000 A</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 PHASOR Ig: 0.000 A 0.0°</div>
MESSAGE	▲	<div>▲</div> <div>SRC 1 PHASOR Igd: 0.000 A 0.0°</div>
▲		
<input checked="" type="checkbox"/> PHASE VOLTAGE <input checked="" type="checkbox"/> SRC 1		<div>◀▶</div> <div>SRC 1 RMS Vag: 0.00 V</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 RMS Vbg: 0.00 V</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 RMS Vcg: 0.00 V</div>
MESSAGE	▲▼	<div>▲▼</div> <div>SRC 1 PHASOR Vag: 0.000 V 0.0°</div>

MESSAGE		SRC 1 PHASOR Vbg: 0.000 V 0.0°
MESSAGE		SRC 1 PHASOR Vcg: 0.000 V 0.0°
MESSAGE		SRC 1 RMS Vab: 0.00 V
MESSAGE		SRC 1 RMS Vbc: 0.00 V
MESSAGE		SRC 1 RMS Vca: 0.00 V
MESSAGE		SRC 1 PHASOR Vab: 0.000 V 0.0°
MESSAGE		SRC 1 PHASOR Vbc: 0.000 V 0.0°
MESSAGE		SRC 1 PHASOR Vca: 0.000 V 0.0°
MESSAGE		SRC 1 ZERO SEQ V0: 0.000 V 0.0°
MESSAGE		SRC 1 POS SEQ V1: 0.000 V 0.0°
MESSAGE		SRC 1 NEG SEQ V2: 0.000 V 0.0°
<div> <div>6</div> <div> <div></div> <div> <div>AUXILIARY VOLTAGE</div> <div>SRC 1</div> </div> </div> </div>		
		SRC 1 RMS Vx: 0.00 V
MESSAGE		SRC 1 PHASOR Vx: 0.000 V 0.0°
<div> <div>POWER</div> <div>SRC 1</div> </div>		
		SRC 1 REAL POWER 3φ: 0.000 W
MESSAGE		SRC 1 REAL POWER φa: 0.000 W
MESSAGE		SRC 1 REAL POWER φb: 0.000 W
MESSAGE		SRC 1 REAL POWER φc: 0.000 W
MESSAGE		SRC 1 REACTIVE PWR 3φ: 0.000 var
MESSAGE		SRC 1 REACTIVE PWR φa: 0.000 var
MESSAGE		SRC 1 REACTIVE PWR φb: 0.000 var
MESSAGE		SRC 1 REACTIVE PWR φc: 0.000 var

MESSAGE		SRC 1 APPARENT PWR 3 ϕ : 0.000 VA
MESSAGE		SRC 1 APPARENT PWR ϕ a: 0.000 VA
MESSAGE		SRC 1 APPARENT PWR ϕ b: 0.000 VA
MESSAGE		SRC 1 APPARENT PWR ϕ c: 0.000 VA
MESSAGE		SRC 1 POWER FACTOR 3 ϕ : 1.000
MESSAGE		SRC 1 POWER FACTOR ϕ a: 1.000
MESSAGE		SRC 1 POWER FACTOR ϕ b: 1.000
MESSAGE		SRC 1 POWER FACTOR ϕ c: 1.000
<div>■ ENERGY ■ SRC 1</div>		SRC 1 POS WATTHOUR: 0.000 Wh
MESSAGE		SRC 1 NEG WATTHOUR: 0.000 Wh
MESSAGE		SRC 1 POS VARHOUR: 0.000 varh
MESSAGE		SRC 1 NEG VARHOUR: 0.000 varh
<div>■ DEMAND ■ SRC 1</div>		SRC 1 DMD IA: 0.000 A
MESSAGE		SRC 1 DMD IA MAX: 0.000 A
MESSAGE		SRC 1 DMD IA DATE: 2001/07/31 16:30:07
MESSAGE		SRC 1 DMD IB: 0.000 A
MESSAGE		SRC 1 DMD IB MAX: 0.000 A
MESSAGE		SRC 1 DMD IB DATE: 2001/07/31 16:30:07
MESSAGE		SRC 1 DMD IC: 0.000 A
MESSAGE		SRC 1 DMD IC MAX: 0.000 A
MESSAGE		SRC 1 DMD IC DATE: 2001/07/31 16:30:07

MESSAGE		SRC 1 DMD W: 0.000 W
MESSAGE		SRC 1 DMD W MAX: 0.000 W
MESSAGE		SRC 1 DMD W DATE: 2001/07/31 16:30:07
MESSAGE		SRC 1 DMD VAR: 0.000 var
MESSAGE		SRC 1 DMD VAR MAX: 0.000 var
MESSAGE		SRC 1 DMD VAR DATE: 2001/07/31 16:30:07
MESSAGE		SRC 1 DMD VA: 0.000 VA
MESSAGE		SRC 1 DMD VA MAX: 0.000 VA
MESSAGE		SRC 1 DMD VA DATE: 2001/07/31 16:30:07
FREQUENCY SRC 1		SRC 1 FREQUENCY: 0.00 Hz

Six identical Source menus are available. The "SRC 1" text will be replaced by whatever name was programmed by the user for the associated source (see **SETTINGS** ⇒ **SYSTEM SETUP** ⇒ **SIGNAL SOURCES**).

6

The relay measures (absolute values only) **SOURCE DEMAND** on each phase and average three phase demand for real, reactive, and apparent power. These parameters can be monitored to reduce supplier demand penalties or for statistical metering purposes. Demand calculations are based on the measurement type selected in the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **DEMAND** menu. For each quantity, the relay displays the demand over the most recent demand time interval, the maximum demand since the last maximum demand reset, and the time and date stamp of this maximum demand value. Maximum demand quantities can be reset to zero with the **CLEAR RECORDS** ⇒ **CLEAR DEMAND RECORDS** command.

SOURCE FREQUENCY is measured via software-implemented zero-crossing detection of an AC signal. The signal is either a Clarke transformation of three-phase voltages or currents, auxiliary voltage, or ground current as per source configuration (see the **SYSTEM SETUP** ⇒ **POWER SYSTEM** settings). The signal used for frequency estimation is low-pass filtered. The final frequency measurement is passed through a validation filter that eliminates false readings due to signal distortions and transients.

6.3.3 SENSITIVE DIRECTIONAL POWER

PATH: ACTUAL VALUES ⇒ **METERING** ⇒ **SENSITIVE DIRECTIONAL POWER**

SENSITIVE DIRECTIONAL POWER		DIRECTIONAL POWER 1 3Φ: 0.000 W
MESSAGE		DIRECTIONAL POWER 2 3Φ: 0.000 W

The effective operating quantities of the sensitive directional power elements are displayed here. The display may be useful to calibrate the feature by compensating the angular errors of the CTs and VTs with the use of the **RCA** and **CALIBRATION** settings.

6.3.4 SYNCHROCHECK

PATH: ACTUAL VALUES ⇒ METERING ⇒ SYNCHROCHECK ⇒ SYNCHROCHECK 1(2)

■ SYNCHROCHECK 1	◀▶	SYNCHROCHECK 1 DELTA VOLT: 0.000 V
MESSAGE	▲▼	SYNCHROCHECK 1 DELTA PHASE: 0.0°
MESSAGE	▲	SYNCHROCHECK 1 DELTA FREQ: 0.00 Hz

The Actual Values menu for Synchrocheck 2 is identical to that of Synchrocheck 1. If a synchrocheck function setting is "Disabled", the corresponding actual values menu item will not be displayed.

6.3.5 TRACKING FREQUENCY

PATH: ACTUAL VALUES ⇒ METERING ⇒ TRACKING FREQUENCY

■ TRACKING FREQUENCY	◀▶	TRACKING FREQUENCY: 60.00 Hz
----------------------	----	---------------------------------

The tracking frequency is displayed here. The frequency is tracked based on configuration of the reference source. The **TRACKING FREQUENCY** is based upon positive sequence current phasors from all line terminals and is synchronously adjusted at all terminals. If currents are below 0.125 pu, then the **NOMINAL FREQUENCY** is used.

6.3.6 FLEXELEMENTS™

PATH: ACTUAL VALUES ⇒ METERING ⇒ FLEXELEMENTS ⇒ FLEXELEMENT 1(16)

■ FLEXELEMENT 1	◀▶	FLEXELEMENT 1 OpSig: 0.000 pu
-----------------	----	----------------------------------

The operating signals for the FlexElements™ are displayed in pu values using the following definitions of the base units.

Table 6–2: FLEXELEMENT™ BASE UNITS

dcmA	BASE = maximum value of the DCMA INPUT MAX setting for the two transducers configured under the +IN and –IN inputs.
FREQUENCY	$f_{BASE} = 1 \text{ Hz}$
PHASE ANGLE	$\phi_{BASE} = 360 \text{ degrees (see the UR angle referencing convention)}$
POWER FACTOR	$PF_{BASE} = 1.00$
RTDs	BASE = 100°C
SENSITIVE DIR POWER (Sns Dir Power)	$P_{BASE} = \text{maximum value of } 3 \times V_{BASE} \times I_{BASE} \text{ for the +IN and –IN inputs of the sources configured for the sensitive power directional element(s).}$
SOURCE CURRENT	$I_{BASE} = \text{maximum nominal primary RMS value of the +IN and –IN inputs}$
SOURCE ENERGY (SRC X Positive and Negative Watthours); (SRC X Positive and Negative Varhours)	$E_{BASE} = 10000 \text{ MWh or MVAh, respectively}$
SOURCE POWER	$P_{BASE} = \text{maximum value of } V_{BASE} \times I_{BASE} \text{ for the +IN and –IN inputs}$
SOURCE VOLTAGE	$V_{BASE} = \text{maximum nominal primary RMS value of the +IN and –IN inputs}$
SYNCHROCHECK (Max Delta Volts)	$V_{BASE} = \text{maximum primary RMS value of all the sources related to the +IN and –IN inputs}$

6.3.7 DIGITIZERS

PATH: ACTUAL VALUES ⇒ METERING ⇒ DIGITIZERS ⇒ DIGITIZER 1(5)

■ DIGITIZER 1	◀▶	DIGITIZER 1 Out: 0	Range: -127 to 127
---------------	----	-----------------------	--------------------

The digitized output signals are displayed as signed integer numbers.

6.3.8 8-BIT COMPARATORS

PATH: ACTUAL VALUES ⇒ METERING ⇒ 8-BIT COMPARATORS ⇒ 8-BIT COMP 1(6)

■ 8-BIT ■ COMP 1	◀▶	8BIT COMP 1 A: 0.00	Range: -12700.00 to 12700.00
MESSAGE	▲▼	8BIT COMP 1 B: 0.00	Range: -12700.00 to 12700.00
MESSAGE	▲▼	8BIT COMP 1 Out: 0.00	Range: -12700.00 to 12700.00

The two (A and B) 8-bit digitized inputs as well as the resulting sum/difference are available for display. The values are re-scaled from the original integer numbers using the 8BIT COMP 1 SCALE FACTOR setting.

6.3.9 PHASOR MEASUREMENT UNIT

PATH: ACTUAL VALUES ⇒ METERING ⇒ PHASOR MEASUREMENT UNIT ⇒ PMU 1(4)

■ PMU 1	◀▶	PMU 1 VA: 0.0000 kV, 0.00°	Range: Va or Vab per VT bank connection
MESSAGE	▲▼	PMU 1 VB: 0.0000 kV, 0.00°	Range: Va or Vab per VT bank connection
MESSAGE	▲▼	PMU 1 VC: 0.0000 kV, 0.00°	Range: Va or Vab per VT bank connection
MESSAGE	▲▼	PMU 1 VX: 0.0000 kV, 0.00°	
MESSAGE	▲▼	PMU 1 V1: 0.0000 kV, 0.00°	
MESSAGE	▲▼	PMU 1 V2: 0.0000 kV, 0.00°	
MESSAGE	▲▼	PMU 1 V0: 0.0000 kV, 0.00°	Range: Substituted with zero if delta-connected VTs.
MESSAGE	▲▼	PMU 1 IA: 0.0000 kA, 0.00°	
MESSAGE	▲▼	PMU 1 IB: 0.0000 kA, 0.00°	
MESSAGE	▲▼	PMU 1 IC: 0.0000 kA, 0.00°	
MESSAGE	▲▼	PMU 1 IG: 0.0000 kA, 0.00°	
MESSAGE	▲▼	PMU 1 I1: 0.0000 kA, 0.00°	

MESSAGE		PMU 1 I2: 0.0000 kA, 0.00°
MESSAGE		PMU 1 I0: 0.0000 kA, 0.00°
MESSAGE		PMU 1 FREQUENCY: 0.0000 Hz
MESSAGE		PMU 1 df/dt: 0.0000 Hz/s
MESSAGE		PMU 1 CONFIG CHANGE COUNTER: 0

Range: 0 to 65535

The above actual values are displayed without the corresponding time stamp as they become available per the recording rate setting. Also, the recording post-filtering setting is applied to these values.

6.3.10 TRANSDUCER INPUTS/OUTPUTS

PATH: ACTUAL VALUES ⇒ METERING ⇒ TRANSDUCER I/O DCMA INPUTS ⇒ DCMA INPUT xx

■ DCMA INPUT xx		DCMA INPUT xx 0.000 mA
-----------------	--	---------------------------

Actual values for each dcma input channel that is enabled are displayed with the top line as the programmed Channel ID and the bottom line as the value followed by the programmed units.

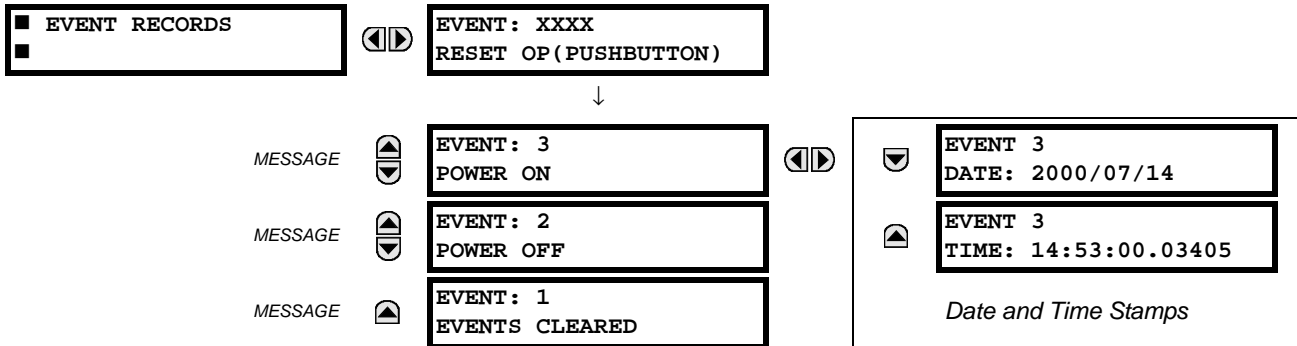
PATH: ACTUAL VALUES ⇒ METERING ⇒ TRANSDUCER I/O RTD INPUTS ⇒ RTD INPUT xx

■ RTD INPUT xx		RTD INPUT xx -50 °C
----------------	--	------------------------

Actual values for each RTD input channel that is enabled are displayed with the top line as the programmed Channel ID and the bottom line as the value.

6.4.1 EVENT RECORDS

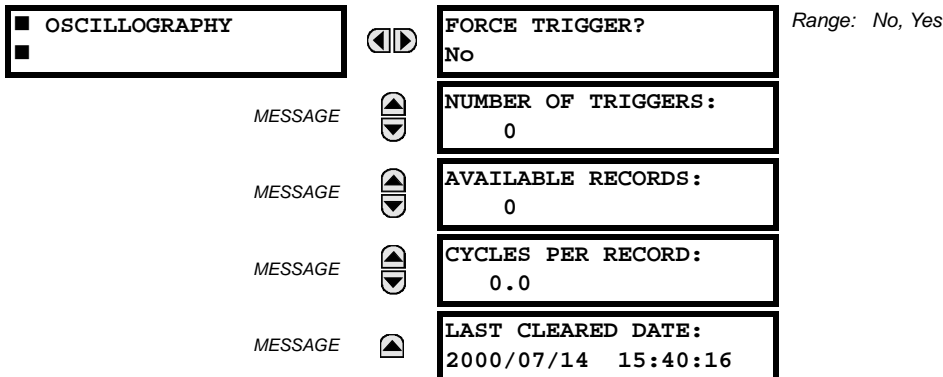
PATH: ACTUAL VALUES ⇒ RECORDS ⇒ EVENT RECORDS



The Event Records menu shows the contextual data associated with up to the last 1024 events, listed in chronological order from most recent to oldest. If all 1024 event records have been filled, the oldest record will be removed as a new record is added. Each event record shows the event identifier/sequence number, cause, and date/time stamp associated with the event trigger. Refer to the **COMMANDS** ⇒ **CLEAR RECORDS** menu for clearing event records.

6.4.2 OSCILLOGRAPHY

PATH: ACTUAL VALUES ⇒ RECORDS ⇒ OSCILLOGRAPHY

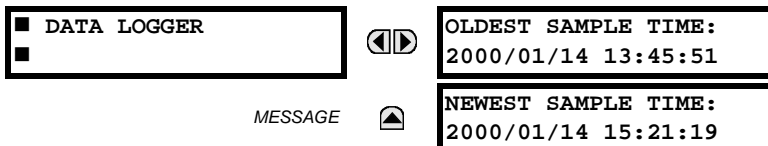


This menu allows the user to view the number of triggers involved and number of oscillography traces available. The 'cycles per record' value is calculated to account for the fixed amount of data storage for oscillography. See the Oscillography section of Chapter 5 for further details.

A trigger can be forced here at any time by setting "Yes" to the **FORCE TRIGGER?** command. Refer to the **COMMANDS** ⇒ **CLEAR RECORDS** menu for clearing the oscillography records.

6.4.3 DATA LOGGER

PATH: ACTUAL VALUES ⇒ RECORDS ⇒ DATA LOGGER



The **OLDEST SAMPLE TIME** is the time at which the oldest available samples were taken. It will be static until the log gets full, at which time it will start counting at the defined sampling rate. The **NEWEST SAMPLE TIME** is the time the most recent samples were taken. It counts up at the defined sampling rate. If Data Logger channels are defined, then both values are static.

Refer to the **COMMANDS** ⇒ **CLEAR RECORDS** menu for clearing data logger records.

6.4.4 PHASE MEASUREMENT UNIT RECORDS

PATH: ACTUAL VALUES ⇒ RECORDS ⇒ PMU RECORDS

■ PMU ■ RECORDS	◀▶	NUMBER OF TRIGGERS: 0	
MESSAGE	▲▼	■ PMU 1 ■ RECORDING	See below.
MESSAGE	▲▼	■ PMU 2 ■ RECORDING	See below.
		↓	
MESSAGE	▲	■ PMU 6 ■ RECORDING	See below.

The number of triggers applicable to all the phase measurement unit recorders is indicated by the **NUMBER OF TRIGGERS** value. The status for each of the six phase measurement unit recorders is indicated as follows:

PATH: ACTUAL VALUES ⇒ RECORDS ⇒ PMU RECORDS ⇒ PMU 1(6) RECORDING

■ PMU 1 ■ RECORDING	◀▶	PMU 1 FORCE TRIGGER: Yes
MESSAGE	▲▼	PUM 1 AVAILABLE RECORDS: 0
MESSAGE	▲▼	PUM 1 SECONDS PER RECORD: 0.0
MESSAGE	▲	PUM 1 LAST CLEARED: 2005/07/14 015:40:16

6.4.5 BREAKER MAINTENANCE

6

PATH: ACTUAL VALUES ⇒ RECORDS ⇒ MAINTENANCE ⇒ BREAKER 1

■ BREAKER 1 ■	◀▶	BKR 1 ARCING AMP ϕ A: 0.00 kA ² -cyc
MESSAGE	▲▼	BKR 1 ARCING AMP ϕ B: 0.00 kA ² -cyc
MESSAGE	▲▼	BKR 1 ARCING AMP ϕ C: 0.00 kA ² -cyc
MESSAGE	▲▼	BKR 1 OPERATING TIME ϕ A: 0 ms
MESSAGE	▲▼	BKR 1 OPERATING TIME ϕ B: 0 ms
MESSAGE	▲▼	BKR 1 OPERATING TIME ϕ C: 0 ms
MESSAGE	▲	BKR 1 OPERATING TIME: 0 ms

There is an identical menu for each of the breakers. The **BKR 1 ARCING AMP** values are in units of kA²-cycles. Refer to the **COMMANDS ⇒ CLEAR RECORDS** menu for clearing breaker arcing current records. The **BREAKER OPERATING TIME** is defined as the slowest operating time of breaker poles that were initiated to open.

6.5.1 MODEL INFORMATION

PATH: ACTUAL VALUES ⇒ ↓ PRODUCT INFO ⇒ MODEL INFORMATION

■ MODEL INFORMATION		◀▶	ORDER CODE LINE 1: N60-E00-HCH-F8H-H6G	Example code shown
MESSAGE	▲▼		ORDER CODE LINE 2:	
MESSAGE	▲▼		ORDER CODE LINE 3:	
MESSAGE	▲▼		ORDER CODE LINE 4:	
MESSAGE	▲▼		SERIAL NUMBER:	
MESSAGE	▲▼		ETHERNET MAC ADDRESS 000000000000	
MESSAGE	▲▼		MANUFACTURING DATE: 0	Range: YYYY/MM/DD HH:MM:SS
MESSAGE	▲		OPERATING TIME: 0:00:00	

The product order code, serial number, Ethernet MAC address, date/time of manufacture, and operating time are shown here.

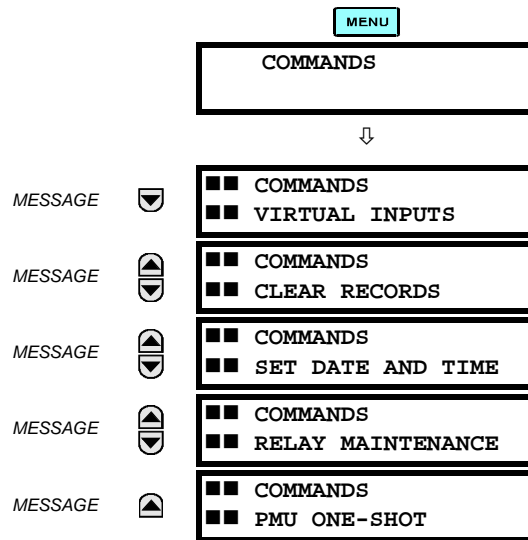
6.5.2 FIRMWARE REVISIONS

PATH: ACTUAL VALUES ⇒ ↓ PRODUCT INFO ⇒ ↓ FIRMWARE REVISIONS

■ FIRMWARE REVISIONS		◀▶	N60 Network Relay REVISION: 4.40	Range: 0.00 to 655.35 Revision number of the application firmware.
MESSAGE	▲▼		MODIFICATION FILE NUMBER: 0	Range: 0 to 65535 (ID of the MOD FILE) Value is 0 for each standard firmware release.
MESSAGE	▲▼		BOOT PROGRAM REVISION: 1.13	Range: 0.00 to 655.35 Revision number of the boot program firmware.
MESSAGE	▲▼		FRONT PANEL PROGRAM REVISION: 0.08	Range: 0.00 to 655.35 Revision number of faceplate program firmware.
MESSAGE	▲▼		COMPILE DATE: 2004/09/15 04:55:16	Range: Any valid date and time. Date and time when product firmware was built.
MESSAGE	▲		BOOT DATE: 2004/09/15 16:41:32	Range: Any valid date and time. Date and time when the boot program was built.

The shown data is illustrative only. A modification file number of 0 indicates that, currently, no modifications have been installed.

7.1.1 COMMANDS MENU

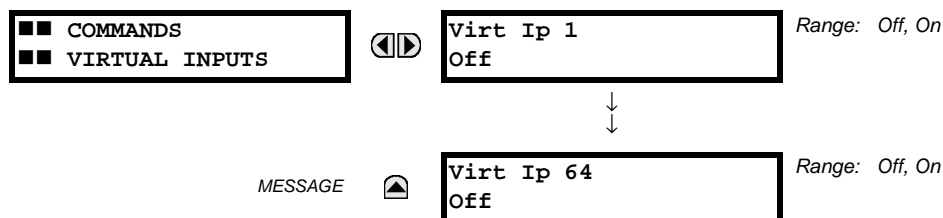


The commands menu contains relay directives intended for operations personnel. All commands can be protected from unauthorized access via the command password; see the *Password Security* section of Chapter 5 for details. The following flash message appears after successfully command entry:

COMMAND
EXECUTED

7.1.2 VIRTUAL INPUTS

PATH: COMMANDS ⇒ VIRTUAL INPUTS



The states of up to 64 virtual inputs are changed here. The first line of the display indicates the ID of the virtual input. The second line indicates the current or selected status of the virtual input. This status will be a logical state 'Off' (0) or 'On' (1).

7.1.3 CLEAR RECORDS

PATH: COMMANDS ⇒ ↓ CLEAR RECORDS

■ ■ COMMANDS	◀▶	CLEAR EVENT RECORDS?	Range: No, Yes
■ ■ CLEAR RECORDS		No	
	▲▼	CLEAR OSCILLOGRAPHY?	Range: No, Yes
		No	
	▲▼	CLEAR DATA LOGGER?	Range: No, Yes
		No	
	▲▼	CLEAR DEMAND RECORDS?:	Range: No, Yes
		No	
	▲▼	CLEAR ENERGY?	Range: No, Yes
		No	
	▲▼	CLEAR UNAUTHORIZED ACCESS?	Range: No, Yes
		No	
	▲▼	CLEAR DIRECT I/O COUNTERS?	Range: No, Yes. Valid only for units with Direct Input/Output module.
		No	
	▲▼	CLEAR PMU 1 RECORDS?	Range: No, Yes
		No	
	▲▼	CLEAR PMU 2 RECORDS?	Range: No, Yes
		No	
	▲▼	CLEAR PMU 3 RECORDS?	Range: No, Yes
		No	
	▲▼	CLEAR PMU 4 RECORDS?	Range: No, Yes
		No	
	▲▼	CLEAR PMU 1 CONFIG CHANGE COUNTER?	Range: No, Yes
		No	
	▲▼	CLEAR PMU 2 CONFIG CHANGE COUNTER?	Range: No, Yes
		No	
	▲▼	CLEAR PMU 3 CONFIG CHANGE COUNTER?	Range: No, Yes
		No	
	▲▼	CLEAR PMU 4 CONFIG CHANGE COUNTER?	Range: No, Yes
		No	
	▲	CLEAR ALL RELAY RECORDS?	Range: No, Yes
		No	

This menu contains commands for clearing historical data such as the Event Records. Data is cleared by changing a command setting to “Yes” and pressing the **ENTER** key. After clearing data, the command setting automatically reverts to “No”.

7.1.4 SET DATE AND TIME

PATH: COMMANDS ⇒ ↓ SET DATE AND TIME

■ ■ COMMANDS	◀▶	SET DATE AND TIME:	(YYYY/MM/DD HH:MM:SS)
■ ■ SET DATE AND TIME		2000/01/14 13:47:03	

The date and time can be entered here via the faceplate keypad only if the IRIG-B or SNTP signal is not in use. The time setting is based on the 24-hour clock. The complete date, as a minimum, must be entered to allow execution of this command. The new time will take effect at the moment the **ENTER** key is clicked.

7.1.5 RELAY MAINTENANCE

PATH: COMMANDS ⇒ RELAY MAINTENANCE

<div>■ ■ COMMANDS</div> <div>■ ■ RELAY MAINTENANCE</div>	◀ ▶	<div>PERFORM LAMPTTEST?</div> <div>No</div>	Range: No, Yes
	▲	<div>UPDATE ORDER CODE?</div> <div>No</div>	Range: No, Yes

This menu contains commands for relay maintenance purposes. Commands are activated by changing a command setting to “Yes” and pressing the **ENTER** key. The command setting will then automatically revert to “No”.

The **PERFORM LAMPTTEST** command turns on all faceplate LEDs and display pixels for a short duration. The **UPDATE ORDER CODE** command causes the relay to scan the backplane for the hardware modules and update the order code to match. If an update occurs, the following message is shown.

UPDATING...
 PLEASE WAIT

There is no impact if there have been no changes to the hardware modules. When an update does not occur, the **ORDER CODE NOT UPDATED** message will be shown.

7.1.6 PHASE MEASUREMENT UNIT ONE-SHOT

PATH: COMMANDS ⇒ PMU ONE-SHOT

<div>■ ■ COMMANDS</div> <div>■ ■ PMU ONE-SHOT</div>	◀ ▶	<div>PMU ONE-SHOT</div> <div>FUNCTION: Disabled</div>	Range: Enabled, Disabled
	▲	<div>PMU ONE-SHOT</div> <div>SEQUENCE NUMBER: 0</div>	Range: 0 to nominal frequency – 1 in steps of 1
	▲	<div>PMU ONE-SHOT TIME:</div> <div>2005/06/14 7:58:35</div>	Range: 24h time format

This feature allows pre-scheduling a PMU measurement at a specific point in time. This functionality can be used to test for accuracy of the PMU, and for manual collection of synchronized measurements through the system, as explained below.

When enabled, the function continuously compares the present time with the pre-set **PMU ONE-SHOT TIME**. When the two times match, the function compares the present sequence number of the measured synchrophasors with the pre-set **PMU ONE-SHOT SEQUENCE NUMBER**. When the two numbers match, the function freezes the synchrophasor actual values and the corresponding protocol data items for 30 seconds. This allows manual read-out of the synchrophasor values for the pre-set time and pre-set sequence number (via the faceplate display, supported communication protocols such as Modbus or DNP, and the EnerVista UR Setup software).

When freezing the actual values the function also asserts a PMU ONE-SHOT OP FlexLogic™ operand. This operand may be configured to drive an output contact and trigger an external measuring device such as a digital scope with the intent to verify the accuracy of the PMU under test.

With reference to the figure below, the PMU one-shot function (when enabled) controls three FlexLogic™ operands:

- The PMU ONE-SHOT EXPIRED operand indicates that the one-shot operation has been executed, and the present time is at least 30 seconds past the scheduled one-shot time.
- The PMU ONE-SHOT PENDING operand indicates that the one-shot operation is pending; that is, the present time is before the scheduled one-shot time.
- The PMU ONE-SHOT OP operand indicates the one-shot operation and remains asserted for 30 seconds afterwards.

When the function is disabled, all three operands are de-asserted. The one-shot function applies to all logical PMUs of a given N60 relay.

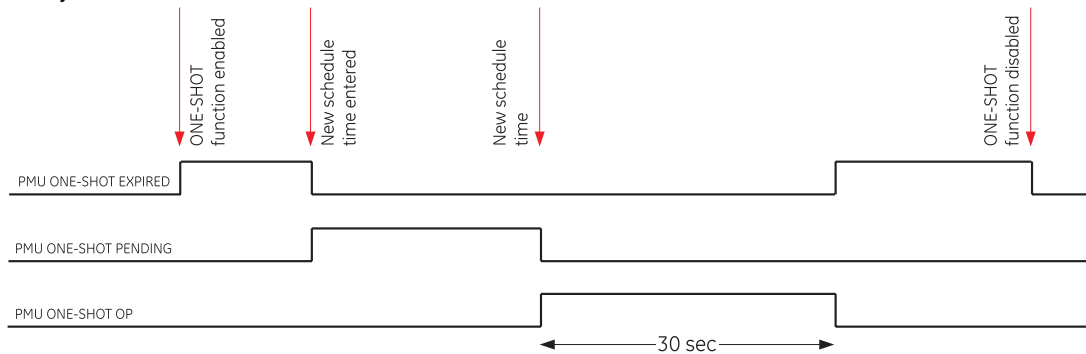


Figure 7-1: PMU ONE-SHOT FLEXLOGIC™ OPERANDS

TESTING ACCURACY OF THE PMU:

The one-shot feature can be used to test accuracy of the synchrophasor measurement. GPS-synchronized test sets perform a similar function to PMUs: instead of measuring the phasor from physical signals with respect to the externally provided time reference, they produce the physical signals with respect to the externally provided time reference, given the desired phasor values. Therefore the GPS-synchronized test sets cannot be automatically assumed more accurate than the PMUs under test. This calls for a method to verify both the measuring device (PMU) and the source of signal (test set).

With reference to the figure below, the one-shot feature could be configured to trigger a high-accuracy scope to capture both the time reference signal (rising edge of the 1 pps signal of the IRIG-B time reference), and the measured waveform. The high-accuracy high-sampling rate record of the two signals captured by the scope can be processed using digital tools to verify the magnitude and phase angle with respect to the time reference signal. As both the time reference and the measured signals are raw inputs to the PMU under test, their independently captured record, processed using third-party software, is a good reference point for accuracy calculations. Such a record proves useful when discussing the test results, and should be retained as a part of the testing documentation.

Note that the PMU under such test does not have to be connected to a real GPS receiver as the accuracy is measured with respect to the timing reference provided to the PMU and not to the absolute UTC time. Therefore a simple IRIG-B generator could be used instead. Also, the test set does not have to support GPS synchronization. Any stable signal source can be used. If both the PMU under test and the test set use the timing reference, they should be driven from the same IRIG-B signal: either the same GPS receiver or IRIG-B generator. Otherwise, the setpoints of the test set and the PMU measurements should not be compared as they are referenced to different time scales.

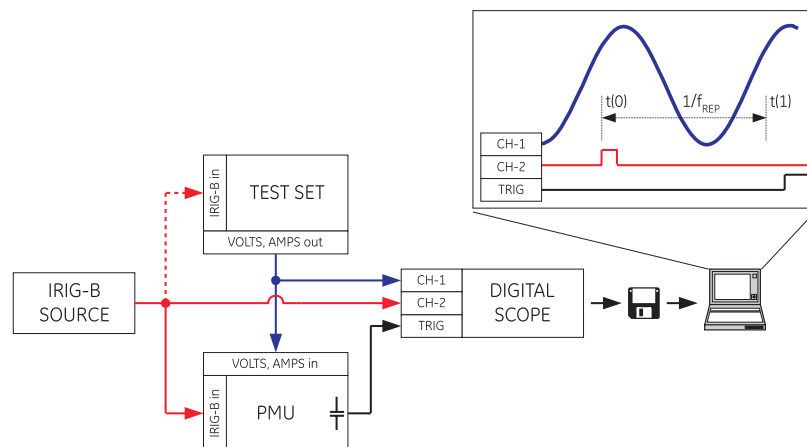
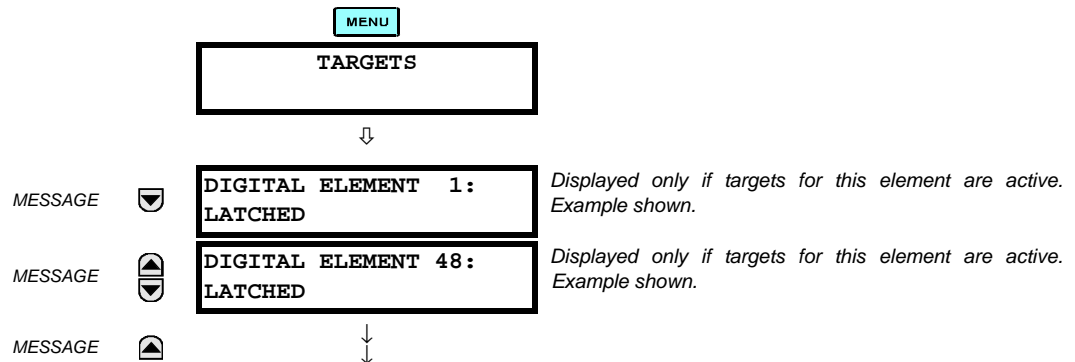


Figure 7-2: USING THE PMU ONE-SHOT FEATURE TO TEST SYNCHROPHASOR MEASUREMENT ACCURACY

COLLECTING SYNCHRONIZED MEASUREMENTS AD HOC:

The one-shot feature can be used for ad hoc collection of synchronized measurements in the network. Two or more PMU can be pre-scheduled to freeze their measurements at the same time. When frozen the measurements could be collected using EnerVista UR Setup or a protocol client.

7.2.1 TARGETS MENU



The status of any active targets will be displayed in the Targets menu. If no targets are active, the display will read **No Active Targets**:

7.2.2 TARGET MESSAGES

When there are no active targets, the first target to become active will cause the display to immediately default to that message. If there are active targets and the user is navigating through other messages, and when the default message timer times out (i.e. the keypad has not been used for a determined period of time), the display will again default back to the target message.

The range of variables for the target messages is described below. Phase information will be included if applicable. If a target message status changes, the status with the highest priority will be displayed.

Table 7–1: TARGET MESSAGE PRIORITY STATUS

PRIORITY	ACTIVE STATUS	DESCRIPTION
1	OP	element operated and still picked up
2	PKP	element picked up and timed out
3	LATCHED	element had operated but has dropped out

If a self test error is detected, a message appears indicating the cause of the error. For example **UNIT NOT PROGRAMMED** indicates that the minimal relay settings have not been programmed.

7.2.3 RELAY SELF-TESTS

The relay performs a number of self-test diagnostic checks to ensure device integrity. The two types of self-tests (major and minor) are listed in the tables below. When either type of self-test error occurs, the Trouble LED Indicator will turn on and a target message displayed. All errors record an event in the event recorder. Latched errors can be cleared by pressing the RESET key, providing the condition is no longer present.

Major self-test errors also result in the following:

- the critical fail relay on the power supply module is de-energized
- all other output relays are de-energized and are prevented from further operation
- the faceplate In Service LED indicator is turned off
- a RELAY OUT OF SERVICE event is recorded

Most of the minor self-test errors can be disabled. Refer to the settings in the User-Programmable Self-Tests section in Chapter 5 for additional details.

Table 7-2: MAJOR SELF-TEST ERROR MESSAGES

SELF-TEST ERROR MESSAGE	LATCHED TARGET MESSAGE?	DESCRIPTION OF PROBLEM	HOW OFTEN THE TEST IS PERFORMED	WHAT TO DO
DSP ERRORS: A/D Calibration, A/D Interrupt, A/D Reset, Inter DSP Rx, Sample Int, Rx Interrupt, Tx Interrupt, Rx Sample Index, Invalid Settings, Rx Checksum	Yes	CT/VT module with digital signal processor may have a problem.	Every 1/8th of a cycle.	Cycle the control power (if the problem recurs, contact the factory).
DSP ERROR: INVALID REVISION	Yes	One or more DSP modules in a multiple DSP unit has Rev. C hardware	Rev. C DSP needs to be replaced with a Rev. D DSP.	Contact the factory
EQUIPMENT MISMATCH with 2nd-line detail	No	Configuration of modules does not match the order code stored in the CPU.	On power up; thereafter, the backplane is checked for missing cards every 5 seconds.	Check all modules against the order code, ensure they are inserted properly, and cycle control power (if problem persists, contact factory).
FLEXLOGIC ERR TOKEN with 2nd-line detail	No	FlexLogic™ equations do not compile properly.	Event driven; whenever Flex-Logic™ equations are modified.	Finish all equation editing and use self test to debug any errors.
LATCHING OUTPUT ERROR	No	Discrepancy in the position of a latching contact between firmware and hardware has been detected.	Every 1/8th of a cycle.	The latching output module failed. Replace the Module.
PROGRAM MEMORY Test Failed	Yes	Error was found while checking Flash memory.	Once flash is uploaded with new firmware.	Contact the factory.
UNIT NOT CALIBRATED	No	Settings indicate the unit is not calibrated.	On power up.	Contact the factory.
UNIT NOT PROGRAMMED	No	PRODUCT SETUP ⇄ INSTALLATION setting indicates relay is not in a programmed state.	On power up and whenever the RELAY PROGRAMMED setting is altered.	Program all settings (especially those under PRODUCT SETUP ⇄ INSTALLATION).

Table 7-3: MINOR SELF-TEST ERROR MESSAGES

SELF-TEST ERROR MESSAGE	LATCHED TARGET MESSAGE	DESCRIPTION OF PROBLEM	HOW OFTEN THE TEST IS PERFORMED	WHAT TO DO
BATTERY FAIL	Yes	Battery is not functioning.	Monitored every 5 seconds. Reported after 1 minute if problem persists.	Replace the battery located in the power supply module (1H or 1L).
DIRECT RING BREAK	No	Direct input/output settings configured for a ring, but the connection is not in a ring.	Every second.	Check direct input/output configuration and/or wiring.
DIRECT DEVICE OFF	No	A direct device is configured but not connected.	Every second.	Check direct input/output configuration and/or wiring.
EEPROM DATA ERROR	Yes	The non-volatile memory has been corrupted.	On power up only.	If this message appears after an order code update is performed, press the RESET key to clear target message. In other cases, contact the factory.
IRIG-B FAILURE	No	A bad IRIG-B input signal has been detected	Monitored whenever an IRIG-B signal is received.	Ensure the IRIG-B cable is connected, check cable functionality (i.e. look for physical damage or perform continuity test), ensure IRIG-B receiver is functioning, and check input signal level (it may be less than specification). If none of these apply, contact the factory.
LATCHING OUT ERROR	Yes	Latching output failure.	Event driven.	Contact the factory.
LOW ON MEMORY	Yes	Memory is close to 100% capacity.	Monitored every 5 seconds.	Contact the factory.
PRI ETHERNET FAIL	Yes	Primary Ethernet connection failed.	Monitored every 2 seconds	Check connections.
PROTOTYPE FIRMWARE	Yes	A prototype version of the firmware is loaded.	On power up only.	Contact the factory.
REMOTE DEVICE OFF	No	One or more GOOSE devices are not responding.	Event driven – occurs when a device programmed to receive GOOSE messages stops receiving. Every 1 to 60 s, depending on GOOSE packets.	Check GOOSE setup.
SEC ETHERNET FAIL	Yes	Sec. Ethernet connection failed.	Monitored every 2 seconds	Check connections.
SNTP FAILURE	No	SNTP server not responding.	10 to 60 seconds.	Check SNTP configuration and/or network connections.
SYSTEM EXCEPTION	Yes	Abnormal restart from modules being removed/inserted when powered-up, abnormal DC supply, or internal relay failure.	Event driven.	Contact the factory.
WATCHDOG ERROR	No	Some tasks are behind schedule.	Event driven.	Contact the factory.

Table A-1: FLEXANALOG DATA ITEMS (Sheet 1 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
5760	Sens Dir Power 1 Actual	Sns Dir Power 1
5762	Sens Dir Power 2 Actual	Sns Dir Power 2
5856	Frequency Rate of Change 1 Actual	Freq Rate 1 Value
5860	Frequency Rate of Change 2 Actual	Freq Rate 2 Value
5864	Frequency Rate of Change 3 Actual	Freq Rate 3 Value
5868	Frequency Rate of Change 4 Actual	Freq Rate 4 Value
6144	SRC 1 Phase A Current RMS	SRC 1 Ia RMS
6146	SRC 1 Phase B Current RMS	SRC 1 Ib RMS
6148	SRC 1 Phase C Current RMS	SRC 1 Ic RMS
6150	SRC 1 Neutral Current RMS	SRC 1 In RMS
6152	SRC 1 Phase A Current Magnitude	SRC 1 Ia Mag
6154	SRC 1 Phase A Current Angle	SRC 1 Ia Angle
6155	SRC 1 Phase B Current Magnitude	SRC 1 Ib Mag
6157	SRC 1 Phase B Current Angle	SRC 1 Ib Angle
6158	SRC 1 Phase C Current Magnitude	SRC 1 Ic Mag
6160	SRC 1 Phase C Current Angle	SRC 1 Ic Angle
6161	SRC 1 Neutral Current Magnitude	SRC 1 In Mag
6163	SRC 1 Neutral Current Angle	SRC 1 In Angle
6164	SRC 1 Ground Current RMS	SRC 1 Ig RMS
6166	SRC 1 Ground Current Magnitude	SRC 1 Ig Mag
6168	SRC 1 Ground Current Angle	SRC 1 Ig Angle
6169	SRC 1 Zero Seq. Current Magnitude	SRC 1 I ₀ Mag
6171	SRC 1 Zero Sequence Current Angle	SRC 1 I ₀ Angle
6172	SRC 1 Pos. Seq. Current Magnitude	SRC 1 I ₁ Mag
6174	SRC 1 Pos. Seq. Current Angle	SRC 1 I ₁ Angle
6175	SRC 1 Neg. Seq. Current Magnitude	SRC 1 I ₂ Mag
6177	SRC 1 Neg. Seq. Current Angle	SRC 1 I ₂ Angle
6178	SRC 1 Differential Gnd Current Mag.	SRC 1 Igd Mag
6180	SRC 1 Diff. Gnd. Current Angle	SRC 1 Igd Angle
6208	SRC 2 Phase A Current RMS	SRC 2 Ia RMS
6210	SRC 2 Phase B Current RMS	SRC 2 Ib RMS
6212	SRC 2 Phase C Current RMS	SRC 2 Ic RMS
6214	SRC 2 Neutral Current RMS	SRC 2 In RMS
6216	SRC 2 Phase A Current Magnitude	SRC 2 Ia Mag
6218	SRC 2 Phase A Current Angle	SRC 2 Ia Angle
6219	SRC 2 Phase B Current Magnitude	SRC 2 Ib Mag
6221	SRC 2 Phase B Current Angle	SRC 2 Ib Angle
6222	SRC 2 Phase C Current Magnitude	SRC 2 Ic Mag
6224	SRC 2 Phase C Current Angle	SRC 2 Ic Angle
6225	SRC 2 Neutral Current Magnitude	SRC 2 In Mag
6227	SRC 2 Neutral Current Angle	SRC 2 In Angle
6228	SRC 2 Ground Current RMS	SRC 2 Ig RMS
6230	SRC 2 Ground Current Magnitude	SRC 2 Ig Mag
6232	SRC 2 Ground Current Angle	SRC 2 Ig Angle
6233	SRC 2 Zero Seq. Current Magnitude	SRC 2 I ₀ Mag
6235	SRC 2 Zero Sequence Current Angle	SRC 2 I ₀ Angle
6236	SRC 2 Pos. Seq. Current Magnitude	SRC 2 I ₁ Mag
6238	SRC 2 Positive Seq. Current Angle	SRC 2 I ₁ Angle
6239	SRC 2 Neg. Seq. Current Magnitude	SRC 2 I ₂ Mag
6241	SRC 2 Negative Seq. Current Angle	SRC 2 I ₂ Angle

Table A-1: FLEXANALOG DATA ITEMS (Sheet 2 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
6242	SRC 2 Differential Gnd Current Mag.	SRC 2 Igd Mag
6244	SRC 2 Diff. Gnd Current Angle	SRC 2 Igd Angle
6272	SRC 3 Phase A Current RMS	SRC 3 Ia RMS
6274	SRC 3 Phase B Current RMS	SRC 3 Ib RMS
6276	SRC 3 Phase C Current RMS	SRC 3 Ic RMS
6278	SRC 3 Neutral Current RMS	SRC 3 In RMS
6280	SRC 3 Phase A Current Magnitude	SRC 3 Ia Mag
6282	SRC 3 Phase A Current Angle	SRC 3 Ia Angle
6283	SRC 3 Phase B Current Magnitude	SRC 3 Ib Mag
6285	SRC 3 Phase B Current Angle	SRC 3 Ib Angle
6286	SRC 3 Phase C Current Magnitude	SRC 3 Ic Mag
6288	SRC 3 Phase C Current Angle	SRC 3 Ic Angle
6289	SRC 3 Neutral Current Magnitude	SRC 3 In Mag
6291	SRC 3 Neutral Current Angle	SRC 3 In Angle
6292	SRC 3 Ground Current RMS	SRC 3 Ig RMS
6294	SRC 3 Ground Current Magnitude	SRC 3 Ig Mag
6296	SRC 3 Ground Current Angle	SRC 3 Ig Angle
6297	SRC 3 Zero Seq. Current Magnitude	SRC 3 I ₀ Mag
6299	SRC 3 Zero Sequence Current Angle	SRC 3 I ₀ Angle
6300	SRC 3 Pos. Seq. Current Magnitude	SRC 3 I ₁ Mag
6302	SRC 3 Positive Seq. Current Angle	SRC 3 I ₁ Angle
6303	SRC 3 Neg. Seq. Current Magnitude	SRC 3 I ₂ Mag
6305	SRC 3 Negative Seq. Current Angle	SRC 3 I ₂ Angle
6306	SRC 3 Differential Gnd Current Mag.	SRC 3 Igd Mag
6308	SRC 3 Differential Gnd Current Angle	SRC 3 Igd Angle
6336	SRC 4 Phase A Current RMS	SRC 4 Ia RMS
6338	SRC 4 Phase B Current RMS	SRC 4 Ib RMS
6340	SRC 4 Phase C Current RMS	SRC 4 Ic RMS
6342	SRC 4 Neutral Current RMS	SRC 4 In RMS
6344	SRC 4 Phase A Current Magnitude	SRC 4 Ia Mag
6346	SRC 4 Phase A Current Angle	SRC 4 Ia Angle
6347	SRC 4 Phase B Current Magnitude	SRC 4 Ib Mag
6349	SRC 4 Phase B Current Angle	SRC 4 Ib Angle
6350	SRC 4 Phase C Current Magnitude	SRC 4 Ic Mag
6352	SRC 4 Phase C Current Angle	SRC 4 Ic Angle
6353	SRC 4 Neutral Current Magnitude	SRC 4 In Mag
6355	SRC 4 Neutral Current Angle	SRC 4 In Angle
6356	SRC 4 Ground Current RMS	SRC 4 Ig RMS
6358	SRC 4 Ground Current Magnitude	SRC 4 Ig Mag
6360	SRC 4 Ground Current Angle	SRC 4 Ig Angle
6361	SRC 4 Zero Seq. Current Magnitude	SRC 4 I ₀ Mag
6363	SRC 4 Zero Seq. Current Angle	SRC 4 I ₀ Angle
6364	SRC 4 Positive Seq. Current Mag.	SRC 4 I ₁ Mag
6366	SRC 4 Positive Seq. Current Angle	SRC 4 I ₁ Angle
6367	SRC 4 Negative Seq. Current Mag.	SRC 4 I ₂ Mag
6369	SRC 4 Negative Seq. Current Angle	SRC 4 I ₂ Angle
6370	SRC 4 Differential Gnd Current Mag.	SRC 4 Igd Mag
6372	SRC 4 Differential Gnd Current Angle	SRC 4 Igd Angle
6400	SRC 5 Phase A Current RMS	SRC 5 Ia RMS
6402	SRC 5 Phase B Current RMS	SRC 5 Ib RMS

A

Table A-1: FLEXANALOG DATA ITEMS (Sheet 3 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
6404	SRC 5 Phase C Current RMS	SRC 5 Ic RMS
6406	SRC 5 Neutral Current RMS	SRC 5 In RMS
6408	SRC 5 Phase A Current Magnitude	SRC 5 Ia Mag
6410	SRC 5 Phase A Current Angle	SRC 5 Ia Angle
6411	SRC 5 Phase B Current Magnitude	SRC 5 Ib Mag
6413	SRC 5 Phase B Current Angle	SRC 5 Ib Angle
6414	SRC 5 Phase C Current Magnitude	SRC 5 Ic Mag
6416	SRC 5 Phase C Current Angle	SRC 5 Ic Angle
6417	SRC 5 Neutral Current Magnitude	SRC 5 In Mag
6419	SRC 5 Neutral Current Angle	SRC 5 In Angle
6420	SRC 5 Ground Current RMS	SRC 5 Ig RMS
6422	SRC 5 Ground Current Magnitude	SRC 5 Ig Mag
6424	SRC 5 Ground Current Angle	SRC 5 Ig Angle
6425	SRC 5 Zero Seq. Current Magnitude	SRC 5 I_0 Mag
6427	SRC 5 Zero Sequence Current Angle	SRC 5 I_0 Angle
6428	SRC 5 Positive Seq. Current Mag.	SRC 5 I_1 Mag
6430	SRC 5 Positive Seq. Current Angle	SRC 5 I_1 Angle
6431	SRC 5 Negative Seq. Current Mag.	SRC 5 I_2 Mag
6433	SRC 5 Negative Seq. Current Angle	SRC 5 I_2 Angle
6434	SRC 5 Differential Gnd Current Mag.	SRC 5 Igd Mag
6436	SRC 5 Differential Gnd Current Angle	SRC 5 Igd Angle
6464	SRC 6 Phase A Current RMS	SRC 6 Ia RMS
6466	SRC 6 Phase B Current RMS	SRC 6 Ib RMS
6468	SRC 6 Phase C Current RMS	SRC 6 Ic RMS
6470	SRC 6 Neutral Current RMS	SRC 6 In RMS
6472	SRC 6 Phase A Current Magnitude	SRC 6 Ia Mag
6474	SRC 6 Phase A Current Angle	SRC 6 Ia Angle
6475	SRC 6 Phase B Current Magnitude	SRC 6 Ib Mag
6477	SRC 6 Phase B Current Angle	SRC 6 Ib Angle
6478	SRC 6 Phase C Current Magnitude	SRC 6 Ic Mag
6480	SRC 6 Phase C Current Angle	SRC 6 Ic Angle
6481	SRC 6 Neutral Current Magnitude	SRC 6 In Mag
6483	SRC 6 Neutral Current Angle	SRC 6 In Angle
6484	SRC 6 Ground Current RMS	SRC 6 Ig RMS
6486	SRC 6 Ground Current Magnitude	SRC 6 Ig Mag
6488	SRC 6 Ground Current Angle	SRC 6 Ig Angle
6489	SRC 6 Zero Seq. Current Magnitude	SRC 6 I_0 Mag
6491	SRC 6 Zero Sequence Current Angle	SRC 6 I_0 Angle
6492	SRC 6 Positive Seq. Current Mag.	SRC 6 I_1 Mag
6494	SRC 6 Positive Seq. Current Angle	SRC 6 I_1 Angle
6495	SRC 6 Negative Seq. Current Mag.	SRC 6 I_2 Mag
6497	SRC 6 Negative Seq. Current Angle	SRC 6 I_2 Angle
6498	SRC 6 Differential Gnd Current Mag.	SRC 6 Igd Mag
6500	SRC 6 Differential Gnd Current Angle	SRC 6 Igd Angle
6656	SRC 1 Phase AG Voltage RMS	SRC 1 Vag RMS
6658	SRC 1 Phase BG Voltage RMS	SRC 1 Vbg RMS
6660	SRC 1 Phase CG Voltage RMS	SRC 1 Vcg RMS
6662	SRC 1 Phase AG Voltage Magnitude	SRC 1 Vag Mag
6664	SRC 1 Phase AG Voltage Angle	SRC 1 Vag Angle
6665	SRC 1 Phase BG Voltage Magnitude	SRC 1 Vbg Mag
6667	SRC 1 Phase BG Voltage Angle	SRC 1 Vbg Angle
6668	SRC 1 Phase CG Voltage Magnitude	SRC 1 Vcg Mag

Table A-1: FLEXANALOG DATA ITEMS (Sheet 4 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
6670	SRC 1 Phase CG Voltage Angle	SRC 1 Vcg Angle
6671	SRC 1 Phase AB Voltage RMS	SRC 1 Vab RMS
6673	SRC 1 Phase BC Voltage RMS	SRC 1 Vbc RMS
6675	SRC 1 Phase CA Voltage RMS	SRC 1 Vca RMS
6677	SRC 1 Phase AB Voltage Magnitude	SRC 1 Vab Mag
6679	SRC 1 Phase AB Voltage Angle	SRC 1 Vab Angle
6680	SRC 1 Phase BC Voltage Magnitude	SRC 1 Vbc Mag
6682	SRC 1 Phase BC Voltage Angle	SRC 1 Vbc Angle
6683	SRC 1 Phase CA Voltage Magnitude	SRC 1 Vca Mag
6685	SRC 1 Phase CA Voltage Angle	SRC 1 Vca Angle
6686	SRC 1 Auxiliary Voltage RMS	SRC 1 Vx RMS
6688	SRC 1 Auxiliary Voltage Magnitude	SRC 1 Vx Mag
6690	SRC 1 Auxiliary Voltage Angle	SRC 1 Vx Angle
6691	SRC 1 Zero Sequence Voltage Mag.	SRC 1 V_0 Mag
6693	SRC 1 Zero Sequence Voltage Angle	SRC 1 V_0 Angle
6694	SRC 1 Positive Seq. Voltage Mag.	SRC 1 V_1 Mag
6696	SRC 1 Positive Seq. Voltage Angle	SRC 1 V_1 Angle
6697	SRC 1 Negative Seq. Voltage Mag.	SRC 1 V_2 Mag
6699	SRC 1 Negative Seq. Voltage Angle	SRC 1 V_2 Angle
6720	SRC 2 Phase AG Voltage RMS	SRC 2 Vag RMS
6722	SRC 2 Phase BG Voltage RMS	SRC 2 Vbg RMS
6724	SRC 2 Phase CG Voltage RMS	SRC 2 Vcg RMS
6726	SRC 2 Phase AG Voltage Magnitude	SRC 2 Vag Mag
6728	SRC 2 Phase AG Voltage Angle	SRC 2 Vag Angle
6729	SRC 2 Phase BG Voltage Magnitude	SRC 2 Vbg Mag
6731	SRC 2 Phase BG Voltage Angle	SRC 2 Vbg Angle
6732	SRC 2 Phase CG Voltage Magnitude	SRC 2 Vcg Mag
6734	SRC 2 Phase CG Voltage Angle	SRC 2 Vcg Angle
6735	SRC 2 Phase AB Voltage RMS	SRC 2 Vab RMS
6737	SRC 2 Phase BC Voltage RMS	SRC 2 Vbc RMS
6739	SRC 2 Phase CA Voltage RMS	SRC 2 Vca RMS
6741	SRC 2 Phase AB Voltage Magnitude	SRC 2 Vab Mag
6743	SRC 2 Phase AB Voltage Angle	SRC 2 Vab Angle
6744	SRC 2 Phase BC Voltage Magnitude	SRC 2 Vbc Mag
6746	SRC 2 Phase BC Voltage Angle	SRC 2 Vbc Angle
6747	SRC 2 Phase CA Voltage Magnitude	SRC 2 Vca Mag
6749	SRC 2 Phase CA Voltage Angle	SRC 2 Vca Angle
6750	SRC 2 Auxiliary Voltage RMS	SRC 2 Vx RMS
6752	SRC 2 Auxiliary Voltage Magnitude	SRC 2 Vx Mag
6754	SRC 2 Auxiliary Voltage Angle	SRC 2 Vx Angle
6755	SRC 2 Zero Seq. Voltage Magnitude	SRC 2 V_0 Mag
6757	SRC 2 Zero Sequence Voltage Angle	SRC 2 V_0 Angle
6758	SRC 2 Positive Seq. Voltage Mag.	SRC 2 V_1 Mag
6760	SRC 2 Positive Seq. Voltage Angle	SRC 2 V_1 Angle
6761	SRC 2 Negative Seq. Voltage Mag.	SRC 2 V_2 Mag
6763	SRC 2 Negative Seq. Voltage Angle	SRC 2 V_2 Angle
6784	SRC 3 Phase AG Voltage RMS	SRC 3 Vag RMS
6786	SRC 3 Phase BG Voltage RMS	SRC 3 Vbg RMS
6788	SRC 3 Phase CG Voltage RMS	SRC 3 Vcg RMS
6790	SRC 3 Phase AG Voltage Magnitude	SRC 3 Vag Mag
6792	SRC 3 Phase AG Voltage Angle	SRC 3 Vag Angle
6793	SRC 3 Phase BG Voltage Magnitude	SRC 3 Vbg Mag

Table A-1: FLEXANALOG DATA ITEMS (Sheet 5 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
6795	SRC 3 Phase BG Voltage Angle	SRC 3 Vbg Angle
6796	SRC 3 Phase CG Voltage Magnitude	SRC 3 Vcg Mag
6798	SRC 3 Phase CG Voltage Angle	SRC 3 Vcg Angle
6799	SRC 3 Phase AB Voltage RMS	SRC 3 Vab RMS
6801	SRC 3 Phase BC Voltage RMS	SRC 3 Vbc RMS
6803	SRC 3 Phase CA Voltage RMS	SRC 3 Vca RMS
6805	SRC 3 Phase AB Voltage Magnitude	SRC 3 Vab Mag
6807	SRC 3 Phase AB Voltage Angle	SRC 3 Vab Angle
6808	SRC 3 Phase BC Voltage Magnitude	SRC 3 Vbc Mag
6810	SRC 3 Phase BC Voltage Angle	SRC 3 Vbc Angle
6811	SRC 3 Phase CA Voltage Magnitude	SRC 3 Vca Mag
6813	SRC 3 Phase CA Voltage Angle	SRC 3 Vca Angle
6814	SRC 3 Auxiliary Voltage RMS	SRC 3 Vx RMS
6816	SRC 3 Auxiliary Voltage Magnitude	SRC 3 Vx Mag
6818	SRC 3 Auxiliary Voltage Angle	SRC 3 Vx Angle
6819	SRC 3 Zero Seq. Voltage Magnitude	SRC 3 V_0 Mag
6821	SRC 3 Zero Sequence Voltage Angle	SRC 3 V_0 Angle
6822	SRC 3 Positive Seq. Voltage Mag.	SRC 3 V_1 Mag
6824	SRC 3 Positive Seq. Voltage Angle	SRC 3 V_1 Angle
6825	SRC 3 Negative Seq. Voltage Mag.	SRC 3 V_2 Mag
6827	SRC 3 Negative Seq. Voltage Angle	SRC 3 V_2 Angle
6848	SRC 4 Phase AG Voltage RMS	SRC 4 Vag RMS
6850	SRC 4 Phase BG Voltage RMS	SRC 4 Vbg RMS
6852	SRC 4 Phase CG Voltage RMS	SRC 4 Vcg RMS
6854	SRC 4 Phase AG Voltage Magnitude	SRC 4 Vag Mag
6856	SRC 4 Phase AG Voltage Angle	SRC 4 Vag Angle
6857	SRC 4 Phase BG Voltage Magnitude	SRC 4 Vbg Mag
6859	SRC 4 Phase BG Voltage Angle	SRC 4 Vbg Angle
6860	SRC 4 Phase CG Voltage Magnitude	SRC 4 Vcg Mag
6862	SRC 4 Phase CG Voltage Angle	SRC 4 Vcg Angle
6863	SRC 4 Phase AB Voltage RMS	SRC 4 Vab RMS
6865	SRC 4 Phase BC Voltage RMS	SRC 4 Vbc RMS
6867	SRC 4 Phase CA Voltage RMS	SRC 4 Vca RMS
6869	SRC 4 Phase AB Voltage Magnitude	SRC 4 Vab Mag
6871	SRC 4 Phase AB Voltage Angle	SRC 4 Vab Angle
6872	SRC 4 Phase BC Voltage Magnitude	SRC 4 Vbc Mag
6874	SRC 4 Phase BC Voltage Angle	SRC 4 Vbc Angle
6875	SRC 4 Phase CA Voltage Magnitude	SRC 4 Vca Mag
6877	SRC 4 Phase CA Voltage Angle	SRC 4 Vca Angle
6878	SRC 4 Auxiliary Voltage RMS	SRC 4 Vx RMS
6880	SRC 4 Auxiliary Voltage Magnitude	SRC 4 Vx Mag
6882	SRC 4 Auxiliary Voltage Angle	SRC 4 Vx Angle
6883	SRC 4 Zero Seq. Voltage Magnitude	SRC 4 V_0 Mag
6885	SRC 4 Zero Sequence Voltage Angle	SRC 4 V_0 Angle
6886	SRC 4 Positive Seq. Voltage Mag.	SRC 4 V_1 Mag
6888	SRC 4 Positive Seq. Voltage Angle	SRC 4 V_1 Angle
6889	SRC 4 Negative Seq. Voltage Mag.	SRC 4 V_2 Mag
6891	SRC 4 Negative Seq. Voltage Angle	SRC 4 V_2 Angle
6912	SRC 5 Phase AG Voltage RMS	SRC 5 Vag RMS
6914	SRC 5 Phase BG Voltage RMS	SRC 5 Vbg RMS
6916	SRC 5 Phase CG Voltage RMS	SRC 5 Vcg RMS
6918	SRC 5 Phase AG Voltage Magnitude	SRC 5 Vag Mag

Table A-1: FLEXANALOG DATA ITEMS (Sheet 6 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
6920	SRC 5 Phase AG Voltage Angle	SRC 5 Vag Angle
6921	SRC 5 Phase BG Voltage Magnitude	SRC 5 Vbg Mag
6923	SRC 5 Phase BG Voltage Angle	SRC 5 Vbg Angle
6924	SRC 5 Phase CG Voltage Magnitude	SRC 5 Vcg Mag
6926	SRC 5 Phase CG Voltage Angle	SRC 5 Vcg Angle
6927	SRC 5 Phase AB Voltage RMS	SRC 5 Vab RMS
6929	SRC 5 Phase BC Voltage RMS	SRC 5 Vbc RMS
6931	SRC 5 Phase CA Voltage RMS	SRC 5 Vca RMS
6933	SRC 5 Phase AB Voltage Magnitude	SRC 5 Vab Mag
6935	SRC 5 Phase AB Voltage Angle	SRC 5 Vab Angle
6936	SRC 5 Phase BC Voltage Magnitude	SRC 5 Vbc Mag
6938	SRC 5 Phase BC Voltage Angle	SRC 5 Vbc Angle
6939	SRC 5 Phase CA Voltage Magnitude	SRC 5 Vca Mag
6941	SRC 5 Phase CA Voltage Angle	SRC 5 Vca Angle
6942	SRC 5 Auxiliary Voltage RMS	SRC 5 Vx RMS
6944	SRC 5 Auxiliary Voltage Magnitude	SRC 5 Vx Mag
6946	SRC 5 Auxiliary Voltage Angle	SRC 5 Vx Angle
6947	SRC 5 Zero Seq. Voltage Magnitude	SRC 5 V_0 Mag
6949	SRC 5 Zero Sequence Voltage Angle	SRC 5 V_0 Angle
6950	SRC 5 Positive Seq. Voltage Mag.	SRC 5 V_1 Mag
6952	SRC 5 Positive Seq. Voltage Angle	SRC 5 V_1 Angle
6953	SRC 5 Negative Seq. Voltage Mag.	SRC 5 V_2 Mag
6955	SRC 5 Negative Seq. Voltage Angle	SRC 5 V_2 Angle
6976	SRC 6 Phase AG Voltage RMS	SRC 6 Vag RMS
6978	SRC 6 Phase BG Voltage RMS	SRC 6 Vbg RMS
6980	SRC 6 Phase CG Voltage RMS	SRC 6 Vcg RMS
6982	SRC 6 Phase AG Voltage Magnitude	SRC 6 Vag Mag
6984	SRC 6 Phase AG Voltage Angle	SRC 6 Vag Angle
6985	SRC 6 Phase BG Voltage Magnitude	SRC 6 Vbg Mag
6987	SRC 6 Phase BG Voltage Angle	SRC 6 Vbg Angle
6988	SRC 6 Phase CG Voltage Magnitude	SRC 6 Vcg Mag
6990	SRC 6 Phase CG Voltage Angle	SRC 6 Vcg Angle
6991	SRC 6 Phase AB Voltage RMS	SRC 6 Vab RMS
6993	SRC 6 Phase BC Voltage RMS	SRC 6 Vbc RMS
6995	SRC 6 Phase CA Voltage RMS	SRC 6 Vca RMS
6997	SRC 6 Phase AB Voltage Magnitude	SRC 6 Vab Mag
6999	SRC 6 Phase AB Voltage Angle	SRC 6 Vab Angle
7000	SRC 6 Phase BC Voltage Magnitude	SRC 6 Vbc Mag
7002	SRC 6 Phase BC Voltage Angle	SRC 6 Vbc Angle
7003	SRC 6 Phase CA Voltage Magnitude	SRC 6 Vca Mag
7005	SRC 6 Phase CA Voltage Angle	SRC 6 Vca Angle
7006	SRC 6 Auxiliary Voltage RMS	SRC 6 Vx RMS
7008	SRC 6 Auxiliary Voltage Magnitude	SRC 6 Vx Mag
7010	SRC 6 Auxiliary Voltage Angle	SRC 6 Vx Angle
7011	SRC 6 Zero Seq. Voltage Magnitude	SRC 6 V_0 Mag
7013	SRC 6 Zero Sequence Voltage Angle	SRC 6 V_0 Angle
7014	SRC 6 Positive Seq. Voltage Mag.	SRC 6 V_1 Mag
7016	SRC 6 Positive Seq. Voltage Angle	SRC 6 V_1 Angle
7017	SRC 6 Negative Seq. Voltage Mag.	SRC 6 V_2 Mag
7019	SRC 6 Negative Seq. Voltage Angle	SRC 6 V_2 Angle
7168	SRC 1 Three Phase Real Power	SRC 1 P
7170	SRC 1 Phase A Real Power	SRC 1 Pa

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Table A-1: FLEXANALOG DATA ITEMS (Sheet 7 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
7172	SRC 1 Phase B Real Power	SRC 1 Pb
7174	SRC 1 Phase C Real Power	SRC 1 Pc
7176	SRC 1 Three Phase Reactive Power	SRC 1 Q
7178	SRC 1 Phase A Reactive Power	SRC 1 Qa
7180	SRC 1 Phase B Reactive Power	SRC 1 Qb
7182	SRC 1 Phase C Reactive Power	SRC 1 Qc
7184	SRC 1 Three Phase Apparent Power	SRC 1 S
7186	SRC 1 Phase A Apparent Power	SRC 1 Sa
7188	SRC 1 Phase B Apparent Power	SRC 1 Sb
7190	SRC 1 Phase C Apparent Power	SRC 1 Sc
7192	SRC 1 Three Phase Power Factor	SRC 1 PF
7193	SRC 1 Phase A Power Factor	SRC 1 Phase A PF
7194	SRC 1 Phase B Power Factor	SRC 1 Phase B PF
7195	SRC 1 Phase C Power Factor	SRC 1 Phase C PF
7200	SRC 2 Three Phase Real Power	SRC 2 P
7202	SRC 2 Phase A Real Power	SRC 2 Pa
7204	SRC 2 Phase B Real Power	SRC 2 Pb
7206	SRC 2 Phase C Real Power	SRC 2 Pc
7208	SRC 2 Three Phase Reactive Power	SRC 2 Q
7210	SRC 2 Phase A Reactive Power	SRC 2 Qa
7212	SRC 2 Phase B Reactive Power	SRC 2 Qb
7214	SRC 2 Phase C Reactive Power	SRC 2 Qc
7216	SRC 2 Three Phase Apparent Power	SRC 2 S
7218	SRC 2 Phase A Apparent Power	SRC 2 Sa
7220	SRC 2 Phase B Apparent Power	SRC 2 Sb
7222	SRC 2 Phase C Apparent Power	SRC 2 Sc
7224	SRC 2 Three Phase Power Factor	SRC 2 PF
7225	SRC 2 Phase A Power Factor	SRC 2 Phase A PF
7226	SRC 2 Phase B Power Factor	SRC 2 Phase B PF
7227	SRC 2 Phase C Power Factor	SRC 2 Phase C PF
7232	SRC 3 Three Phase Real Power	SRC 3 P
7234	SRC 3 Phase A Real Power	SRC 3 Pa
7236	SRC 3 Phase B Real Power	SRC 3 Pb
7238	SRC 3 Phase C Real Power	SRC 3 Pc
7240	SRC 3 Three Phase Reactive Power	SRC 3 Q
7242	SRC 3 Phase A Reactive Power	SRC 3 Qa
7244	SRC 3 Phase B Reactive Power	SRC 3 Qb
7246	SRC 3 Phase C Reactive Power	SRC 3 Qc
7248	SRC 3 Three Phase Apparent Power	SRC 3 S
7250	SRC 3 Phase A Apparent Power	SRC 3 Sa
7252	SRC 3 Phase B Apparent Power	SRC 3 Sb
7254	SRC 3 Phase C Apparent Power	SRC 3 Sc
7256	SRC 3 Three Phase Power Factor	SRC 3 PF
7257	SRC 3 Phase A Power Factor	SRC 3 Phase A PF
7258	SRC 3 Phase B Power Factor	SRC 3 Phase B PF
7259	SRC 3 Phase C Power Factor	SRC 3 Phase C PF
7264	SRC 4 Three Phase Real Power	SRC 4 P
7266	SRC 4 Phase A Real Power	SRC 4 Pa
7268	SRC 4 Phase B Real Power	SRC 4 Pb
7270	SRC 4 Phase C Real Power	SRC 4 Pc
7272	SRC 4 Three Phase Reactive Power	SRC 4 Q
7274	SRC 4 Phase A Reactive Power	SRC 4 Qa

Table A-1: FLEXANALOG DATA ITEMS (Sheet 8 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
7276	SRC 4 Phase B Reactive Power	SRC 4 Qb
7278	SRC 4 Phase C Reactive Power	SRC 4 Qc
7280	SRC 4 Three Phase Apparent Power	SRC 4 S
7282	SRC 4 Phase A Apparent Power	SRC 4 Sa
7284	SRC 4 Phase B Apparent Power	SRC 4 Sb
7286	SRC 4 Phase C Apparent Power	SRC 4 Sc
7288	SRC 4 Three Phase Power Factor	SRC 4 PF
7289	SRC 4 Phase A Power Factor	SRC 4 Phase A PF
7290	SRC 4 Phase B Power Factor	SRC 4 Phase B PF
7291	SRC 4 Phase C Power Factor	SRC 4 Phase C PF
7296	SRC 5 Three Phase Real Power	SRC 5 P
7298	SRC 5 Phase A Real Power	SRC 5 Pa
7300	SRC 5 Phase B Real Power	SRC 5 Pb
7302	SRC 5 Phase C Real Power	SRC 5 Pc
7304	SRC 5 Three Phase Reactive Power	SRC 5 Q
7306	SRC 5 Phase A Reactive Power	SRC 5 Qa
7308	SRC 5 Phase B Reactive Power	SRC 5 Qb
7310	SRC 5 Phase C Reactive Power	SRC 5 Qc
7312	SRC 5 Three Phase Apparent Power	SRC 5 S
7314	SRC 5 Phase A Apparent Power	SRC 5 Sa
7316	SRC 5 Phase B Apparent Power	SRC 5 Sb
7318	SRC 5 Phase C Apparent Power	SRC 5 Sc
7320	SRC 5 Three Phase Power Factor	SRC 5 PF
7321	SRC 5 Phase A Power Factor	SRC 5 Phase A PF
7322	SRC 5 Phase B Power Factor	SRC 5 Phase B PF
7323	SRC 5 Phase C Power Factor	SRC 5 Phase C PF
7328	SRC 6 Three Phase Real Power	SRC 6 P
7330	SRC 6 Phase A Real Power	SRC 6 Pa
7332	SRC 6 Phase B Real Power	SRC 6 Pb
7334	SRC 6 Phase C Real Power	SRC 6 Pc
7336	SRC 6 Three Phase Reactive Power	SRC 6 Q
7338	SRC 6 Phase A Reactive Power	SRC 6 Qa
7340	SRC 6 Phase B Reactive Power	SRC 6 Qb
7342	SRC 6 Phase C Reactive Power	SRC 6 Qc
7344	SRC 6 Three Phase Apparent Power	SRC 6 S
7346	SRC 6 Phase A Apparent Power	SRC 6 Sa
7348	SRC 6 Phase B Apparent Power	SRC 6 Sb
7350	SRC 6 Phase C Apparent Power	SRC 6 Sc
7352	SRC 6 Three Phase Power Factor	SRC 6 PF
7353	SRC 6 Phase A Power Factor	SRC 6 Phase A PF
7354	SRC 6 Phase B Power Factor	SRC 6 Phase B PF
7355	SRC 6 Phase C Power Factor	SRC 6 Phase C PF
7552	SRC 1 Frequency	SRC 1 Frequency
7553	SRC 2 Frequency	SRC 2 Frequency
7554	SRC 3 Frequency	SRC 3 Frequency
7555	SRC 4 Frequency	SRC 4 Frequency
7556	SRC 5 Frequency	SRC 5 Frequency
7557	SRC 6 Frequency	SRC 6 Frequency
7680	SRC 1 Demand Ia	SRC 1 Demand Ia
7682	SRC 1 Demand Ib	SRC 1 Demand Ib
7684	SRC 1 Demand Ic	SRC 1 Demand Ic
7686	SRC 1 Demand Watt	SRC 1 Demand Watt

Table A-1: FLEXANALOG DATA ITEMS (Sheet 9 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
7688	SRC 1 Demand Var	SRC 1 Demand var
7690	SRC 1 Demand Va	SRC 1 Demand Va
7696	SRC 2 Demand Ia	SRC 2 Demand Ia
7698	SRC 2 Demand Ib	SRC 2 Demand Ib
7700	SRC 2 Demand Ic	SRC 2 Demand Ic
7702	SRC 2 Demand Watt	SRC 2 Demand Watt
7704	SRC 2 Demand Var	SRC 2 Demand var
7706	SRC 2 Demand Va	SRC 2 Demand Va
7712	SRC 3 Demand Ia	SRC 3 Demand Ia
7714	SRC 3 Demand Ib	SRC 3 Demand Ib
7716	SRC 3 Demand Ic	SRC 3 Demand Ic
7718	SRC 3 Demand Watt	SRC 3 Demand Watt
7720	SRC 3 Demand Var	SRC 3 Demand var
7722	SRC 3 Demand Va	SRC 3 Demand Va
7728	SRC 4 Demand Ia	SRC 4 Demand Ia
7730	SRC 4 Demand Ib	SRC 4 Demand Ib
7732	SRC 4 Demand Ic	SRC 4 Demand Ic
7734	SRC 4 Demand Watt	SRC 4 Demand Watt
7736	SRC 4 Demand Var	SRC 4 Demand var
7738	SRC 4 Demand Va	SRC 4 Demand Va
7744	SRC 5 Demand Ia	SRC 5 Demand Ia
7746	SRC 5 Demand Ib	SRC 5 Demand Ib
7748	SRC 5 Demand Ic	SRC 5 Demand Ic
7750	SRC 5 Demand Watt	SRC 5 Demand Watt
7752	SRC 5 Demand Var	SRC 5 Demand var
7754	SRC 5 Demand Va	SRC 5 Demand Va
7760	SRC 6 Demand Ia	SRC 6 Demand Ia
7762	SRC 6 Demand Ib	SRC 6 Demand Ib
7764	SRC 6 Demand Ic	SRC 6 Demand Ic
7766	SRC 6 Demand Watt	SRC 6 Demand Watt
7768	SRC 6 Demand Var	SRC 6 Demand var
7770	SRC 6 Demand Va	SRC 6 Demand Va
9216	Synchrocheck 1 Delta Voltage	Synchchk 1 Delta V
9218	Synchrocheck 1 Delta Frequency	Synchchk 1 Delta F
9219	Synchrocheck 1 Delta Phase	Synchchk 1 Delta Phs
9220	Synchrocheck 2 Delta Voltage	Synchchk 2 Delta V
9222	Synchrocheck 2 Delta Frequency	Synchchk 2 Delta F
9223	Synchrocheck 2 Delta Phase	Synchchk 2 Delta Phs
9536	PMU 1 Voltage Magnitude	PMU 1 Va Mag
9538	PMU 1 Phase A Voltage Angle	PMU 1 Va Angle
9539	PMU 1 Phase B Voltage Magnitude	PMU 1 Vb Mag
9541	PMU 1 Phase B Voltage Angle	PMU 1 Vb Angle
9542	PMU 1 Phase C Voltage Magnitude	PMU 1 Vc Mag
9544	PMU 1 Phase C Voltage Angle	PMU 1 Vc Angle
9545	PMU 1 Auxiliary Voltage Magnitude	PMU 1 Vx Mag
9547	PMU 1 Auxiliary Voltage Angle	PMU 1 Vx Angle
9548	PMU 1 Pos. Seq. Voltage Magnitude	PMU 1 V1 Mag
9550	PMU 1 Pos. Seq. Voltage Angle	PMU 1 V1 Angle
9551	PMU 1 Neg. Seq. Voltage Magnitude	PMU 1 V2 Mag
9553	PMU 1 Negative Seq. Voltage Angle	PMU 1 V2 Angle
9554	PMU 1 Zero Seq. Voltage Magnitude	PMU 1 V0 Mag
9556	PMU 1 Zero Sequence Voltage Angle	PMU 1 V0 Angle

Table A-1: FLEXANALOG DATA ITEMS (Sheet 10 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
9557	PMU 1 Phase A Current Magnitude	PMU 1 Ia Mag
9559	PMU 1 Phase A Current Angle	PMU 1 Ia Angle
9560	PMU 1 Phase B Current Magnitude	PMU 1 Ib Mag
9562	PMU 1 Phase B Current Angle	PMU 1 Ib Angle
9563	PMU 1 Phase C Current Magnitude	PMU 1 Ic Mag
9565	PMU 1 Phase C Current Angle	PMU 1 Ic Angle
9566	PMU 1 Ground Current Magnitude	PMU 1 Ig Mag
9568	PMU 1 Ground Current Angle	PMU 1 Ig Angle
9569	PMU 1 Pos. Seq. Current Magnitude	PMU 1 I1 Mag
9571	PMU 1 Positive Seq. Current Angle	PMU 1 I1 Angle
9572	PMU 1 Neg. Seq. Current Magnitude	PMU 1 I2 Mag
9574	PMU 1 Negative Seq. Current Angle	PMU 1 I2 Angle
9575	PMU 1 Zero Seq. Current Magnitude	PMU 1 I0 Mag
9577	PMU 1 Zero Sequence Current Angle	PMU 1 I0 Angle
9578	PMU 1 Frequency	PMU 1 Freq
9580	PMU 1 df/dt	PMU 1 df dt
9581	PMU 1 Config Change Counter	PMU 1 Conf Ch
9586	PMU 2 Phase A Voltage Magnitude	PMU 2 Va Mag
9588	PMU 2 Phase A Voltage Angle	PMU 2 Va Angle
9589	PMU 2 Phase B Voltage Magnitude	PMU 2 Vb Mag
9591	PMU 2 Phase B Voltage Angle	PMU 2 Vb Angle
9592	PMU 2 Phase C Voltage Magnitude	PMU 2 Vc Mag
9594	PMU 2 Phase C Voltage Angle	PMU 2 Vc Angle
9595	PMU 2 Auxiliary Voltage Magnitude	PMU 2 Vx Mag
9597	PMU 2 Auxiliary Voltage Angle	PMU 2 Vx Angle
9598	PMU 2 Pos. Seq. Voltage Magnitude	PMU 2 V1 Mag
9600	PMU 2 Positive Seq. Voltage Angle	PMU 2 V1 Angle
9601	PMU 2 Neg. Seq. Voltage Magnitude	PMU 2 V2 Mag
9603	PMU 2 Negative Seq. Voltage Angle	PMU 2 V2 Angle
9604	PMU 2 Zero Seq. Voltage Magnitude	PMU 2 V0 Mag
9606	PMU 2 Zero Sequence Voltage Angle	PMU 2 V0 Angle
9607	PMU 2 Phase A Current Magnitude	PMU 2 Ia Mag
9609	PMU 2 Phase A Current Angle	PMU 2 Ia Angle
9610	PMU 2 Phase B Current Magnitude	PMU 2 Ib Mag
9612	PMU 2 Phase B Current Angle	PMU 2 Ib Angle
9613	PMU 2 Phase C Current Magnitude	PMU 2 Ic Mag
9615	PMU 2 Phase C Current Angle	PMU 2 Ic Angle
9616	PMU 2 Ground Current Magnitude	PMU 2 Ig Mag
9618	PMU 2 Ground Current Angle	PMU 2 Ig Angle
9619	PMU 2 Pos. Seq. Current Magnitude	PMU 2 I1 Mag
9621	PMU 2 Positive Seq. Current Angle	PMU 2 I1 Angle
9622	PMU 2 Neg. Seq. Current Magnitude	PMU 2 I2 Mag
9624	PMU 2 Negative Seq. Current Angle	PMU 2 I2 Angle
9625	PMU 2 Zero Seq. Current Magnitude	PMU 2 I0 Mag
9627	PMU 2 Zero Sequence Current Angle	PMU 2 I0 Angle
9628	PMU 2 Frequency	PMU 2 Freq
9630	PMU 2 df/dt	PMU 2 df dt
9631	PMU 2 Config Change Counter	PMU 2 Conf Ch
9636	PMU 3 Phase A Voltage Magnitude	PMU 3 Va Mag
9638	PMU 3 Phase A Voltage Angle	PMU 3 Va Angle
9639	PMU 3 Phase B Voltage Magnitude	PMU 3 Vb Mag
9641	PMU 3 Phase B Voltage Angle	PMU 3 Vb Angle

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Table A-1: FLEXANALOG DATA ITEMS (Sheet 11 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
9642	PMU 3 Phase C Voltage Magnitude	PMU 3 Vc Mag
9644	PMU 3 Phase C Voltage Angle	PMU 3 Vc Angle
9645	PMU 3 Auxiliary Voltage Magnitude	PMU 3 Vx Mag
9647	PMU 3 Auxiliary Voltage Angle	PMU 3 Vx Angle
9648	PMU 3 Pos. Seq. Voltage Magnitude	PMU 3 V1 Mag
9650	PMU 3 Positive Seq. Voltage Angle	PMU 3 V1 Angle
9651	PMU 3 Neg. Seq. Voltage Magnitude	PMU 3 V2 Mag
9653	PMU 3 Negative Seq. Voltage Angle	PMU 3 V2 Angle
9654	PMU 3 Zero Seq. Voltage Magnitude	PMU 3 V0 Mag
9656	PMU 3 Zero Sequence Voltage Angle	PMU 3 V0 Angle
9657	PMU 3 Phase A Current Magnitude	PMU 3 Ia Mag
9659	PMU 3 Phase A Current Angle	PMU 3 Ia Angle
9660	PMU 3 Phase B Current Magnitude	PMU 3 Ib Mag
9662	PMU 3 Phase B Current Angle	PMU 3 Ib Angle
9663	PMU 3 Phase C Current Magnitude	PMU 3 Ic Mag
9665	PMU 3 Phase C Current Angle	PMU 3 Ic Angle
9666	PMU 3 Ground Current Magnitude	PMU 3 Ig Mag
9668	PMU 3 Ground Current Angle	PMU 3 Ig Angle
9669	PMU 3 Pos. Seq. Current Magnitude	PMU 3 I1 Mag
9671	PMU 3 Positive Seq. Current Angle	PMU 3 I1 Angle
9672	PMU 3 Neg. Seq. Current Magnitude	PMU 3 I2 Mag
9674	PMU 3 Negative Seq. Current Angle	PMU 3 I2 Angle
9675	PMU 3 Zero Seq. Current Magnitude	PMU 3 I0 Mag
9677	PMU 3 Zero Sequence Current Angle	PMU 3 I0 Angle
9678	PMU 3 Frequency	PMU 3 Freq
9680	PMU 3 df/dt	PMU 3 df dt
9681	PMU 3 Config Change Counter	PMU 3 Conf Ch
9686	PMU 4 Phase A Voltage Magnitude	PMU 4 Va Mag
9688	PMU 4 Phase A Voltage Angle	PMU 4 Va Angle
9689	PMU 4 Phase B Voltage Magnitude	PMU 4 Vb Mag
9691	PMU 4 Phase B Voltage Angle	PMU 4 Vb Angle
9692	PMU 4 Phase C Voltage Magnitude	PMU 4 Vc Mag
9694	PMU 4 Phase C Voltage Angle	PMU 4 Vc Angle
9695	PMU 4 Auxiliary Voltage Magnitude	PMU 4 Vx Mag
9697	PMU 4 Auxiliary Voltage Angle	PMU 4 Vx Angle
9698	PMU 4 Pos. Seq. Voltage Magnitude	PMU 4 V1 Mag
9700	PMU 4 Positive Seq. Voltage Angle	PMU 4 V1 Angle
9701	PMU 4 Neg. Seq. Voltage Magnitude	PMU 4 V2 Mag
9703	PMU 4 Negative Seq. Voltage Angle	PMU 4 V2 Angle
9704	PMU 4 Zero Seq. Voltage Magnitude	PMU 4 V0 Mag
9706	PMU 4 Zero Sequence Voltage Angle	PMU 4 V0 Angle
9707	PMU 4 Phase A Current Magnitude	PMU 4 Ia Mag
9709	PMU 4 Phase A Current Angle	PMU 4 Ia Angle
9710	PMU 4 Phase B Current Magnitude	PMU 4 Ib Mag
9712	PMU 4 Phase B Current Angle	PMU 4 Ib Angle
9713	PMU 4 Phase C Current Magnitude	PMU 4 Ic Mag
9715	PMU 4 Phase C Current Angle	PMU 4 Ic Angle
9716	PMU 4 Ground Current Magnitude	PMU 4 Ig Mag
9718	PMU 4 Ground Current Angle	PMU 4 Ig Angle
9719	PMU 4 Pos. Seq. Current Magnitude	PMU 4 I1 Mag
9721	PMU 4 Positive Seq. Current Angle	PMU 4 I1 Angle
9722	PMU 4 Neg. Seq. Current Magnitude	PMU 4 I2 Mag

Table A-1: FLEXANALOG DATA ITEMS (Sheet 12 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
9724	PMU 4 Negative Seq. Current Angle	PMU 4 I2 Angle
9725	PMU 4 Zero Seq. Current Magnitude	PMU 4 I0 Mag
9727	PMU 4 Zero Sequence Current Angle	PMU 4 I0 Angle
9728	PMU 4 Frequency	PMU 4 Freq
9730	PMU 4 df/dt	PMU 4 df dt
9731	PMU 4 Config Change Counter	PMU 4 Conf Ch
13504	DCMA Inputs 1 Value	DCMA Inputs 1 Value
13506	DCMA Inputs 2 Value	DCMA Inputs 2 Value
13508	DCMA Inputs 3 Value	DCMA Inputs 3 Value
13510	DCMA Inputs 4 Value	DCMA Inputs 4 Value
13512	DCMA Inputs 5 Value	DCMA Inputs 5 Value
13514	DCMA Inputs 6 Value	DCMA Inputs 6 Value
13516	DCMA Inputs 7 Value	DCMA Inputs 7 Value
13518	DCMA Inputs 8 Value	DCMA Inputs 8 Value
13520	DCMA Inputs 9 Value	DCMA Inputs 9 Value
13522	DCMA Inputs 10 Value	DCMA Inputs 10 Value
13524	DCMA Inputs 11 Value	DCMA Inputs 11 Value
13526	DCMA Inputs 12 Value	DCMA Inputs 12 Value
13528	DCMA Inputs 13 Value	DCMA Inputs 13 Value
13530	DCMA Inputs 14 Value	DCMA Inputs 14 Value
13532	DCMA Inputs 15 Value	DCMA Inputs 15 Value
13534	DCMA Inputs 16 Value	DCMA Inputs 16 Value
13536	DCMA Inputs 17 Value	DCMA Inputs 17 Value
13538	DCMA Inputs 18 Value	DCMA Inputs 18 Value
13540	DCMA Inputs 19 Value	DCMA Inputs 19 Value
13542	DCMA Inputs 20 Value	DCMA Inputs 20 Value
13544	DCMA Inputs 21 Value	DCMA Inputs 21 Value
13546	DCMA Inputs 22 Value	DCMA Inputs 22 Value
13548	DCMA Inputs 23 Value	DCMA Inputs 23 Value
13550	DCMA Inputs 24 Value	DCMA Inputs 24 Value
13552	RTD Inputs 1 Value	RTD Inputs 1 Value
13553	RTD Inputs 2 Value	RTD Inputs 2 Value
13554	RTD Inputs 3 Value	RTD Inputs 3 Value
13555	RTD Inputs 4 Value	RTD Inputs 4 Value
13556	RTD Inputs 5 Value	RTD Inputs 5 Value
13557	RTD Inputs 6 Value	RTD Inputs 6 Value
13558	RTD Inputs 7 Value	RTD Inputs 7 Value
13559	RTD Inputs 8 Value	RTD Inputs 8 Value
13560	RTD Inputs 9 Value	RTD Inputs 9 Value
13561	RTD Inputs 10 Value	RTD Inputs 10 Value
13562	RTD Inputs 11 Value	RTD Inputs 11 Value
13563	RTD Inputs 12 Value	RTD Inputs 12 Value
13564	RTD Inputs 13 Value	RTD Inputs 13 Value
13565	RTD Inputs 14 Value	RTD Inputs 14 Value
13566	RTD Inputs 15 Value	RTD Inputs 15 Value
13567	RTD Inputs 16 Value	RTD Inputs 16 Value
13568	RTD Inputs 17 Value	RTD Inputs 17 Value
13569	RTD Inputs 18 Value	RTD Inputs 18 Value
13570	RTD Inputs 19 Value	RTD Inputs 19 Value
13571	RTD Inputs 20 Value	RTD Inputs 20 Value
13572	RTD Inputs 21 Value	RTD Inputs 21 Value
13573	RTD Inputs 22 Value	RTD Inputs 22 Value

Table A-1: FLEXANALOG DATA ITEMS (Sheet 13 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
13574	RTD Inputs 23 Value	RTD Inputs 23 Value
13575	RTD Inputs 24 Value	RTD Inputs 24 Value
13576	RTD Inputs 25 Value	RTD Inputs 25 Value
13577	RTD Inputs 26 Value	RTD Inputs 26 Value
13578	RTD Inputs 27 Value	RTD Inputs 27 Value
13579	RTD Inputs 28 Value	RTD Inputs 28 Value
13580	RTD Inputs 29 Value	RTD Inputs 29 Value
13581	RTD Inputs 30 Value	RTD Inputs 30 Value
13582	RTD Inputs 31 Value	RTD Inputs 31 Value
13583	RTD Inputs 32 Value	RTD Inputs 32 Value
13584	RTD Inputs 33 Value	RTD Inputs 33 Value
13585	RTD Inputs 34 Value	RTD Inputs 34 Value
13586	RTD Inputs 35 Value	RTD Inputs 35 Value
13587	RTD Inputs 36 Value	RTD Inputs 36 Value
13588	RTD Inputs 37 Value	RTD Inputs 37 Value
13589	RTD Inputs 38 Value	RTD Inputs 38 Value
13590	RTD Inputs 39 Value	RTD Inputs 39 Value
13591	RTD Inputs 40 Value	RTD Inputs 40 Value
13592	RTD Inputs 41 Value	RTD Inputs 41 Value
13593	RTD Inputs 42 Value	RTD Inputs 42 Value
13594	RTD Inputs 43 Value	RTD Inputs 43 Value
13595	RTD Inputs 44 Value	RTD Inputs 44 Value
13596	RTD Inputs 45 Value	RTD Inputs 45 Value
13597	RTD Inputs 46 Value	RTD Inputs 46 Value
13598	RTD Inputs 47 Value	RTD Inputs 47 Value
13599	RTD Inputs 48 Value	RTD Inputs 48 Value
32768	Tracking Frequency	Tracking Frequency
33024	Comp 1 A	Comp Arg A 1
33026	Comp 1 B	Comp Arg B 1
33028	Comp 1 Out	Comp Out 1
33030	Comp 2 A	Comp Arg A 2
33032	Comp 2 B	Comp Arg B 2
33034	Comp 2 Out	Comp Out 2
33036	Comp 3 A	Comp Arg A 3
33038	Comp 3 B	Comp Arg B 3
33040	Comp 3 Out	Comp Out 3
33042	Comp 4 A	Comp Arg A 4
33044	Comp 4 B	Comp Arg B 4
33046	Comp 4 Out	Comp Out 4
33048	Comp 5 A	Comp Arg A 5
33050	Comp 5 B	Comp Arg B 5
33052	Comp 5 Out	Comp Out 5
33054	Comp 6 A	Comp Arg A 6
33056	Comp 6 B	Comp Arg B 6
33058	Comp 6 Out	Comp Out 6
33440	Digitizer 1 Output	Digitizer 1 Out
33442	Digitizer 2 Output	Digitizer 2 Out
33444	Digitizer 3 Output	Digitizer 3 Out
33446	Digitizer 4 Output	Digitizer 4 Out
33448	Digitizer 5 Output	Digitizer 5 Out
39425	FlexElement 1 Actual	FlexElement 1 Value
39427	FlexElement 2 Actual	FlexElement 2 Value

Table A-1: FLEXANALOG DATA ITEMS (Sheet 14 of 14)

ADDR	DATA ITEM	FLEXANALOG NAME
39429	FlexElement 3 Actual	FlexElement 3 Value
39431	FlexElement 4 Actual	FlexElement 4 Value
39433	FlexElement 5 Actual	FlexElement 5 Value
39435	FlexElement 6 Actual	FlexElement 6 Value
39437	FlexElement 7 Actual	FlexElement 7 Value
39439	FlexElement 8 Actual	FlexElement 8 Value
39441	FlexElement 9 Actual	FlexElement 9 Value
39443	FlexElement 10 Actual	FlexElement 10 Value
39445	FlexElement 11 Actual	FlexElement 11 Value
39447	FlexElement 12 Actual	FlexElement 12 Value
39449	FlexElement 13 Actual	FlexElement 13 Value
39451	FlexElement 14 Actual	FlexElement 14 Value
39453	FlexElement 15 Actual	FlexElement 15 Value
39455	FlexElement 16 Actual	FlexElement 16 Value
40971	Current Setting Group	Active Setting Group
61449	PMU Recording Number of Triggers	PMU Num Triggers

A

B.1.1 INTRODUCTION

The UR-series relays support a number of communications protocols to allow connection to equipment such as personal computers, RTUs, SCADA masters, and programmable logic controllers. The Modicon Modbus RTU protocol is the most basic protocol supported by the UR. Modbus is available via RS232 or RS485 serial links or via ethernet (using the Modbus/TCP specification). The following description is intended primarily for users who wish to develop their own master communication drivers and applies to the serial Modbus RTU protocol. Note that:

- The UR always acts as a slave device, meaning that it never initiates communications; it only listens and responds to requests issued by a master computer.
- For Modbus®, a subset of the Remote Terminal Unit (RTU) protocol format is supported that allows extensive monitoring, programming, and control functions using read and write register commands.

B.1.2 PHYSICAL LAYER

The Modbus® RTU protocol is hardware-independent so that the physical layer can be any of a variety of standard hardware configurations including RS232 and RS485. The relay includes a faceplate (front panel) RS232 port and two rear terminal communications ports that may be configured as RS485, fiber optic, 10BaseT, or 10BaseF. Data flow is half-duplex in all configurations. See Chapter 3 for details on wiring.

Each data byte is transmitted in an asynchronous format consisting of 1 start bit, 8 data bits, 1 stop bit, and possibly 1 parity bit. This produces a 10 or 11 bit data frame. This can be important for transmission through modems at high bit rates (11 bit data frames are not supported by many modems at baud rates greater than 300).

The baud rate and parity are independently programmable for each communications port. Baud rates of 300, 1200, 2400, 4800, 9600, 14400, 19200, 28800, 33600, 38400, 57600, or 115200 bps are available. Even, odd, and no parity are available. Refer to the *Communications* section of Chapter 5 for further details.

The master device in any system must know the address of the slave device with which it is to communicate. The relay will not act on a request from a master if the address in the request does not match the relay's slave address (unless the address is the broadcast address – see below).

A single setting selects the slave address used for all ports, with the exception that for the faceplate port, the relay will accept any address when the Modbus® RTU protocol is used.

B.1.3 DATA LINK LAYER

Communications takes place in packets which are groups of asynchronously framed byte data. The master transmits a packet to the slave and the slave responds with a packet. The end of a packet is marked by 'dead-time' on the communications line. The following describes general format for both transmit and receive packets. For exact details on packet formatting, refer to subsequent sections describing each function code.

Table B-1: MODBUS PACKET FORMAT

DESCRIPTION	SIZE
SLAVE ADDRESS	1 byte
FUNCTION CODE	1 byte
DATA	N bytes
CRC	2 bytes
DEAD TIME	3.5 bytes transmission time

- **SLAVE ADDRESS:** This is the address of the slave device that is intended to receive the packet sent by the master and to perform the desired action. Each slave device on a communications bus must have a unique address to prevent bus contention. All of the relay's ports have the same address which is programmable from 1 to 254; see Chapter 5 for details. Only the addressed slave will respond to a packet that starts with its address. Note that the faceplate port is an exception to this rule; it will act on a message containing any slave address.

A master transmit packet with slave address 0 indicates a broadcast command. All slaves on the communication link take action based on the packet, but none respond to the master. Broadcast mode is only recognized when associated with Function Code 05h. For any other function code, a packet with broadcast mode slave address 0 will be ignored.

- **FUNCTION CODE:** This is one of the supported functions codes of the unit which tells the slave what action to perform. See the Supported Function Codes section for complete details. An exception response from the slave is indicated by setting the high order bit of the function code in the response packet. See the Exception Responses section for further details.
- **DATA:** This will be a variable number of bytes depending on the function code. This may include actual values, settings, or addresses sent by the master to the slave or by the slave to the master.
- **CRC:** This is a two byte error checking code. The RTU version of Modbus® includes a 16-bit cyclic redundancy check (CRC-16) with every packet which is an industry standard method used for error detection. If a Modbus slave device receives a packet in which an error is indicated by the CRC, the slave device will not act upon or respond to the packet thus preventing any erroneous operations. See the *CRC-16 Algorithm* section for details on calculating the CRC.
- **DEAD TIME:** A packet is terminated when no data is received for a period of 3.5 byte transmission times (about 15 ms at 2400 bps, 2 ms at 19200 bps, and 300 μ s at 115200 bps). Consequently, the transmitting device must not allow gaps between bytes longer than this interval. Once the dead time has expired without a new byte transmission, all slaves start listening for a new packet from the master except for the addressed slave.

B.1.4 CRC-16 ALGORITHM

The CRC-16 algorithm essentially treats the entire data stream (data bits only; start, stop and parity ignored) as one continuous binary number. This number is first shifted left 16 bits and then divided by a characteristic polynomial (11000000000000101B). The 16 bit remainder of the division is appended to the end of the packet, MSByte first. The resulting packet including CRC, when divided by the same polynomial at the receiver will give a zero remainder if no transmission errors have occurred. This algorithm requires the characteristic polynomial to be reverse bit ordered. The most significant bit of the characteristic polynomial is dropped, since it does not affect the value of the remainder.

A C programming language implementation of the CRC algorithm will be provided upon request.

Table B-2: CRC-16 ALGORITHM

SYMBOLS:	-->	data transfer	
	A	16 bit working register	
	A _{low}	low order byte of A	
	A _{high}	high order byte of A	
	CRC	16 bit CRC-16 result	
	i,j	loop counters	
	(+)	logical EXCLUSIVE-OR operator	
	N	total number of data bytes	
	D _i	i-th data byte (i = 0 to N-1)	
	G	16 bit characteristic polynomial = 1010000000000001 (binary) with MSbit dropped and bit order reversed	
	shr (x)	right shift operator (th LSbit of x is shifted into a carry flag, a '0' is shifted into the MSbit of x, all other bits are shifted right one location)	
ALGORITHM:	1.	FFFF (hex) --> A	
	2.	0 --> i	
	3.	0 --> j	
	4.	D _i (+) A _{low} --> A _{low}	
	5.	j + 1 --> j	
	6.	shr (A)	
	7.	Is there a carry?	No: go to 8; Yes: G (+) A --> A and continue.
	8.	Is j = 8?	No: go to 5; Yes: continue
	9.	i + 1 --> i	
	10.	Is i = N?	No: go to 3; Yes: continue
	11.	A --> CRC	

B.2.1 SUPPORTED FUNCTION CODES

Modbus® officially defines function codes from 1 to 127 though only a small subset is generally needed. The relay supports some of these functions, as summarized in the following table. Subsequent sections describe each function code in detail.

FUNCTION CODE		MODBUS DEFINITION	GE MULTILIN DEFINITION
HEX	DEC		
03	3	Read Holding Registers	Read Actual Values or Settings
04	4	Read Holding Registers	Read Actual Values or Settings
05	5	Force Single Coil	Execute Operation
06	6	Preset Single Register	Store Single Setting
10	16	Preset Multiple Registers	Store Multiple Settings

B.2.2 READ ACTUAL VALUES OR SETTINGS (FUNCTION CODE 03/04H)

This function code allows the master to read one or more consecutive data registers (actual values or settings) from a relay. Data registers are always 16 bit (two byte) values transmitted with high order byte first. The maximum number of registers that can be read in a single packet is 125. See the Modbus Memory Map table for exact details on the data registers.

Since some PLC implementations of Modbus® only support one of function codes 03h and 04h, the relay interpretation allows either function code to be used for reading one or more consecutive data registers. The data starting address will determine the type of data being read. Function codes 03h and 04h are therefore identical.

The following table shows the format of the master and slave packets. The example shows a master device requesting 3 register values starting at address 4050h from slave device 11h (17 decimal); the slave device responds with the values 40, 300, and 0 from registers 4050h, 4051h, and 4052h, respectively.

Table B-3: MASTER AND SLAVE DEVICE PACKET TRANSMISSION EXAMPLE

MASTER TRANSMISSION		SLAVE RESPONSE	
PACKET FORMAT	EXAMPLE (HEX)	PACKET FORMAT	EXAMPLE (HEX)
SLAVE ADDRESS	11	SLAVE ADDRESS	11
FUNCTION CODE	04	FUNCTION CODE	04
DATA STARTING ADDRESS - high	40	BYTE COUNT	06
DATA STARTING ADDRESS - low	50	DATA #1 - high	00
NUMBER OF REGISTERS - high	00	DATA #1 - low	28
NUMBER OF REGISTERS - low	03	DATA #2 - high	01
CRC - low	A7	DATA #2 - low	2C
CRC - high	4A	DATA #3 - high	00
		DATA #3 - low	00
		CRC - low	0D
		CRC - high	60

B.2.3 EXECUTE OPERATION (FUNCTION CODE 05H)

This function code allows the master to perform various operations in the relay. Available operations are shown in the *Summary of Operation Codes* table below.

The following table shows the format of the master and slave packets. The example shows a master device requesting the slave device 11h (17 decimal) to perform a reset. The high and low code value bytes always have the values “FF” and “00” respectively and are a remnant of the original Modbus® definition of this function code.

Table B–4: MASTER AND SLAVE DEVICE PACKET TRANSMISSION EXAMPLE

MASTER TRANSMISSION		SLAVE RESPONSE	
PACKET FORMAT	EXAMPLE (HEX)	PACKET FORMAT	EXAMPLE (HEX)
SLAVE ADDRESS	11	SLAVE ADDRESS	11
FUNCTION CODE	05	FUNCTION CODE	05
OPERATION CODE - high	00	OPERATION CODE - high	00
OPERATION CODE - low	01	OPERATION CODE - low	01
CODE VALUE - high	FF	CODE VALUE - high	FF
CODE VALUE - low	00	CODE VALUE - low	00
CRC - low	DF	CRC - low	DF
CRC - high	6A	CRC - high	6A

Table B–5: SUMMARY OF OPERATION CODES FOR FUNCTION 05H

OPERATION CODE (HEX)	DEFINITION	DESCRIPTION
0000	NO OPERATION	Does not do anything.
0001	RESET	Performs the same function as the faceplate RESET key.
0005	CLEAR EVENT RECORDS	Performs the same function as the faceplate CLEAR EVENT RECORDS menu command.
0006	CLEAR OSCILLOGRAPHY	Clears all oscillography records.
1000 to 103F	VIRTUAL IN 1 to 64 ON/OFF	Sets the states of Virtual Inputs 1 to 64 either “ON” or “OFF”.

B.2.4 STORE SINGLE SETTING (FUNCTION CODE 06H)

This function code allows the master to modify the contents of a single setting register in an relay. Setting registers are always 16 bit (two byte) values transmitted high order byte first. The following table shows the format of the master and slave packets. The example shows a master device storing the value 200 at memory map address 4051h to slave device 11h (17 dec).

Table B–6: MASTER AND SLAVE DEVICE PACKET TRANSMISSION EXAMPLE

MASTER TRANSMISSION		SLAVE RESPONSE	
PACKET FORMAT	EXAMPLE (HEX)	PACKET FORMAT	EXAMPLE (HEX)
SLAVE ADDRESS	11	SLAVE ADDRESS	11
FUNCTION CODE	06	FUNCTION CODE	06
DATA STARTING ADDRESS - high	40	DATA STARTING ADDRESS - high	40
DATA STARTING ADDRESS - low	51	DATA STARTING ADDRESS - low	51
DATA - high	00	DATA - high	00
DATA - low	C8	DATA - low	C8
CRC - low	CE	CRC - low	CE
CRC - high	DD	CRC - high	DD

B.2.5 STORE MULTIPLE SETTINGS (FUNCTION CODE 10H)

This function code allows the master to modify the contents of a one or more consecutive setting registers in a relay. Setting registers are 16-bit (two byte) values transmitted high order byte first. The maximum number of setting registers that can be stored in a single packet is 60. The following table shows the format of the master and slave packets. The example shows a master device storing the value 200 at memory map address 4051h, and the value 1 at memory map address 4052h to slave device 11h (17 decimal).

Table B-7: MASTER AND SLAVE DEVICE PACKET TRANSMISSION EXAMPLE

MASTER TRANSMISSION		SLAVE RESPONSE	
PACKET FORMAT	EXAMPLE (HEX)	PACKET FORMAT	EXAMPLE (HEX)
SLAVE ADDRESS	11	SLAVE ADDRESS	11
FUNCTION CODE	10	FUNCTION CODE	10
DATA STARTING ADDRESS - hi	40	DATA STARTING ADDRESS - hi	40
DATA STARTING ADDRESS - lo	51	DATA STARTING ADDRESS - lo	51
NUMBER OF SETTINGS - hi	00	NUMBER OF SETTINGS - hi	00
NUMBER OF SETTINGS - lo	02	NUMBER OF SETTINGS - lo	02
BYTE COUNT	04	CRC - lo	07
DATA #1 - high order byte	00	CRC - hi	64
DATA #1 - low order byte	C8		
DATA #2 - high order byte	00		
DATA #2 - low order byte	01		
CRC - low order byte	12		
CRC - high order byte	62		

B.2.6 EXCEPTION RESPONSES

Programming or operation errors usually happen because of illegal data in a packet. These errors result in an exception response from the slave. The slave detecting one of these errors sends a response packet to the master with the high order bit of the function code set to 1.

The following table shows the format of the master and slave packets. The example shows a master device sending the unsupported function code 39h to slave device 11.

Table B-8: MASTER AND SLAVE DEVICE PACKET TRANSMISSION EXAMPLE

MASTER TRANSMISSION		SLAVE RESPONSE	
PACKET FORMAT	EXAMPLE (HEX)	PACKET FORMAT	EXAMPLE (HEX)
SLAVE ADDRESS	11	SLAVE ADDRESS	11
FUNCTION CODE	39	FUNCTION CODE	B9
CRC - low order byte	CD	ERROR CODE	01
CRC - high order byte	F2	CRC - low order byte	93
		CRC - high order byte	95

B.3.1 OBTAINING RELAY FILES VIA MODBUS

a) INTRODUCTION

The UR relay has a generic file transfer facility, meaning that you use the same method to obtain all of the different types of files from the unit. The Modbus registers that implement file transfer are found in the "Modbus File Transfer (Read/Write)" and "Modbus File Transfer (Read Only)" modules, starting at address 3100 in the Modbus Memory Map. To read a file from the UR relay, use the following steps:

1. Write the filename to the "Name of file to read" register using a write multiple registers command. If the name is shorter than 80 characters, you may write only enough registers to include all the text of the filename. Filenames are not case sensitive.
2. Repeatedly read all the registers in "Modbus File Transfer (Read Only)" using a read multiple registers command. It is not necessary to read the entire data block, since the UR relay will remember which was the last register you read. The "position" register is initially zero and thereafter indicates how many bytes (2 times the number of registers) you have read so far. The "size of..." register indicates the number of bytes of data remaining to read, to a maximum of 244.
3. Keep reading until the "size of..." register is smaller than the number of bytes you are transferring. This condition indicates end of file. Discard any bytes you have read beyond the indicated block size.
4. If you need to re-try a block, read only the "size of.." and "block of data", without reading the position. The file pointer is only incremented when you read the position register, so the same data block will be returned as was read in the previous operation. On the next read, check to see if the position is where you expect it to be, and discard the previous block if it is not (this condition would indicate that the UR relay did not process your original read request).

The UR relay retains connection-specific file transfer information, so files may be read simultaneously on multiple Modbus connections.

b) OTHER PROTOCOLS

All the files available via Modbus may also be retrieved using the standard file transfer mechanisms in other protocols (for example, TFTP or MMS).

c) COMTRADE, OSCILLOGRAPHY, AND DATA LOGGER FILES

Oscillography and data logger files are formatted using the COMTRADE file format per IEEE PC37.111 Draft 7c (02 September 1997). The files may be obtained in either text or binary COMTRADE format.

d) READING OSCILLOGRAPHY FILES

Familiarity with the oscillography feature is required to understand the following description. Refer to the Oscillography section in Chapter 5 for additional details.

The Oscillography Number of Triggers register is incremented by one every time a new oscillography file is triggered (captured) and cleared to zero when oscillography data is cleared. When a new trigger occurs, the associated oscillography file is assigned a file identifier number equal to the incremented value of this register; the newest file number is equal to the Oscillography_Number_of_Triggers register. This register can be used to determine if any new data has been captured by periodically reading it to see if the value has changed; if the number has increased then new data is available.

The Oscillography Number of Records register specifies the maximum number of files (and the number of cycles of data per file) that can be stored in memory of the relay. The Oscillography Available Records register specifies the actual number of files that are stored and still available to be read out of the relay.

Writing "Yes" (i.e. the value 1) to the Oscillography Clear Data register clears oscillography data files, clears both the Oscillography Number of Triggers and Oscillography Available Records registers to zero, and sets the Oscillography Last Cleared Date to the present date and time.

To read binary COMTRADE oscillography files, read the following filenames:

OSCnnnn.CFG and OSCnnn.DAT

Replace "nnn" with the desired oscillography trigger number. For ASCII format, use the following file names

OSCAnnnn.CFG and OSCAnnn.DAT

e) READING DATA LOGGER FILES

Familiarity with the data logger feature is required to understand this description. Refer to the Data Logger section of Chapter 5 for details. To read the entire data logger in binary COMTRADE format, read the following files.

`datalog.cfg` and `datalog.dat`

To read the entire data logger in ASCII COMTRADE format, read the following files.

`dataloga.cfg` and `dataloga.dat`

To limit the range of records to be returned in the COMTRADE files, append the following to the filename before writing it:

- To read from a specific time to the end of the log: `<space> startTime`
- To read a specific range of records: `<space> startTime <space> endTime`
- Replace `<startTime>` and `<endTime>` with Julian dates (seconds since Jan. 1 1970) as numeric text.

f) READING EVENT RECORDER FILES

To read the entire event recorder contents in ASCII format (the only available format), use the following filename:

`EVT.TXT`

To read from a specific record to the end of the log, use the following filename:

`EVTnnn.TXT` (replace `nnn` with the desired starting record number)

To read from a specific record to another specific record, use the following filename:

`EVT.TXT xxxxx yyyy` (replace `xxxxx` with the starting record number and `yyyy` with the ending record number)

B.3.2 MODBUS PASSWORD OPERATION

The COMMAND password is set up at memory location 4000. Storing a value of “0” removes COMMAND password protection. When reading the password setting, the encrypted value (zero if no password is set) is returned. COMMAND security is required to change the COMMAND password. Similarly, the SETTING password is set up at memory location 4002. These are the same settings and encrypted values found in the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **PASSWORD SECURITY** menu via the keypad. Enabling password security for the faceplate display will also enable it for Modbus, and vice-versa.

To gain COMMAND level security access, the COMMAND password must be entered at memory location 4008. To gain SETTING level security access, the SETTING password must be entered at memory location 400A. The entered SETTING password must match the current SETTING password setting, or must be zero, to change settings or download firmware.

COMMAND and SETTING passwords each have a 30-minute timer. Each timer starts when you enter the particular password, and is re-started whenever you “use” it. For example, writing a setting re-starts the SETTING password timer and writing a command register or forcing a coil re-starts the COMMAND password timer. The value read at memory location 4010 can be used to confirm whether a COMMAND password is enabled or disabled (0 for Disabled). The value read at memory location 4011 can be used to confirm whether a SETTING password is enabled or disabled.

COMMAND or SETTING password security access is restricted to the particular port or particular TCP/IP connection on which the entry was made. Passwords must be entered when accessing the relay through other ports or connections, and the passwords must be re-entered after disconnecting and re-connecting on TCP/IP.

B.4.1 MODBUS MEMORY MAP

Table B–9: MODBUS MEMORY MAP (Sheet 1 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
Product Information (Read Only)						
0000	UR Product Type	0 to 65535	---	1	F001	0
0002	Product Version	0 to 655.35	---	0.01	F001	1
Product Information (Read Only -- Written by Factory)						
0010	Serial Number	---	---	---	F203	"0"
0020	Manufacturing Date	0 to 4294967295	---	1	F050	0
0022	Modification Number	0 to 65535	---	1	F001	0
0040	Order Code	---	---	---	F204	"Order Code x"
0090	Ethernet MAC Address	---	---	---	F072	0
0093	Reserved (13 items)	---	---	---	F001	0
00A0	CPU Module Serial Number	---	---	---	F203	(none)
00B0	CPU Supplier Serial Number	---	---	---	F203	(none)
00C0	Ethernet Sub Module Serial Number (8 items)	---	---	---	F203	(none)
Self Test Targets (Read Only)						
0200	Self Test States (2 items)	0 to 4294967295	0	1	F143	0
Front Panel (Read Only)						
0204	LED Column n State, $n = 1$ to 10 (10 items)	0 to 65535	---	1	F501	0
0220	Display Message	---	---	---	F204	(none)
0248	Last Key Pressed	0 to 47	---	1	F530	0 (None)
Keypress Emulation (Read/Write)						
0280	Simulated keypress -- write zero before each keystroke	0 to 42	---	1	F190	0 (No key -- use between real keys)
Virtual Input Commands (Read/Write Command) (64 modules)						
0400	Virtual Input 1 State	0 to 1	---	1	F108	0 (Off)
0401	Virtual Input 2 State	0 to 1	---	1	F108	0 (Off)
0402	Virtual Input 3 State	0 to 1	---	1	F108	0 (Off)
0403	Virtual Input 4 State	0 to 1	---	1	F108	0 (Off)
0404	Virtual Input 5 State	0 to 1	---	1	F108	0 (Off)
0405	Virtual Input 6 State	0 to 1	---	1	F108	0 (Off)
0406	Virtual Input 7 State	0 to 1	---	1	F108	0 (Off)
0407	Virtual Input 8 State	0 to 1	---	1	F108	0 (Off)
0408	Virtual Input 9 State	0 to 1	---	1	F108	0 (Off)
0409	Virtual Input 10 State	0 to 1	---	1	F108	0 (Off)
040A	Virtual Input 11 State	0 to 1	---	1	F108	0 (Off)
040B	Virtual Input 12 State	0 to 1	---	1	F108	0 (Off)
040C	Virtual Input 13 State	0 to 1	---	1	F108	0 (Off)
040D	Virtual Input 14 State	0 to 1	---	1	F108	0 (Off)
040E	Virtual Input 15 State	0 to 1	---	1	F108	0 (Off)
040F	Virtual Input 16 State	0 to 1	---	1	F108	0 (Off)
0410	Virtual Input 17 State	0 to 1	---	1	F108	0 (Off)
0411	Virtual Input 18 State	0 to 1	---	1	F108	0 (Off)
0412	Virtual Input 19 State	0 to 1	---	1	F108	0 (Off)
0413	Virtual Input 20 State	0 to 1	---	1	F108	0 (Off)
0414	Virtual Input 21 State	0 to 1	---	1	F108	0 (Off)
0415	Virtual Input 22 State	0 to 1	---	1	F108	0 (Off)
0416	Virtual Input 23 State	0 to 1	---	1	F108	0 (Off)
0417	Virtual Input 24 State	0 to 1	---	1	F108	0 (Off)
0418	Virtual Input 25 State	0 to 1	---	1	F108	0 (Off)
0419	Virtual Input 26 State	0 to 1	---	1	F108	0 (Off)
041A	Virtual Input 27 State	0 to 1	---	1	F108	0 (Off)
041B	Virtual Input 28 State	0 to 1	---	1	F108	0 (Off)

Table B-9: MODBUS MEMORY MAP (Sheet 2 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
041C	Virtual Input 29 State	0 to 1	---	1	F108	0 (Off)
041D	Virtual Input 30 State	0 to 1	---	1	F108	0 (Off)
041E	Virtual Input 31 State	0 to 1	---	1	F108	0 (Off)
041F	Virtual Input 32 State	0 to 1	---	1	F108	0 (Off)
0420	Virtual Input 33 State	0 to 1	---	1	F108	0 (Off)
0421	Virtual Input 34 State	0 to 1	---	1	F108	0 (Off)
0422	Virtual Input 35 State	0 to 1	---	1	F108	0 (Off)
0423	Virtual Input 36 State	0 to 1	---	1	F108	0 (Off)
0424	Virtual Input 37 State	0 to 1	---	1	F108	0 (Off)
0425	Virtual Input 38 State	0 to 1	---	1	F108	0 (Off)
0426	Virtual Input 39 State	0 to 1	---	1	F108	0 (Off)
0427	Virtual Input 40 State	0 to 1	---	1	F108	0 (Off)
0428	Virtual Input 41 State	0 to 1	---	1	F108	0 (Off)
0429	Virtual Input 42 State	0 to 1	---	1	F108	0 (Off)
042A	Virtual Input 43 State	0 to 1	---	1	F108	0 (Off)
042B	Virtual Input 44 State	0 to 1	---	1	F108	0 (Off)
042C	Virtual Input 45 State	0 to 1	---	1	F108	0 (Off)
042D	Virtual Input 46 State	0 to 1	---	1	F108	0 (Off)
042E	Virtual Input 47 State	0 to 1	---	1	F108	0 (Off)
042F	Virtual Input 48 State	0 to 1	---	1	F108	0 (Off)
0430	Virtual Input 49 State	0 to 1	---	1	F108	0 (Off)
0431	Virtual Input 50 State	0 to 1	---	1	F108	0 (Off)
0432	Virtual Input 51 State	0 to 1	---	1	F108	0 (Off)
0433	Virtual Input 52 State	0 to 1	---	1	F108	0 (Off)
0434	Virtual Input 53 State	0 to 1	---	1	F108	0 (Off)
0435	Virtual Input 54 State	0 to 1	---	1	F108	0 (Off)
0436	Virtual Input 55 State	0 to 1	---	1	F108	0 (Off)
0437	Virtual Input 56 State	0 to 1	---	1	F108	0 (Off)
0438	Virtual Input 57 State	0 to 1	---	1	F108	0 (Off)
0439	Virtual Input 58 State	0 to 1	---	1	F108	0 (Off)
043A	Virtual Input 59 State	0 to 1	---	1	F108	0 (Off)
043B	Virtual Input 60 State	0 to 1	---	1	F108	0 (Off)
043C	Virtual Input 61 State	0 to 1	---	1	F108	0 (Off)
043D	Virtual Input 62 State	0 to 1	---	1	F108	0 (Off)
043E	Virtual Input 63 State	0 to 1	---	1	F108	0 (Off)
043F	Virtual Input 64 State	0 to 1	---	1	F108	0 (Off)
Digital Counter States (Read Only Non-Volatile) (8 modules)						
0800	Digital Counter 1 Value	-2147483647 to 2147483647	---	1	F004	0
0802	Digital Counter 1 Frozen	-2147483647 to 2147483647	---	1	F004	0
0804	Digital Counter 1 Frozen Time Stamp	0 to 4294967295	---	1	F050	0
0806	Digital Counter 1 Frozen Time Stamp us	0 to 4294967295	---	1	F003	0
0808	...Repeated for Digital Counter 2					
0810	...Repeated for Digital Counter 3					
0818	...Repeated for Digital Counter 4					
0820	...Repeated for Digital Counter 5					
0828	...Repeated for Digital Counter 6					
0830	...Repeated for Digital Counter 7					
0838	...Repeated for Digital Counter 8					
FlexStates (Read Only)						
0900	FlexState Bits (16 items)	0 to 65535	---	1	F001	0
Element States (Read Only)						
1000	Element Operate States (64 items)	0 to 65535	---	1	F502	0

Table B–9: MODBUS MEMORY MAP (Sheet 3 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
User Displays Actuals (Read Only)						
1080	Formatted user-definable displays (16 items)	---	---	---	F200	(none)
Modbus User Map Actuals (Read Only)						
1200	User Map Values (256 items)	0 to 65535	---	1	F001	0
Element Targets (Read Only)						
14C0	Target Sequence	0 to 65535	---	1	F001	0
14C1	Number of Targets	0 to 65535	---	1	F001	0
Element Targets (Read/Write)						
14C2	Target to Read	0 to 65535	---	1	F001	0
Element Targets (Read Only)						
14C3	Target Message	---	---	---	F200	“ ”
Digital Input/Output States (Read Only)						
1500	Contact Input States (6 items)	0 to 65535	---	1	F500	0
1508	Virtual Input States (8 items)	0 to 65535	---	1	F500	0
1510	Contact Output States (4 items)	0 to 65535	---	1	F500	0
1518	Contact Output Current States (4 items)	0 to 65535	---	1	F500	0
1520	Contact Output Voltage States (4 items)	0 to 65535	---	1	F500	0
1528	Virtual Output States (6 items)	0 to 65535	---	1	F500	0
1530	Contact Output Detectors (4 items)	0 to 65535	---	1	F500	0
Remote Input/Output States (Read Only)						
1540	Remote Device 1 States	0 to 65535	---	1	F500	0
1542	Remote Input States (4 items)	0 to 65535	---	1	F500	0
1550	Remote Devices Online	0 to 1	---	1	F126	0 (No)
Remote Device Status (Read Only) (16 modules)						
1551	Remote Device 1 StNum	0 to 4294967295	---	1	F003	0
1553	Remote Device 1 SqNum	0 to 4294967295	---	1	F003	0
1555	...Repeated for Remote Device 2					
1559	...Repeated for Remote Device 3					
155D	...Repeated for Remote Device 4					
1561	...Repeated for Remote Device 5					
1565	...Repeated for Remote Device 6					
1569	...Repeated for Remote Device 7					
156D	...Repeated for Remote Device 8					
1571	...Repeated for Remote Device 9					
1575	...Repeated for Remote Device 10					
1579	...Repeated for Remote Device 11					
157D	...Repeated for Remote Device 12					
1581	...Repeated for Remote Device 13					
1585	...Repeated for Remote Device 14					
1589	...Repeated for Remote Device 15					
158D	...Repeated for Remote Device 16					
Platform Direct Input/Output States (Read Only)						
15C0	Direct input states (6 items)	0 to 65535	---	1	F500	0
15C8	Direct outputs average message return time 1	0 to 65535	ms	1	F001	0
15C9	Direct outputs average message return time 2	0 to 65535	ms	1	F001	0
15CA	Direct inputs/outputs unreturned message count - Ch. 1	0 to 65535	---	1	F001	0
15CB	Direct inputs/outputs unreturned message count - Ch. 2	0 to 65535	---	1	F001	0
15D0	Direct device states	0 to 65535	---	1	F500	0
15D1	Reserved	0 to 65535	---	1	F001	0
15D2	Direct inputs/outputs CRC fail count 1	0 to 65535	---	1	F001	0
15D3	Direct inputs/outputs CRC fail count 2	0 to 65535	---	1	F001	0
Ethernet Fibre Channel Status (Read/Write)						
1610	Ethernet primary fibre channel status	0 to 2	---	1	F134	0 (Fail)
1611	Ethernet secondary fibre channel status	0 to 2	---	1	F134	0 (Fail)

Table B-9: MODBUS MEMORY MAP (Sheet 4 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
Data Logger Actuals (Read Only)						
1618	Data logger channel count	0 to 16	channel	1	F001	0
1619	Time of oldest available samples	0 to 4294967295	seconds	1	F050	0
161B	Time of newest available samples	0 to 4294967295	seconds	1	F050	0
161D	Data logger duration	0 to 999.9	days	0.1	F001	0
Sensitive Directional Power Actuals (Read Only) (2 modules)						
1680	Sensitive Directional Power 1 Power	-2147483647 to 2147483647	W	1	F060	0
1682	Sensitive Directional Power 2 Power	-2147483647 to 2147483647	W	1	F060	0
Frequency Rate of Change Actuals (Read Only) (4 modules)						
16E0	Frequency Rate of Change 1	-327.67 to 327.67	Hz/s	0.01	F002	0
16E1	Reserved (3 items)	0 to 65535	---	1	F001	0
16E4	...Repeated for Frequency Rate of Change 2					
16E8	...Repeated for Frequency Rate of Change 3					
16EC	...Repeated for Frequency Rate of Change 4					
Source Current (Read Only) (6 modules)						
1800	Source 1 Phase A Current RMS	0 to 999999.999	A	0.001	F060	0
1802	Source 1 Phase B Current RMS	0 to 999999.999	A	0.001	F060	0
1804	Source 1 Phase C Current RMS	0 to 999999.999	A	0.001	F060	0
1806	Source 1 Neutral Current RMS	0 to 999999.999	A	0.001	F060	0
1808	Source 1 Phase A Current Magnitude	0 to 999999.999	A	0.001	F060	0
180A	Source 1 Phase A Current Angle	-359.9 to 0	degrees	0.1	F002	0
180B	Source 1 Phase B Current Magnitude	0 to 999999.999	A	0.001	F060	0
180D	Source 1 Phase B Current Angle	-359.9 to 0	degrees	0.1	F002	0
180E	Source 1 Phase C Current Magnitude	0 to 999999.999	A	0.001	F060	0
1810	Source 1 Phase C Current Angle	-359.9 to 0	degrees	0.1	F002	0
1811	Source 1 Neutral Current Magnitude	0 to 999999.999	A	0.001	F060	0
1813	Source 1 Neutral Current Angle	-359.9 to 0	degrees	0.1	F002	0
1814	Source 1 Ground Current RMS	0 to 999999.999	A	0.001	F060	0
1816	Source 1 Ground Current Magnitude	0 to 999999.999	A	0.001	F060	0
1818	Source 1 Ground Current Angle	-359.9 to 0	degrees	0.1	F002	0
1819	Source 1 Zero Sequence Current Magnitude	0 to 999999.999	A	0.001	F060	0
181B	Source 1 Zero Sequence Current Angle	-359.9 to 0	degrees	0.1	F002	0
181C	Source 1 Positive Sequence Current Magnitude	0 to 999999.999	A	0.001	F060	0
181E	Source 1 Positive Sequence Current Angle	-359.9 to 0	degrees	0.1	F002	0
181F	Source 1 Negative Sequence Current Magnitude	0 to 999999.999	A	0.001	F060	0
1821	Source 1 Negative Sequence Current Angle	-359.9 to 0	degrees	0.1	F002	0
1822	Source 1 Differential Ground Current Magnitude	0 to 999999.999	A	0.001	F060	0
1824	Source 1 Differential Ground Current Angle	-359.9 to 0	degrees	0.1	F002	0
1825	Reserved (27 items)	---	---	---	F001	0
1840	...Repeated for Source 2					
1880	...Repeated for Source 3					
18C0	...Repeated for Source 4					
1900	...Repeated for Source 5					
1940	...Repeated for Source 6					
Source Voltage (Read Only) (6 modules)						
1A00	Source 1 Phase AG Voltage RMS	0 to 999999.999	V	0.001	F060	0
1A02	Source 1 Phase BG Voltage RMS	0 to 999999.999	V	0.001	F060	0
1A04	Source 1 Phase CG Voltage RMS	0 to 999999.999	V	0.001	F060	0
1A06	Source 1 Phase AG Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A08	Source 1 Phase AG Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A09	Source 1 Phase BG Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A0B	Source 1 Phase BG Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A0C	Source 1 Phase CG Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A0E	Source 1 Phase CG Voltage Angle	-359.9 to 0	degrees	0.1	F002	0

Table B–9: MODBUS MEMORY MAP (Sheet 5 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
1A0F	Source 1 Phase AB or AC Voltage RMS	0 to 999999.999	V	0.001	F060	0
1A11	Source 1 Phase BC or BA Voltage RMS	0 to 999999.999	V	0.001	F060	0
1A13	Source 1 Phase CA or CB Voltage RMS	0 to 999999.999	V	0.001	F060	0
1A15	Source 1 Phase AB or AC Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A17	Source 1 Phase AB or AC Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A18	Source 1 Phase BC or BA Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A1A	Source 1 Phase BC or BA Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A1B	Source 1 Phase CA or CB Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A1D	Source 1 Phase CA or CB Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A1E	Source 1 Auxiliary Voltage RMS	0 to 999999.999	V	0.001	F060	0
1A20	Source 1 Auxiliary Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A22	Source 1 Auxiliary Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A23	Source 1 Zero Sequence Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A25	Source 1 Zero Sequence Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A26	Source 1 Positive Sequence Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A28	Source 1 Positive Sequence Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A29	Source 1 Negative Sequence Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
1A2B	Source 1 Negative Sequence Voltage Angle	-359.9 to 0	degrees	0.1	F002	0
1A2C	Reserved (20 items)	---	---	---	F001	0
1A40	...Repeated for Source 2					
1A80	...Repeated for Source 3					
1AC0	...Repeated for Source 4					
1B00	...Repeated for Source 5					
1B40	...Repeated for Source 6					
Source Power (Read Only) (6 modules)						
1C00	Source 1 Three Phase Real Power	-1000000000000 to 1000000000000	W	0.001	F060	0
1C02	Source 1 Phase A Real Power	-1000000000000 to 1000000000000	W	0.001	F060	0
1C04	Source 1 Phase B Real Power	-1000000000000 to 1000000000000	W	0.001	F060	0
1C06	Source 1 Phase C Real Power	-1000000000000 to 1000000000000	W	0.001	F060	0
1C08	Source 1 Three Phase Reactive Power	-1000000000000 to 1000000000000	var	0.001	F060	0
1C0A	Source 1 Phase A Reactive Power	-1000000000000 to 1000000000000	var	0.001	F060	0
1C0C	Source 1 Phase B Reactive Power	-1000000000000 to 1000000000000	var	0.001	F060	0
1C0E	Source 1 Phase C Reactive Power	-1000000000000 to 1000000000000	var	0.001	F060	0
1C10	Source 1 Three Phase Apparent Power	-1000000000000 to 1000000000000	VA	0.001	F060	0
1C12	Source 1 Phase A Apparent Power	-1000000000000 to 1000000000000	VA	0.001	F060	0
1C14	Source 1 Phase B Apparent Power	-1000000000000 to 1000000000000	VA	0.001	F060	0
1C16	Source 1 Phase C Apparent Power	-1000000000000 to 1000000000000	VA	0.001	F060	0
1C18	Source 1 Three Phase Power Factor	-0.999 to 1	---	0.001	F013	0
1C19	Source 1 Phase A Power Factor	-0.999 to 1	---	0.001	F013	0
1C1A	Source 1 Phase B Power Factor	-0.999 to 1	---	0.001	F013	0
1C1B	Source 1 Phase C Power Factor	-0.999 to 1	---	0.001	F013	0
1C1C	Reserved (4 items)	---	---	---	F001	0
1C20	...Repeated for Source 2					
1C40	...Repeated for Source 3					
1C60	...Repeated for Source 4					
1C80	...Repeated for Source 5					

Table B-9: MODBUS MEMORY MAP (Sheet 6 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
1CA0	...Repeated for Source 6					
Source Energy (Read Only Non-Volatile) (6 modules)						
1D00	Source 1 Positive Watthour	0 to 1000000000000	Wh	0.001	F060	0
1D02	Source 1 Negative Watthour	0 to 1000000000000	Wh	0.001	F060	0
1D04	Source 1 Positive Varhour	0 to 1000000000000	varh	0.001	F060	0
1D06	Source 1 Negative Varhour	0 to 1000000000000	varh	0.001	F060	0
1D08	Reserved (8 items)	---	---	---	F001	0
1D10	...Repeated for Source 2					
1D20	...Repeated for Source 3					
1D30	...Repeated for Source 4					
1D40	...Repeated for Source 5					
1D50	...Repeated for Source 6					
Energy Commands (Read/Write Command)						
1D60	Energy Clear Command	0 to 1	---	1	F126	0 (No)
Source Frequency (Read Only) (6 modules)						
1D80	Frequency for Source 1	2.000 to 90.000	Hz	0.001	F003	0
1D81	Frequency for Source 2	2.000 to 90.000	Hz	0.001	F003	0
1D82	Frequency for Source 3	2.000 to 90.000	Hz	0.001	F003	0
1D83	Frequency for Source 4	2.000 to 90.000	Hz	0.001	F003	0
1D84	Frequency for Source 5	2.000 to 90.000	Hz	0.001	F003	0
1D85	Frequency for Source 6	2.000 to 90.000	Hz	0.001	F003	0
Source Demand (Read Only) (6 modules)						
1E00	Source 1 Demand Ia	0 to 999999.999	A	0.001	F060	0
1E02	Source 1 Demand Ib	0 to 999999.999	A	0.001	F060	0
1E04	Source 1 Demand Ic	0 to 999999.999	A	0.001	F060	0
1E06	Source 1 Demand Watt	0 to 999999.999	W	0.001	F060	0
1E08	Source 1 Demand Var	0 to 999999.999	var	0.001	F060	0
1E0A	Source 1 Demand Va	0 to 999999.999	VA	0.001	F060	0
1E0C	Reserved (4 items)	---	---	---	F001	0
1E10	...Repeated for Source 2					
1E20	...Repeated for Source 3					
1E30	...Repeated for Source 4					
1E40	...Repeated for Source 5					
1E50	...Repeated for Source 6					
Source Demand Peaks (Read Only Non-Volatile) (6 modules)						
1E80	Source 1 Demand Ia Maximum	0 to 999999.999	A	0.001	F060	0
1E82	Source 1 Demand Ia Maximum Date	0 to 4294967295	---	1	F050	0
1E84	Source 1 Demand Ib Maximum	0 to 999999.999	A	0.001	F060	0
1E86	Source 1 Demand Ib Maximum Date	0 to 4294967295	---	1	F050	0
1E88	Source 1 Demand Ic Maximum	0 to 999999.999	A	0.001	F060	0
1E8A	Source 1 Demand Ic Maximum Date	0 to 4294967295	---	1	F050	0
1E8C	Source 1 Demand Watt Maximum	0 to 999999.999	W	0.001	F060	0
1E8E	Source 1 Demand Watt Maximum Date	0 to 4294967295	---	1	F050	0
1E90	Source 1 Demand Var	0 to 999999.999	var	0.001	F060	0
1E92	Source 1 Demand Var Maximum Date	0 to 4294967295	---	1	F050	0
1E94	Source 1 Demand Va Maximum	0 to 999999.999	VA	0.001	F060	0
1E96	Source 1 Demand Va Maximum Date	0 to 4294967295	---	1	F050	0
1E98	Reserved (8 items)	---	---	---	F001	0
1EA0	...Repeated for Source 2					
1EC0	...Repeated for Source 3					
1EE0	...Repeated for Source 4					
1F00	...Repeated for Source 5					
1F20	...Repeated for Source 6					

Table B–9: MODBUS MEMORY MAP (Sheet 7 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
Passwords Unauthorized Access (Read/Write Command)						
2230	Reset Unauthorized Access	0 to 1	---	1	F126	0 (No)
Synchrocheck Actuals (Read Only) (2 modules)						
2400	Synchrocheck 1 Delta Voltage	-1000000000000 to 1000000000000	V	1	F060	0
2402	Synchrocheck 1 Delta Frequency	0 to 655.35	Hz	0.01	F001	0
2403	Synchrocheck 1 Delta Phase	0 to 179.9	degrees	0.1	F001	0
2404	...Repeated for Synchrocheck 2					
Phasor Measurement Unit actual values (Read Only) (4 modules)						
2540	PMU 1 Phase A Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
2542	PMU Unit 1 Phase A Voltage Angle	-359.9 to 0	°	0.1	F002	0
2543	PMU 1 Phase B Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
2545	PMU 1 Phase B Voltage Angle	-359.9 to 0	°	0.1	F002	0
2546	PMU 1 Phase C Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
2548	PMU 1 Phase C Voltage Angle	-359.9 to 0	°	0.1	F002	0
2549	PMU 1 Auxiliary Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
254B	PMU 1 Auxiliary Voltage Angle	-359.9 to 0	°	0.1	F002	0
254C	PMU 1 Positive Sequence Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
254E	PMU 1 Positive Sequence Voltage Angle	-359.9 to 0	°	0.1	F002	0
254F	PMU 1 Negative Sequence Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
2551	PMU 1 Negative Sequence Voltage Angle	-359.9 to 0	°	0.1	F002	0
2552	PMU 1 Zero Sequence Voltage Magnitude	0 to 999999.999	V	0.001	F060	0
2554	PMU 1 Zero Sequence Voltage Angle	-359.9 to 0	°	0.1	F002	0
2555	PMU 1 Phase A Current Magnitude	0 to 999999.999	A	0.001	F060	0
2557	PMU 1 Phase A Current Angle	-359.9 to 0	°	0.1	F002	0
2558	PMU 1 Phase B Current Magnitude	0 to 999999.999	A	0.001	F060	0
255A	PMU 1 Phase B Current Angle	-359.9 to 0	°	0.1	F002	0
255B	PMU 1 Phase C Current Magnitude	0 to 999999.999	A	0.001	F060	0
255D	PMU 1 Phase C Current Angle	-359.9 to 0	°	0.1	F002	0
255E	PMU 1 Ground Current Magnitude	0 to 999999.999	A	0.001	F060	0
2560	PMU 1 Ground Current Angle	-359.9 to 0	°	0.1	F002	0
2561	PMU 1 Positive Sequence Current Magnitude	0 to 999999.999	A	0.001	F060	0
2563	PMU 1 Positive Sequence Current Angle	-359.9 to 0	°	0.1	F002	0
2564	PMU 1 Negative Sequence Current Magnitude	0 to 999999.999	A	0.001	F060	0
2566	PMU 1 Negative Sequence Current Angle	-359.9 to 0	°	0.1	F002	0
2567	PMU 1 Zero Sequence Current Magnitude	0 to 999999.999	A	0.001	F060	0
2569	PMU 1 Zero Sequence Current Angle	-359.9 to 0	°	0.1	F002	0
256A	PMU 1 Frequency	2 to 90	Hz	0.001	F003	0
256C	PMU 1 df/dt	-327.67 to 327.67	Hz/s	0.01	F002	0
256D	PMU 1 Config Change Counter	0 to 655.35		0.01	F001	0
256E	Reserved (4 items)	0 to 1	---	1	F001	0
2572	...Repeated for PMU 2					
25A4	...Repeated for PMU 3					
25D6	...Repeated for PMU 4					
Expanded FlexStates (Read Only)						
2B00	FlexStates, one per register (256 items)	0 to 1	---	1	F108	0 (Off)
Expanded Digital Input/Output states (Read Only)						
2D00	Contact Input States, one per register (96 items)	0 to 1	---	1	F108	0 (Off)
2D80	Contact Output States, one per register (64 items)	0 to 1	---	1	F108	0 (Off)
2E00	Virtual Output States, one per register (96 items)	0 to 1	---	1	F108	0 (Off)
Expanded Remote Input/Output Status (Read Only)						
2F00	Remote Device States, one per register (16 items)	0 to 1	---	1	F155	0 (Offline)
2F80	Remote Input States, one per register (64 items)	0 to 1	---	1	F108	0 (Off)

Table B-9: MODBUS MEMORY MAP (Sheet 8 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
Oscillography Values (Read Only)						
3000	Oscillography Number of Triggers	0 to 65535	---	1	F001	0
3001	Oscillography Available Records	0 to 65535	---	1	F001	0
3002	Oscillography Last Cleared Date	0 to 400000000	---	1	F050	0
3004	Oscillography Number Of Cycles Per Record	0 to 65535	---	1	F001	0
Oscillography Commands (Read/Write Command)						
3005	Oscillography Force Trigger	0 to 1	---	1	F126	0 (No)
3011	Oscillography Clear Data	0 to 1	---	1	F126	0 (No)
Modbus File Transfer (Read/Write)						
3100	Name of file to read	---	---	---	F204	(none)
Modbus File Transfer (Read Only)						
3200	Character position of current block within file	0 to 4294967295	---	1	F003	0
3202	Size of currently-available data block	0 to 65535	---	1	F001	0
3203	Block of data from requested file (122 items)	0 to 65535	---	1	F001	0
Event Recorder (Read Only)						
3400	Events Since Last Clear	0 to 4294967295	---	1	F003	0
3402	Number of Available Events	0 to 4294967295	---	1	F003	0
3404	Event Recorder Last Cleared Date	0 to 4294967295	---	1	F050	0
Event Recorder (Read/Write Command)						
3406	Event Recorder Clear Command	0 to 1	---	1	F126	0 (No)
DCMA Input Values (Read Only) (24 modules)						
34C0	DCMA Inputs 1 Value	-9999999 to 9999999	---	1	F004	0
34C2	DCMA Inputs 2 Value	-9999999 to 9999999	---	1	F004	0
34C4	DCMA Inputs 3 Value	-9999999 to 9999999	---	1	F004	0
34C6	DCMA Inputs 4 Value	-9999999 to 9999999	---	1	F004	0
34C8	DCMA Inputs 5 Value	-9999999 to 9999999	---	1	F004	0
34CA	DCMA Inputs 6 Value	-9999999 to 9999999	---	1	F004	0
34CC	DCMA Inputs 7 Value	-9999999 to 9999999	---	1	F004	0
34CE	DCMA Inputs 8 Value	-9999999 to 9999999	---	1	F004	0
34D0	DCMA Inputs 9 Value	-9999999 to 9999999	---	1	F004	0
34D2	DCMA Inputs 10 Value	-9999999 to 9999999	---	1	F004	0
34D4	DCMA Inputs 11 Value	-9999999 to 9999999	---	1	F004	0
34D6	DCMA Inputs 12 Value	-9999999 to 9999999	---	1	F004	0
34D8	DCMA Inputs 13 Value	-9999999 to 9999999	---	1	F004	0
34DA	DCMA Inputs 14 Value	-9999999 to 9999999	---	1	F004	0
34DC	DCMA Inputs 15 Value	-9999999 to 9999999	---	1	F004	0
34DE	DCMA Inputs 16 Value	-9999999 to 9999999	---	1	F004	0
34E0	DCMA Inputs 17 Value	-9999999 to 9999999	---	1	F004	0
34E2	DCMA Inputs 18 Value	-9999999 to 9999999	---	1	F004	0
34E4	DCMA Inputs 19 Value	-9999999 to 9999999	---	1	F004	0
34E6	DCMA Inputs 20 Value	-9999999 to 9999999	---	1	F004	0
34E8	DCMA Inputs 21 Value	-9999999 to 9999999	---	1	F004	0
34EA	DCMA Inputs 22 Value	-9999999 to 9999999	---	1	F004	0
34EC	DCMA Inputs 23 Value	-9999999 to 9999999	---	1	F004	0
34EE	DCMA Inputs 24 Value	-9999999 to 9999999	---	1	F004	0
RTD Input Values (Read Only) (48 modules)						
34F0	RTD Input 1 Value	-32768 to 32767	°C	1	F002	0
34F1	RTD Input 2 Value	-32768 to 32767	°C	1	F002	0
34F2	RTD Input 3 Value	-32768 to 32767	°C	1	F002	0
34F3	RTD Input 4 Value	-32768 to 32767	°C	1	F002	0
34F4	RTD Input 5 Value	-32768 to 32767	°C	1	F002	0
34F5	RTD Input 6 Value	-32768 to 32767	°C	1	F002	0
34F6	RTD Input 7 Value	-32768 to 32767	°C	1	F002	0
34F7	RTD Input 8 Value	-32768 to 32767	°C	1	F002	0

Table B-9: MODBUS MEMORY MAP (Sheet 9 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
34F8	RTD Input 9 Value	-32768 to 32767	°C	1	F002	0
34F9	RTD Input 10 Value	-32768 to 32767	°C	1	F002	0
34FA	RTD Input 11 Value	-32768 to 32767	°C	1	F002	0
34FB	RTD Input 12 Value	-32768 to 32767	°C	1	F002	0
34FC	RTD Input 13 Value	-32768 to 32767	°C	1	F002	0
34FD	RTD Input 14 Value	-32768 to 32767	°C	1	F002	0
34FE	RTD Input 15 Value	-32768 to 32767	°C	1	F002	0
34FF	RTD Input 16 Value	-32768 to 32767	°C	1	F002	0
3500	RTD Input 17 Value	-32768 to 32767	°C	1	F002	0
3501	RTD Input 18 Value	-32768 to 32767	°C	1	F002	0
3502	RTD Input 19 Value	-32768 to 32767	°C	1	F002	0
3503	RTD Input 20 Value	-32768 to 32767	°C	1	F002	0
3504	RTD Input 21 Value	-32768 to 32767	°C	1	F002	0
3505	RTD Input 22 Value	-32768 to 32767	°C	1	F002	0
3506	RTD Input 23 Value	-32768 to 32767	°C	1	F002	0
3507	RTD Input 24 Value	-32768 to 32767	°C	1	F002	0
3508	RTD Input 25 Value	-32768 to 32767	°C	1	F002	0
3509	RTD Input 26 Value	-32768 to 32767	°C	1	F002	0
350A	RTD Input 27 Value	-32768 to 32767	°C	1	F002	0
350B	RTD Input 28 Value	-32768 to 32767	°C	1	F002	0
350C	RTD Input 29 Value	-32768 to 32767	°C	1	F002	0
350D	RTD Input 30 Value	-32768 to 32767	°C	1	F002	0
350E	RTD Input 31 Value	-32768 to 32767	°C	1	F002	0
350F	RTD Input 32 Value	-32768 to 32767	°C	1	F002	0
3510	RTD Input 33 Value	-32768 to 32767	°C	1	F002	0
3511	RTD Input 34 Value	-32768 to 32767	°C	1	F002	0
3512	RTD Input 35 Value	-32768 to 32767	°C	1	F002	0
3513	RTD Input 36 Value	-32768 to 32767	°C	1	F002	0
3514	RTD Input 37 Value	-32768 to 32767	°C	1	F002	0
3515	RTD Input 38 Value	-32768 to 32767	°C	1	F002	0
3516	RTD Input 39 Value	-32768 to 32767	°C	1	F002	0
3517	RTD Input 40 Value	-32768 to 32767	°C	1	F002	0
3518	RTD Input 41 Value	-32768 to 32767	°C	1	F002	0
3519	RTD Input 42 Value	-32768 to 32767	°C	1	F002	0
351A	RTD Input 43 Value	-32768 to 32767	°C	1	F002	0
351B	RTD Input 44 Value	-32768 to 32767	°C	1	F002	0
351C	RTD Input 45 Value	-32768 to 32767	°C	1	F002	0
351D	RTD Input 46 Value	-32768 to 32767	°C	1	F002	0
351E	RTD Input 47 Value	-32768 to 32767	°C	1	F002	0
351F	RTD Input 48 Value	-32768 to 32767	°C	1	F002	0
Expanded Direct Input/Output Status (Read Only)						
3560	Direct Device States, one per register (8 items)	0 to 1	---	1	F155	0 (Offline)
3570	Direct Input States, one per register (96 items)	0 to 1	---	1	F108	0 (Off)
Passwords (Read/Write Command)						
4000	Command Password Setting	0 to 4294967295	---	1	F003	0
Passwords (Read/Write Setting)						
4002	Setting Password Setting	0 to 4294967295	---	1	F003	0
Passwords (Read/Write)						
4008	Command Password Entry	0 to 4294967295	---	1	F003	0
400A	Setting Password Entry	0 to 4294967295	---	1	F003	0
Passwords (Read Only)						
4010	Command Password Status	0 to 1	---	1	F102	0 (Disabled)
4011	Setting Password Status	0 to 1	---	1	F102	0 (Disabled)

Table B–9: MODBUS MEMORY MAP (Sheet 10 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
User Display Invoke (Read/Write Setting)						
4040	Invoke and Scroll Through User Display Menu Operand	0 to 65535	---	1	F300	0
LED Test (Read/Write Setting)						
4048	LED Test Function	0 to 1	---	1	F102	0 (Disabled)
4049	LED Test Control	0 to 65535	---	1	F300	0
Preferences (Read/Write Setting)						
404F	Language	0 to 3	---	1	F531	0 (English)
4050	Flash Message Time	0.5 to 10	s	0.1	F001	10
4051	Default Message Timeout	10 to 900	s	1	F001	300
4052	Default Message Intensity	0 to 3	---	1	F101	0 (25%)
4053	Screen Saver Feature	0 to 1	---	1	F102	0 (Disabled)
4054	Screen Saver Wait Time	1 to 65535	min	1	F001	30
4055	Current Cutoff Level	0.002 to 0.02	pu	0.001	F001	20
4056	Voltage Cutoff Level	0.1 to 1	V	0.1	F001	10
Communications (Read/Write Setting)						
407E	COM1 minimum response time	0 to 1000	ms	10	F001	0
407F	COM2 minimum response time	0 to 1000	ms	10	F001	0
4080	Modbus Slave Address	1 to 254	---	1	F001	254
4083	RS485 Com1 Baud Rate	0 to 11	---	1	F112	8 (115200)
4084	RS485 Com1 Parity	0 to 2	---	1	F113	0 (None)
4085	RS485 Com2 Baud Rate	0 to 11	---	1	F112	8 (115200)
4086	RS485 Com2 Parity	0 to 2	---	1	F113	0 (None)
4087	IP Address	0 to 4294967295	---	1	F003	56554706
4089	IP Subnet Mask	0 to 4294967295	---	1	F003	4294966272
408B	Gateway IP Address	0 to 4294967295	---	1	F003	56554497
408D	Network Address NSAP	---	---	---	F074	0
409A	DNP Channel 1 Port	0 to 4	---	1	F177	0 (None)
409B	DNP Channel 2 Port	0 to 4	---	1	F177	0 (None)
409C	DNP Address	0 to 65519	---	1	F001	1
409D	Reserved	0 to 1	---	1	F001	0
409E	DNP Client Addresses (2 items)	0 to 4294967295	---	1	F003	0
40A3	TCP Port Number for the Modbus protocol	1 to 65535	---	1	F001	502
40A4	TCP/UDP Port Number for the DNP Protocol	1 to 65535	---	1	F001	20000
40A5	TCP Port Number for the HTTP (Web Server) Protocol	1 to 65535	---	1	F001	80
40A6	Main UDP Port Number for the TFTP Protocol	1 to 65535	---	1	F001	69
40A7	Data Transfer UDP Port Numbers for the TFTP Protocol (zero means "automatic") (2 items)	0 to 65535	---	1	F001	0
40A9	DNP Unsolicited Responses Function	0 to 1	---	1	F102	0 (Disabled)
40AA	DNP Unsolicited Responses Timeout	0 to 60	s	1	F001	5
40AB	DNP Unsolicited Responses Max Retries	1 to 255	---	1	F001	10
40AC	DNP Unsolicited Responses Destination Address	0 to 65519	---	1	F001	1
40AD	Ethernet Operation Mode	0 to 1	---	1	F192	0 (Half-Duplex)
40AE	DNP Current Scale Factor	0 to 8	---	1	F194	2 (1)
40AF	DNP Voltage Scale Factor	0 to 8	---	1	F194	2 (1)
40B0	DNP Power Scale Factor	0 to 8	---	1	F194	2 (1)
40B1	DNP Energy Scale Factor	0 to 8	---	1	F194	2 (1)
40B2	DNP Other Scale Factor	0 to 8	---	1	F194	2 (1)
40B3	DNP Current Default Deadband	0 to 65535	---	1	F001	30000
40B4	DNP Voltage Default Deadband	0 to 65535	---	1	F001	30000
40B5	DNP Power Default Deadband	0 to 65535	---	1	F001	30000
40B6	DNP Energy Default Deadband	0 to 65535	---	1	F001	30000
40B7	DNP Other Default Deadband	0 to 65535	---	1	F001	30000
40B8	DNP IIN Time Sync Bit Period	1 to 10080	min	1	F001	1440
40B9	DNP Message Fragment Size	30 to 2048	---	1	F001	240

Table B–9: MODBUS MEMORY MAP (Sheet 11 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
40BA	DNP Client Address 3	0 to 4294967295	---	1	F003	0
40BC	DNP Client Address 4	0 to 4294967295	---	1	F003	0
40BE	DNP Client Address 5	0 to 4294967295	---	1	F003	0
40C0	DNP Number of Paired Binary Output Control Points	0 to 16	---	1	F001	0
40C1	Reserved (31 items)	0 to 1	---	1	F001	0
40E0	TCP Port Number for the IEC 60870-5-104 Protocol	1 to 65535	---	1	F001	2404
40E1	IEC 60870-5-104 Protocol Function	0 to 1	---	1	F102	0 (Disabled)
40E2	IEC 60870-5-104 Protocol Common Address of ASDU	0 to 65535	---	1	F001	0
40E3	IEC 60870-5-104 Protocol Cyclic Data Trans. Period	1 to 65535	s	1	F001	60
40E4	IEC 60870-5-104 Current Default Threshold	0 to 65535	---	1	F001	30000
40E5	IEC 60870-5-104 Voltage Default Threshold	0 to 65535	---	1	F001	30000
40E6	IEC 60870-5-104 Power Default Threshold	0 to 65535	---	1	F001	30000
40E7	IEC 60870-5-104 Energy Default Threshold	0 to 65535	---	1	F001	30000
40E8	IEC 60870-5-104 Other Default Threshold	0 to 65535	---	1	F001	30000
40E9	IEC 60870-5-104 Client Address (5 items)	0 to 4294967295	---	1	F003	0
40FD	IEC 60870-5-104 Communications Reserved (60 items)	0 to 1	---	1	F001	0
4140	DNP Object 1 Default Variation	1 to 2	---	1	F001	2
4141	DNP Object 2 Default Variation	1 to 2	---	1	F001	2
4142	DNP Object 20 Default Variation	0 to 3	---	1	F523	0 (1)
4143	DNP Object 21 Default Variation	0 to 3	---	1	F524	0 (1)
4144	DNP Object 22 Default Variation	0 to 3	---	1	F523	0 (1)
4145	DNP Object 23 Default Variation	0 to 3	---	1	F523	0 (1)
4146	DNP Object 30 Default Variation	1 to 5	---	1	F001	1
4147	DNP Object 32 Default Variation	0 to 5	---	1	F525	0 (1)
Simple Network Time Protocol (Read/Write Setting)						
4168	Simple Network Time Protocol (SNTP) Function	0 to 1	---	1	F102	0 (Disabled)
4169	Simple Network Time Protocol (SNTP) Server IP Address	0 to 4294967295	---	1	F003	0
416B	Simple Network Time Protocol (SNTP) UDP Port Number	1 to 65535	---	1	F001	123
Data Logger Commands (Read/Write Command)						
4170	Data Logger Clear	0 to 1	---	1	F126	0 (No)
Data Logger (Read/Write Setting)						
4181	Data Logger Channel Settings (16 items)	---	---	---	F600	0
4191	Data Logger Mode	0 to 1	---	1	F260	0 (continuous)
4192	Data Logger Trigger	0 to 65535	---	1	F300	0
4193	Data Logger Rate	15 to 3600000	ms	1	F003	60000
Clock (Read/Write Command)						
41A0	Real Time Clock Set Time	0 to 235959	---	1	F050	0
Clock (Read/Write Setting)						
41A2	SR Date Format	0 to 4294967295	---	1	F051	0
41A4	SR Time Format	0 to 4294967295	---	1	F052	0
41A6	IRIG-B Signal Type	0 to 2	---	1	F114	0 (None)
41A7	Clock Events Enable / Disable	0 to 1	---	1	F102	0 (Disabled)
Oscillography (Read/Write Setting)						
41C0	Oscillography Number of Records	1 to 64	---	1	F001	15
41C1	Oscillography Trigger Mode	0 to 1	---	1	F118	0 (Auto. Overwrite)
41C2	Oscillography Trigger Position	0 to 100	%	1	F001	50
41C3	Oscillography Trigger Source	0 to 65535	---	1	F300	0
41C4	Oscillography AC Input Waveforms	0 to 4	---	1	F183	2 (16 samples/cycle)
41D0	Oscillography Analog Channel <i>n</i> (16 items)	0 to 65535	---	1	F600	0
4200	Oscillography Digital Channel <i>n</i> (63 items)	0 to 65535	---	1	F300	0
Trip and Alarm LEDs (Read/Write Setting)						
4260	Trip LED Input FlexLogic Operand	0 to 65535	---	1	F300	0
4261	Alarm LED Input FlexLogic Operand	0 to 65535	---	1	F300	0

Table B-9: MODBUS MEMORY MAP (Sheet 12 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
User Programmable LEDs (Read/Write Setting) (48 modules)						
4280	FlexLogic™ Operand to Activate LED	0 to 65535	---	1	F300	0
4281	User LED type (latched or self-resetting)	0 to 1	---	1	F127	1 (Self-Reset)
4282	...Repeated for User-Programmable LED 2					
4284	...Repeated for User-Programmable LED 3					
4286	...Repeated for User-Programmable LED 4					
4288	...Repeated for User-Programmable LED 5					
428A	...Repeated for User-Programmable LED 6					
428C	...Repeated for User-Programmable LED 7					
428E	...Repeated for User-Programmable LED 8					
4290	...Repeated for User-Programmable LED 9					
4292	...Repeated for User-Programmable LED 10					
4294	...Repeated for User-Programmable LED 11					
4296	...Repeated for User-Programmable LED 12					
4298	...Repeated for User-Programmable LED 13					
429A	...Repeated for User-Programmable LED 14					
429C	...Repeated for User-Programmable LED 15					
429E	...Repeated for User-Programmable LED 16					
42A0	...Repeated for User-Programmable LED 17					
42A2	...Repeated for User-Programmable LED 18					
42A4	...Repeated for User-Programmable LED 19					
42A6	...Repeated for User-Programmable LED 20					
42A8	...Repeated for User-Programmable LED 21					
42AA	...Repeated for User-Programmable LED 22					
42AC	...Repeated for User-Programmable LED 23					
42AE	...Repeated for User-Programmable LED 24					
42B0	...Repeated for User-Programmable LED 25					
42B2	...Repeated for User-Programmable LED 26					
42B4	...Repeated for User-Programmable LED 27					
42B6	...Repeated for User-Programmable LED 28					
42B8	...Repeated for User-Programmable LED 29					
42BA	...Repeated for User-Programmable LED 30					
42BC	...Repeated for User-Programmable LED 31					
42BE	...Repeated for User-Programmable LED 32					
42C0	...Repeated for User-Programmable LED 33					
42C2	...Repeated for User-Programmable LED 34					
42C4	...Repeated for User-Programmable LED 35					
42C6	...Repeated for User-Programmable LED 36					
42C8	...Repeated for User-Programmable LED 37					
42CA	...Repeated for User-Programmable LED 38					
42CC	...Repeated for User-Programmable LED 39					
42CE	...Repeated for User-Programmable LED 40					
42D0	...Repeated for User-Programmable LED 41					
42D2	...Repeated for User-Programmable LED 42					
42D4	...Repeated for User-Programmable LED 43					
42D6	...Repeated for User-Programmable LED 44					
42D8	...Repeated for User-Programmable LED 45					
42DA	...Repeated for User-Programmable LED 46					
42DC	...Repeated for User-Programmable LED 47					
42DE	...Repeated for User-Programmable LED 48					
Installation (Read/Write Setting)						
43E0	Relay Programmed State	0 to 1	---	1	F133	0 (Not Programmed)
43E1	Relay Name	---	---	---	F202	"Relay-1"

Table B-9: MODBUS MEMORY MAP (Sheet 13 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
User Programmable Self Tests (Read/Write Setting)						
4441	User Programmable Detect Ring Break Function	0 to 1	---	1	F102	1 (Enabled)
4442	User Programmable Direct Device Off Function	0 to 1	---	1	F102	1 (Enabled)
4443	User Programmable Remote Device Off Function	0 to 1	---	1	F102	1 (Enabled)
4444	User Programmable Primary Ethernet Fail Function	0 to 1	---	1	F102	0 (Disabled)
4445	User Programmable Secondary Ethernet Fail Function	0 to 1	---	1	F102	0 (Disabled)
4446	User Programmable Battery Fail Function	0 to 1	---	1	F102	1 (Enabled)
4447	User Programmable SNTP Fail Function	0 to 1	---	1	F102	1 (Enabled)
4448	User Programmable IIRIG-B Fail Function	0 to 1	---	1	F102	1 (Enabled)
CT Settings (Read/Write Setting) (6 modules)						
4480	Phase CT 1 Primary	1 to 65000	A	1	F001	1
4481	Phase CT 1 Secondary	0 to 1	---	1	F123	0 (1 A)
4482	Ground CT 1 Primary	1 to 65000	A	1	F001	1
4483	Ground CT 1 Secondary	0 to 1	---	1	F123	0 (1 A)
4484	...Repeated for CT Bank 2					
4488	...Repeated for CT Bank 3					
448C	...Repeated for CT Bank 4					
4490	...Repeated for CT Bank 5					
4494	...Repeated for CT Bank 6					
VT Settings (Read/Write Setting) (3 modules)						
4500	Phase VT 1 Connection	0 to 1	---	1	F100	0 (Wye)
4501	Phase VT 1 Secondary	50 to 240	V	0.1	F001	664
4502	Phase VT 1 Ratio	1 to 24000	:1	1	F060	1
4504	Auxiliary VT 1 Connection	0 to 6	---	1	F166	1 (Vag)
4505	Auxiliary VT 1 Secondary	50 to 240	V	0.1	F001	664
4506	Auxiliary VT 1 Ratio	1 to 24000	:1	1	F060	1
4508	...Repeated for VT Bank 2					
4510	...Repeated for VT Bank 3					
Source Settings (Read/Write Setting) (6 modules)						
4580	Source 1 Name	---	---	---	F206	"SRC 1"
4583	Source 1 Phase CT	0 to 63	---	1	F400	0
4584	Source 1 Ground CT	0 to 63	---	1	F400	0
4585	Source 1 Phase VT	0 to 63	---	1	F400	0
4586	Source 1 Auxiliary VT	0 to 63	---	1	F400	0
4587	...Repeated for Source 2					
458E	...Repeated for Source 3					
4595	...Repeated for Source 4					
459C	...Repeated for Source 5					
45A3	...Repeated for Source 6					
Power System (Read/Write Setting)						
4600	Nominal Frequency	25 to 60	Hz	1	F001	60
4601	Phase Rotation	0 to 1	---	1	F106	0 (ABC)
4602	Frequency And Phase Reference	0 to 5	---	1	F167	0 (SRC 1)
4603	Frequency Tracking Function	0 to 1	---	1	F102	1 (Enabled)
Breaker Control (Read/Write Setting) (2 modules)						
4700	Breaker 1 Function	0 to 1	---	1	F102	0 (Disabled)
4701	Breaker 1 Name	---	---	---	F206	"Bkr 1"
4704	Breaker 1 Mode	0 to 1	---	1	F157	0 (3-Pole)
4705	Breaker 1 Open	0 to 65535	---	1	F300	0
4706	Breaker 1 Close	0 to 65535	---	1	F300	0
4707	Breaker 1 Phase A 3 Pole	0 to 65535	---	1	F300	0
4708	Breaker 1 Phase B	0 to 65535	---	1	F300	0
4709	Breaker 1 Phase C	0 to 65535	---	1	F300	0
470A	Breaker 1 External Alarm	0 to 65535	---	1	F300	0

Table B-9: MODBUS MEMORY MAP (Sheet 14 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
470B	Breaker 1 Alarm Delay	0 to 1000000	s	0.001	F003	0
470D	Breaker 1 Push Button Control	0 to 1	---	1	F102	0 (Disabled)
470E	Breaker 1 Manual Close Recall Time	0 to 1000000	s	0.001	F003	0
4710	Breaker 1 Out Of Service	0 to 65535	---	1	F300	0
4711	Breaker 1 IEC 61850 XCBR.ST.Loc Status operand	0 to 65535	---	1	F300	0
4712	Reserved (6 items)	0 to 65535	s	1	F001	0
4718	...Repeated for Breaker 2					
Synchrocheck (Read/Write Setting) (2 modules)						
4780	Synchrocheck 1 Function	0 to 1	---	1	F102	0 (Disabled)
4781	Synchrocheck 1 V1 Source	0 to 5	---	1	F167	0 (SRC 1)
4782	Synchrocheck 1 V2 Source	0 to 5	---	1	F167	1 (SRC 2)
4783	Synchrocheck 1 Maximum Voltage Difference	0 to 400000	V	1	F060	10000
4785	Synchrocheck 1 Maximum Angle Difference	0 to 100	degrees	1	F001	30
4786	Synchrocheck 1 Maximum Frequency Difference	0 to 2	Hz	0.01	F001	100
4787	Synchrocheck 1 Dead Source Select	0 to 5	---	1	F176	1 (LV1 and DV2)
4788	Synchrocheck 1 Dead V1 Maximum Voltage	0 to 1.25	pu	0.01	F001	30
4789	Synchrocheck 1 Dead V2 Maximum Voltage	0 to 1.25	pu	0.01	F001	30
478A	Synchrocheck 1 Live V1 Minimum Voltage	0 to 1.25	pu	0.01	F001	70
478B	Synchrocheck 1 Live V2 Minimum Voltage	0 to 1.25	pu	0.01	F001	70
478C	Synchrocheck 1 Target	0 to 2	---	1	F109	0 (Self-reset)
478D	Synchrocheck 1 Events	0 to 1	---	1	F102	0 (Disabled)
478E	Synchrocheck 1 Block	0 to 65535	---	1	F300	0
478F	Synchrocheck 1 Frequency Hysteresis	0 to 0.1	Hz	0.01	F001	6
4790	...Repeated for Synchrocheck 2					
Demand (Read/Write Setting)						
47D0	Demand Current Method	0 to 2	---	1	F139	0 (Thrm. Exponential)
47D1	Demand Power Method	0 to 2	---	1	F139	0 (Thrm. Exponential)
47D2	Demand Interval	0 to 5	---	1	F132	2 (15 MIN)
47D3	Demand Input	0 to 65535	---	1	F300	0
Demand (Read/Write Command)						
47D4	Demand Clear Record	0 to 1	---	1	F126	0 (No)
Modbus User Map (Read/Write Setting)						
4A00	Modbus Address Settings for User Map (256 items)	0 to 65535	---	1	F001	0
User Displays Settings (Read/Write Setting) (16 modules)						
4C00	User-Definable Display 1 Top Line Text	---	---	---	F202	" "
4C0A	User-Definable Display 1 Bottom Line Text	---	---	---	F202	""
4C14	Modbus Addresses of Display 1 Items (5 items)	0 to 65535	---	1	F001	0
4C19	Reserved (7 items)	---	---	---	F001	0
4C20	...Repeated for User-Definable Display 2					
4C40	...Repeated for User-Definable Display 3					
4C60	...Repeated for User-Definable Display 4					
4C80	...Repeated for User-Definable Display 5					
4CA0	...Repeated for User-Definable Display 6					
4CC0	...Repeated for User-Definable Display 7					
4CE0	...Repeated for User-Definable Display 8					
4D00	...Repeated for User-Definable Display 9					
4D20	...Repeated for User-Definable Display 10					
4D40	...Repeated for User-Definable Display 11					
4D60	...Repeated for User-Definable Display 12					
4D80	...Repeated for User-Definable Display 13					
4DA0	...Repeated for User-Definable Display 14					
4DC0	...Repeated for User-Definable Display 15					
4DE0	...Repeated for User-Definable Display 16					

Table B–9: MODBUS MEMORY MAP (Sheet 15 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
User Programmable Pushbuttons (Read/Write Setting) (12 modules)						
4E00	User Programmable Pushbutton 1 Function	0 to 2	---	1	F109	2 (Disabled)
4E01	User Programmable Pushbutton 1 Top Line	---	---	---	F202	(none)
4E0B	User Programmable Pushbutton 1 On Text	---	---	---	F202	(none)
4E15	User Programmable Pushbutton 1 Off Text	---	---	---	F202	(none)
4E1F	User Programmable Pushbutton 1 Drop-Out Time	0 to 60	s	0.05	F001	0
4E20	User Programmable Pushbutton 1 Target	0 to 2	---	1	F109	0 (Self-reset)
4E21	User Programmable Pushbutton 1 Events	0 to 1	---	1	F102	0 (Disabled)
4E22	User Programmable Pushbutton 1 Reserved (2 items)	0 to 65535	---	1	F001	0
4E24	...Repeated for User Programmable Pushbutton 2					
4E48	...Repeated for User Programmable Pushbutton 3					
4E6C	...Repeated for User Programmable Pushbutton 4					
4E90	...Repeated for User Programmable Pushbutton 5					
4EB4	...Repeated for User Programmable Pushbutton 6					
4ED8	...Repeated for User Programmable Pushbutton 7					
4EFC	...Repeated for User Programmable Pushbutton 8					
4F20	...Repeated for User Programmable Pushbutton 9					
4F44	...Repeated for User Programmable Pushbutton 10					
4F68	...Repeated for User Programmable Pushbutton 11					
4F8C	...Repeated for User Programmable Pushbutton 12					
Flexlogic (Read/Write Setting)						
5000	FlexLogic™ Entry (512 items)	0 to 65535	---	1	F300	16384
Flexlogic Timers (Read/Write Setting) (32 modules)						
5800	FlexLogic™ Timer 1 Type	0 to 2	---	1	F129	0 (millisecond)
5801	FlexLogic™ Timer 1 Pickup Delay	0 to 60000	---	1	F001	0
5802	FlexLogic™ Timer 1 Dropout Delay	0 to 60000	---	1	F001	0
5803	Reserved (5 items)	0 to 65535	---	1	F001	0
5808	...Repeated for FlexLogic™ Timer 2					
5810	...Repeated for FlexLogic™ Timer 3					
5818	...Repeated for FlexLogic™ Timer 4					
5820	...Repeated for FlexLogic™ Timer 5					
5828	...Repeated for FlexLogic™ Timer 6					
5830	...Repeated for FlexLogic™ Timer 7					
5838	...Repeated for FlexLogic™ Timer 8					
5840	...Repeated for FlexLogic™ Timer 9					
5848	...Repeated for FlexLogic™ Timer 10					
5850	...Repeated for FlexLogic™ Timer 11					
5858	...Repeated for FlexLogic™ Timer 12					
5860	...Repeated for FlexLogic™ Timer 13					
5868	...Repeated for FlexLogic™ Timer 14					
5870	...Repeated for FlexLogic™ Timer 15					
5878	...Repeated for FlexLogic™ Timer 16					
5880	...Repeated for FlexLogic™ Timer 17					
5888	...Repeated for FlexLogic™ Timer 18					
5890	...Repeated for FlexLogic™ Timer 19					
5898	...Repeated for FlexLogic™ Timer 20					
58A0	...Repeated for FlexLogic™ Timer 21					
58A8	...Repeated for FlexLogic™ Timer 22					
58B0	...Repeated for FlexLogic™ Timer 23					
58B8	...Repeated for FlexLogic™ Timer 24					
58C0	...Repeated for FlexLogic™ Timer 25					
58C8	...Repeated for FlexLogic™ Timer 26					
58D0	...Repeated for FlexLogic™ Timer 27					
58D8	...Repeated for FlexLogic™ Timer 28					

Table B-9: MODBUS MEMORY MAP (Sheet 16 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
58E0	...Repeated for FlexLogic™ Timer 29					
58E8	...Repeated for FlexLogic™ Timer 30					
58F0	...Repeated for FlexLogic™ Timer 31					
58F8	...Repeated for FlexLogic™ Timer 32					
Phase Instantaneous Overcurrent (Read/Write Grouped Setting) (12 modules)						
5A00	Phase Instantaneous Overcurrent 1 Function	0 to 1	---	1	F102	0 (Disabled)
5A01	Phase Instantaneous Overcurrent 1 Signal Source	0 to 5	---	1	F167	0 (SRC 1)
5A02	Phase Instantaneous Overcurrent 1 Pickup	0 to 30	pu	0.001	F001	1000
5A03	Phase Instantaneous Overcurrent 1 Delay	0 to 600	s	0.01	F001	0
5A04	Phase Instantaneous Overcurrent 1 Reset Delay	0 to 600	s	0.01	F001	0
5A05	Phase IOC1 Block For Each Phase (3 items)	0 to 65535	---	1	F300	0
5A08	Phase Instantaneous Overcurrent 1 Target	0 to 2	---	1	F109	0 (Self-reset)
5A09	Phase Instantaneous Overcurrent 1 Events	0 to 1	---	1	F102	0 (Disabled)
5A0A	Reserved (6 items)	0 to 1	---	1	F001	0
5A10	...Repeated for Phase Instantaneous Overcurrent 2					
5A20	...Repeated for Phase Instantaneous Overcurrent 3					
5A30	...Repeated for Phase Instantaneous Overcurrent 4					
5A40	...Repeated for Phase Instantaneous Overcurrent 5					
5A50	...Repeated for Phase Instantaneous Overcurrent 6					
5A60	...Repeated for Phase Instantaneous Overcurrent 7					
5A70	...Repeated for Phase Instantaneous Overcurrent 8					
5A80	...Repeated for Phase Instantaneous Overcurrent 9					
5A90	...Repeated for Phase Instantaneous Overcurrent 10					
5AA0	...Repeated for Phase Instantaneous Overcurrent 11					
5AB0	...Repeated for Phase Instantaneous Overcurrent 12					
Setting Groups (Read/Write Setting)						
5F80	Setting Group for Modbus Comms (0 means group 1)	0 to 5	---	1	F001	0
5F81	Setting Groups Block	0 to 65535	---	1	F300	0
5F82	FlexLogic to Activate Groups 2 through 6 (5 items)	0 to 65535	---	1	F300	0
5F89	Setting Group Function	0 to 1	---	1	F102	0 (Disabled)
5F8A	Setting Group Events	0 to 1	---	1	F102	0 (Disabled)
Setting Groups (Read Only)						
5F8B	Current Setting Group	0 to 5	---	1	F001	0
Setting Group Names (Read/Write Setting)						
5F8C	Setting Group 1 Name	---	---	---	F203	(none)
5494	Setting Group 2 Name	---	---	---	F203	(none)
5F9C	Setting Group 3 Name	---	---	---	F203	(none)
5FA4	Setting Group 4 Name	---	---	---	F203	(none)
5FAC	Setting Group 5 Name	---	---	---	F203	(none)
5FB4	Setting Group 6 Name	---	---	---	F203	(none)
Open Pole Detect (Read/Write Grouped Setting)						
6040	Open Pole Detect Function	0 to 1	---	1	F102	0 (Disabled)
6041	Open Pole Detect Block	0 to 65535	---	1	F300	0
6042	Open Pole Detect A Aux Co	0 to 65535	---	1	F300	0
6043	Open Pole Detect B Aux Co	0 to 65535	---	1	F300	0
6044	Open Pole Detect C Aux Co	0 to 65535	---	1	F300	0
6045	Open Pole Detect Current Source	0 to 5	---	1	F167	0 (SRC 1)
6046	Open Pole Detect Current Pickup	0.05 to 20	pu	0.01	F001	20
6047	Open Pole Detect Voltage Source	0 to 5	---	1	F167	0 (SRC 1)
6048	Open Pole Detect Voltage Input	0 to 1	---	1	F102	0 (Disabled)
6049	Open Pole Detect Pickup Delay	0 to 65.535	s	0.001	F001	60
604A	Open Pole Detect Reset Delay	0 to 65.535	s	0.001	F001	100
604B	Open Pole Detect Target	0 to 2	---	1	F109	0 (Self-reset)
604C	Open Pole Detect Events	0 to 1	---	1	F102	0 (Disabled)

Table B–9: MODBUS MEMORY MAP (Sheet 17 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
604D	Open Pole Detect Broken Co	0 to 1	---	1	F102	0 (Disabled)
Overfrequency (Read/Write Setting) (4 modules)						
64D0	Overfrequency 1 Function	0 to 1	---	1	F102	0 (Disabled)
64D1	Overfrequency 1 Block	0 to 65535	---	1	F300	0
64D2	Overfrequency 1 Source	0 to 5	---	1	F167	0 (SRC 1)
64D3	Overfrequency 1 Pickup	20 to 65	Hz	0.01	F001	6050
64D4	Overfrequency 1 Pickup Delay	0 to 65.535	s	0.001	F001	500
64D5	Overfrequency 1 Reset Delay	0 to 65.535	s	0.001	F001	500
64D6	Overfrequency 1 Target	0 to 2	---	1	F109	0 (Self-reset)
64D7	Overfrequency 1 Events	0 to 1	---	1	F102	0 (Disabled)
64D8	Reserved (4 items)	0 to 1	---	1	F001	0
64DC	...Repeated for Overfrequency 2					
64E8	...Repeated for Overfrequency 3					
64F4	...Repeated for Overfrequency 4					
Power Swing Detect (Read/Write Grouped Setting)						
65C0	Power Swing Detect Function	0 to 1	---	1	F102	0 (Disabled)
65C1	Power Swing Detect Source	0 to 5	---	1	F167	0 (SRC 1)
65C2	Power Swing Detect Mode	0 to 1	---	1	F513	0 (Two Step)
65C3	Power Swing Detect Supervision	0.05 to 30	pu	0.001	F001	600
65C4	Power Swing Detect Forward Reach	0.1 to 500	ohms	0.01	F001	5000
65C5	Power Swing Detect Forward RCA	40 to 90	degrees	1	F001	75
65C6	Power Swing Detect Reverse Reach	0.1 to 500	ohms	0.01	F001	5000
65C7	Power Swing Detect Reverse RCA	40 to 90	degrees	1	F001	75
65C8	Power Swing Detect Outer Limit Angle	40 to 140	degrees	1	F001	120
65C9	Power Swing Detect Middle Limit Angle	40 to 140	degrees	1	F001	90
65CA	Power Swing Detect Inner Limit Angle	40 to 140	degrees	1	F001	60
65CB	Power Swing Detect Delay 1 Pickup	0 to 65.535	s	0.001	F001	30
65CC	Power Swing Detect Delay 1 Reset	0 to 65.535	s	0.001	F001	50
65CD	Power Swing Detect Delay 2 Pickup	0 to 65.535	s	0.001	F001	17
65CE	Power Swing Detect Delay 3 Pickup	0 to 65.535	s	0.001	F001	9
65CF	Power Swing Detect Delay 4 Pickup	0 to 65.535	s	0.001	F001	17
65D0	Power Swing Detect Seal In Delay	0 to 65.535	s	0.001	F001	400
65D1	Power Swing Detect Trip Mode	0 to 1	---	1	F514	0 (Delayed)
65D2	Power Swing Detect Block	0 to 65535	---	1	F300	0
65D3	Power Swing Detect Target	0 to 2	---	1	F109	0 (Self-reset)
65D4	Power Swing Detect Event	0 to 1	---	1	F102	0 (Disabled)
65D5	Power Swing Detect Shape	0 to 1	---	1	F085	0 (Mho Shape)
65D6	Power Swing Detect Quad Forward Middle	0.1 to 500	ohms	0.01	F001	6000
65D7	Power Swing Detect Quad Forward Outer	0.1 to 500	ohms	0.01	F001	7000
65D8	Power Swing Detect Quad Reverse Middle	0.1 to 500	ohms	0.01	F001	6000
65D9	Power Swing Detect Quad Reverse Outer	0.1 to 500	ohms	0.01	F001	7000
65DA	Power Swing Detect Outer Right Blinder	0.1 to 500	ohms	0.01	F001	10000
65DB	Power Swing Detect Outer Left Blinder	0.1 to 500	ohms	0.01	F001	10000
65DC	Power Swing Detect Middle Right Blinder	0.1 to 500	ohms	0.01	F001	10000
65DD	Power Swing Detect Middle Left Blinder	0.1 to 500	ohms	0.01	F001	10000
65DE	Power Swing Detect Inner Right Blinder	0.1 to 500	ohms	0.01	F001	10000
65DF	Power Swing Detect Inner Left Blinder	0.1 to 500	ohms	0.01	F001	10000
Sensitive Directional Power (Read/Write Grouped Setting) (2 modules)						
66A0	Sensitive Directional Power 1 Function	0 to 1	---	1	F102	0 (Disabled)
66A1	Sensitive Directional Power 1 Signal Source	0 to 5	---	1	F167	0 (SRC 1)
66A2	Sensitive Directional Power 1 RCA	0 to 359	degrees	1	F001	0
66A3	Sensitive Directional Power 1 Calibration	0 to 0.95	degrees	0.05	F001	0
66A4	Sensitive Directional Power 1 STG1 SMIN	-1.2 to 1.2	pu	0.001	F002	100
66A5	Sensitive Directional Power 1 STG1 Delay	0 to 600	s	0.01	F001	50

Table B-9: MODBUS MEMORY MAP (Sheet 18 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
66A6	Sensitive Directional Power 1 STG2 SMIN	-1.2 to 1.2	pu	0.001	F002	100
66A7	Sensitive Directional Power 1 STG2 Delay	0 to 600	s	0.01	F001	2000
66A8	Sensitive Directional Power 1 Block	---	---	---	F001	0
66A9	Sensitive Directional Power 1 Target	0 to 2	---	1	F109	0 (Self-reset)
66AA	Sensitive Directional Power 1 Events	0 to 1	---	1	F102	0 (Disabled)
66AB	Reserved (5 items)	0 to 65535	---	1	F001	0
66B0	...Repeated for Sensitive Directional Power 2					
Phase Undervoltage (Read/Write Grouped Setting) (2 modules)						
7000	Phase Undervoltage 1 Function	0 to 1	---	1	F102	0 (Disabled)
7001	Phase Undervoltage 1 Signal Source	0 to 5	---	1	F167	0 (SRC 1)
7002	Phase Undervoltage 1 Pickup	0 to 3	pu	0.001	F001	1000
7003	Phase Undervoltage 1 Curve	0 to 1	---	1	F111	0 (Definite Time)
7004	Phase Undervoltage 1 Delay	0 to 600	s	0.01	F001	100
7005	Phase Undervoltage 1 Minimum Voltage	0 to 3	pu	0.001	F001	100
7006	Phase Undervoltage 1 Block	0 to 65535	---	1	F300	0
7007	Phase Undervoltage 1 Target	0 to 2	---	1	F109	0 (Self-reset)
7008	Phase Undervoltage 1 Events	0 to 1	---	1	F102	0 (Disabled)
7009	Phase Undervoltage 1 Measurement Mode	0 to 1	---	1	F186	0 (Phase to Ground)
700A	Reserved (6 items)	0 to 1	---	1	F001	0
7013	...Repeated for Phase Undervoltage 2					
Phase Overvoltage (Read/Write Grouped Setting)						
7040	Phase Overvoltage 1 Function	0 to 1	---	1	F102	0 (Disabled)
7041	Phase Overvoltage 1 Source	0 to 5	---	1	F167	0 (SRC 1)
7042	Phase Overvoltage 1 Pickup	0 to 3	pu	0.001	F001	1000
7043	Phase Overvoltage 1 Delay	0 to 600	s	0.01	F001	100
7044	Phase Overvoltage 1 Reset Delay	0 to 600	s	0.01	F001	100
7045	Phase Overvoltage 1 Block	0 to 65535	---	1	F300	0
7046	Phase Overvoltage 1 Target	0 to 2	---	1	F109	0 (Self-reset)
7047	Phase Overvoltage 1 Events	0 to 1	---	1	F102	0 (Disabled)
7048	Reserved (8 items)	0 to 1	---	1	F001	0
DCMA Inputs (Read/Write Setting) (24 modules)						
7300	dcmA Inputs 1 Function	0 to 1	---	1	F102	0 (Disabled)
7301	dcmA Inputs 1 ID	---	---	---	F205	"DCMA I 1"
7307	Reserved 1 (4 items)	0 to 65535	---	1	F001	0
730B	dcmA Inputs 1 Units	---	---	---	F206	"mA"
730E	dcmA Inputs 1 Range	0 to 6	---	1	F173	6 (4 to 20 mA)
730F	dcmA Inputs 1 Minimum Value	-9999.999 to 9999.999	---	0.001	F004	4000
7311	dcmA Inputs 1 Maximum Value	-9999.999 to 9999.999	---	0.001	F004	20000
7313	Reserved (5 items)	0 to 65535	---	1	F001	0
7318	...Repeated for dcmA Inputs 2					
7330	...Repeated for dcmA Inputs 3					
7348	...Repeated for dcmA Inputs 4					
7360	...Repeated for dcmA Inputs 5					
7378	...Repeated for dcmA Inputs 6					
7390	...Repeated for dcmA Inputs 7					
73A8	...Repeated for dcmA Inputs 8					
73C0	...Repeated for dcmA Inputs 9					
73D8	...Repeated for dcmA Inputs 10					
73F0	...Repeated for dcmA Inputs 11					
7408	...Repeated for dcmA Inputs 12					
7420	...Repeated for dcmA Inputs 13					
7438	...Repeated for dcmA Inputs 14					
7450	...Repeated for dcmA Inputs 15					
7468	...Repeated for dcmA Inputs 16					

Table B–9: MODBUS MEMORY MAP (Sheet 19 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
7480	...Repeated for dcmA Inputs 17					
7498	...Repeated for dcmA Inputs 18					
74B0	...Repeated for dcmA Inputs 19					
74C8	...Repeated for dcmA Inputs 20					
74E0	...Repeated for dcmA Inputs 21					
74F8	...Repeated for dcmA Inputs 22					
7510	...Repeated for dcmA Inputs 23					
7528	...Repeated for dcmA Inputs 24					
RTD Inputs (Read/Write Setting) (48 modules)						
7540	RTD Input 1 Function	0 to 1	---	1	F102	0 (Disabled)
7541	RTD Input 1 ID	---	---	---	F205	"RTD Ip 1"
7547	Reserved (4 items)	0 to 65535	---	1	F001	0
754B	RTD Input 1 Type	0 to 3	---	1	F174	0 (100 Ohm Platinum)
754C	Reserved (4 items)	0 to 65535	---	1	F001	0
7550	...Repeated for RTD Input 2					
7560	...Repeated for RTD Input 3					
7570	...Repeated for RTD Input 4					
7580	...Repeated for RTD Input 5					
7590	...Repeated for RTD Input 6					
75A0	...Repeated for RTD Input 7					
75B0	...Repeated for RTD Input 8					
75C0	...Repeated for RTD Input 9					
75D0	...Repeated for RTD Input 10					
75E0	...Repeated for RTD Input 11					
75F0	...Repeated for RTD Input 12					
7600	...Repeated for RTD Input 13					
7610	...Repeated for RTD Input 14					
7620	...Repeated for RTD Input 15					
7630	...Repeated for RTD Input 16					
7640	...Repeated for RTD Input 17					
7650	...Repeated for RTD Input 18					
7660	...Repeated for RTD Input 19					
7670	...Repeated for RTD Input 20					
7680	...Repeated for RTD Input 21					
7690	...Repeated for RTD Input 22					
76A0	...Repeated for RTD Input 23					
76B0	...Repeated for RTD Input 24					
76C0	...Repeated for RTD Input 25					
76D0	...Repeated for RTD Input 26					
76E0	...Repeated for RTD Input 27					
76F0	...Repeated for RTD Input 28					
7700	...Repeated for RTD Input 29					
7710	...Repeated for RTD Input 30					
7720	...Repeated for RTD Input 31					
7730	...Repeated for RTD Input 32					
7740	...Repeated for RTD Input 33					
7750	...Repeated for RTD Input 34					
7760	...Repeated for RTD Input 35					
7770	...Repeated for RTD Input 36					
7780	...Repeated for RTD Input 37					
7790	...Repeated for RTD Input 38					
77A0	...Repeated for RTD Input 39					
77B0	...Repeated for RTD Input 40					
77C0	...Repeated for RTD Input 41					

Table B-9: MODBUS MEMORY MAP (Sheet 20 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
77D0	...Repeated for RTD Input 42					
77E0	...Repeated for RTD Input 43					
77F0	...Repeated for RTD Input 44					
7800	...Repeated for RTD Input 45					
7810	...Repeated for RTD Input 46					
7820	...Repeated for RTD Input 47					
7830	...Repeated for RTD Input 48					
Underfrequency (Read/Write Setting) (6 modules)						
7E00	Underfrequency Function	0 to 1	---	1	F102	0 (Disabled)
7E01	Underfrequency 1 Block	0 to 65535	---	1	F300	0
7E02	Underfrequency 1 Minimum Current	0.1 to 1.25	pu	0.01	F001	10
7E03	Underfrequency 1 Pickup	20 to 65	Hz	0.01	F001	5950
7E04	Underfrequency 1 Pickup Delay	0 to 65.535	s	0.001	F001	2000
7E05	Underfrequency 1 Reset Delay	0 to 65.535	s	0.001	F001	2000
7E06	Underfrequency 1 Source	0 to 5	---	1	F167	0 (SRC 1)
7E07	Underfrequency 1 Events	0 to 1	---	1	F102	0 (Disabled)
7E08	Underfrequency 1 Target	0 to 2	---	1	F109	0 (Self-reset)
7E09	Reserved (8 items)	0 to 1	---	1	F001	0
7E11	...Repeated for Underfrequency 2					
7E22	...Repeated for Underfrequency 3					
7E33	...Repeated for Underfrequency 4					
7E44	...Repeated for Underfrequency 5					
7E55	...Repeated for Underfrequency 6					
Frequency (Read Only)						
8000	Tracking Frequency	2 to 90	Hz	0.01	F001	0
Generic Comparator (Read/Write Setting) (6 modules)						
8010	Comparator 1 Function	0 to 1	---	1	F102	0 (Disabled)
8011	Comparator 1 Addition/Subtraction Mode Operand	0 to 65535	---	1	F300	0
8012	Comparator 1 Scale Factor	0.01 to 100	---	0.01	F001	100
8013	Comparator 1 Block	0 to 65535	---	1	F300	0
8014	Comparator 1 Target	0 to 2	---	1	F109	0 (Self-reset)
8015	Comparator 1 Events	0 to 1	---	1	F102	0 (Disabled)
8016	Comparator 1 Argument A Bits (8 items)	0 to 65535	---	1	F300	0
801E	Comparator 1 Input Mode	0 to 1	---	1	F515	0 (SIGNED)
801F	Comparator 1 Direction Mode	0 to 1	---	1	F517	0 (OVER)
8020	Comparator 1 Pickup	-25400 to 25400	---	0.01	F004	1000000
8022	Comparator 1 Hysteresis	0 to 5000	---	0.01	F003	50000
8024	Comparator 1 Pickup Delay	0 to 65.353	s	0.001	F001	0
8025	Comparator 1 Reset Delay	0 to 65.353	s	0.001	F001	0
8026	Comparator 1 Argument B Bits (8 items)	0 to 65535	---	1	F300	0
802C	Reserved (4 items)	0 to 65535	---	1	F001	0
8030	...Repeated for Comparator 2					
8050	...Repeated for Comparator 3					
8070	...Repeated for Comparator 4					
8090	...Repeated for Comparator 5					
80B0	...Repeated for Comparator 6					
Generic Comparator Actuals (Read Only) (6 modules)						
8100	Comparator 1 Argument A Value	-12700 to 12700	---	0.01	F004	0
8102	Comparator 1 Argument B Value	-12700 to 12700	---	0.01	F004	0
8104	Comparator 1 Output Value	-25400 to 25400	---	0.01	F004	0
8106	...Repeated for module number 2					
810C	...Repeated for module number 3					
8112	...Repeated for module number 4					
8118	...Repeated for module number 5					

Table B–9: MODBUS MEMORY MAP (Sheet 21 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
811E	...Repeated for module number 6					
Digitizer (Read/Write Setting) (5 modules)						
8200	Digitizer 1 Function	0 to 1	---	1	F102	0 (Disabled)
8201	Digitizer 1 Source Input	0 to 65535	---	1	F600	0
8202	Digitizer 1 Limit Setting	0.05 to 90	pu	0.001	F003	1000
8204	Reserved (4 items)	0 to 65535	---	1	F001	0
8208	...Repeated for Digitizer 2					
8210	...Repeated for Digitizer 3					
8218	...Repeated for Digitizer 4					
8220	...Repeated for Digitizer 5					
Digitizer Actuals (Read Only) (5 modules)						
82A0	Digitizer 1 Output	-127 to 127	---	1	F004	0
82A2	Digitizer 2 Output	-127 to 127	---	1	F004	0
82A4	Digitizer 3 Output	-127 to 127	---	1	F004	0
82A6	Digitizer 4 Output	-127 to 127	---	1	F004	0
82A8	Digitizer 5 Output	-127 to 127	---	1	F004	0
Switch (Read/Write Setting) (6 modules)						
8300	Switch 1 Function	0 to 1	---	1	F102	0 (Disabled)
8301	Switch 1 Argument A Bits (8 items)	0 to 65535	---	1	F300	0
8309	Switch 1 Argument B Bits (8 items)	0 to 65535	---	1	F300	0
8311	Switch 1 Control	0 to 65535	---	1	F300	0
8312	Reserved (6 items)	0 to 65535	---	1	F001	0
8318	...Repeated for Switch 2					
8330	...Repeated for Switch 3					
8348	...Repeated for Switch 4					
8360	...Repeated for Switch 5					
8378	...Repeated for Switch 6					
FlexState Settings (Read/Write Setting)						
8800	FlexState Parameters (256 items)	---	---	---	F300	0
Digital Elements (Read/Write Setting) (48 modules)						
8A00	Digital Element 1 Function	0 to 1	---	1	F102	0 (Disabled)
8A01	Digital Element 1 Name	---	---	---	F203	"Dig Element 1"
8A09	Digital Element 1 Input	0 to 65535	---	1	F300	0
8A0A	Digital Element 1 Pickup Delay	0 to 999999.999	s	0.001	F003	0
8A0C	Digital Element 1 Reset Delay	0 to 999999.999	s	0.001	F003	0
8A0E	Digital Element 1 Block	0 to 65535	---	1	F300	0
8A0F	Digital Element 1 Target	0 to 2	---	1	F109	0 (Self-reset)
8A10	Digital Element 1 Events	0 to 1	---	1	F102	0 (Disabled)
8A11	Digital Element 1 Pickup LED	0 to 1	---	1	F102	1 (Enabled)
8A12	Reserved (2 items)	---	---	---	F001	0
8A14	...Repeated for Digital Element 2					
8A28	...Repeated for Digital Element 3					
8A3C	...Repeated for Digital Element 4					
8A50	...Repeated for Digital Element 5					
8A64	...Repeated for Digital Element 6					
8A78	...Repeated for Digital Element 7					
8A8C	...Repeated for Digital Element 8					
8AA0	...Repeated for Digital Element 9					
8AB4	...Repeated for Digital Element 10					
8AC8	...Repeated for Digital Element 11					
8ADC	...Repeated for Digital Element 12					
8AF0	...Repeated for Digital Element 13					
8B04	...Repeated for Digital Element 14					
8B18	...Repeated for Digital Element 15					

Table B-9: MODBUS MEMORY MAP (Sheet 22 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
8B2C	...Repeated for Digital Element 16					
8B40	...Repeated for Digital Element 17					
8B54	...Repeated for Digital Element 18					
8B68	...Repeated for Digital Element 19					
8B7C	...Repeated for Digital Element 20					
8B90	...Repeated for Digital Element 21					
8BA4	...Repeated for Digital Element 22					
8BB8	...Repeated for Digital Element 23					
8BCC	...Repeated for Digital Element 24					
8BE0	...Repeated for Digital Element 25					
8BF4	...Repeated for Digital Element 26					
8C08	...Repeated for Digital Element 27					
8C1C	...Repeated for Digital Element 28					
8C30	...Repeated for Digital Element 29					
8C44	...Repeated for Digital Element 30					
8C58	...Repeated for Digital Element 31					
8C6C	...Repeated for Digital Element 32					
8C80	...Repeated for Digital Element 33					
8C94	...Repeated for Digital Element 34					
8CA8	...Repeated for Digital Element 35					
8CBC	...Repeated for Digital Element 36					
8CD0	...Repeated for Digital Element 37					
8CE4	...Repeated for Digital Element 38					
8CF8	...Repeated for Digital Element 39					
8D0C	...Repeated for Digital Element 40					
8D20	...Repeated for Digital Element 41					
8D34	...Repeated for Digital Element 42					
8D48	...Repeated for Digital Element 43					
8D5C	...Repeated for Digital Element 44					
8D70	...Repeated for Digital Element 45					
8D84	...Repeated for Digital Element 46					
8D98	...Repeated for Digital Element 47					
8DAC	...Repeated for Digital Element 48					
FlexElement (Read/Write Setting) (16 modules)						
9000	FlexElement™ 1 Function	0 to 1	---	1	F102	0 (Disabled)
9001	FlexElement™ 1 Name	---	---	---	F206	"FxE 1"
9004	FlexElement™ 1 InputP	0 to 65535	---	1	F600	0
9005	FlexElement™ 1 InputM	0 to 65535	---	1	F600	0
9006	FlexElement™ 1 Compare	0 to 1	---	1	F516	0 (LEVEL)
9007	FlexElement™ 1 Input	0 to 1	---	1	F515	0 (SIGNED)
9008	FlexElement™ 1 Direction	0 to 1	---	1	F517	0 (OVER)
9009	FlexElement™ 1 Hysteresis	0.1 to 50	%	0.1	F001	30
900A	FlexElement™ 1 Pickup	-90 to 90	pu	0.001	F004	1000
900C	FlexElement™ 1 DeltaT Units	0 to 2	---	1	F518	0 (Milliseconds)
900D	FlexElement™ 1 DeltaT	20 to 86400	---	1	F003	20
900F	FlexElement™ 1 Pickup Delay	0 to 65.535	s	0.001	F001	0
9010	FlexElement™ 1 Reset Delay	0 to 65.535	s	0.001	F001	0
9011	FlexElement™ 1 Block	0 to 65535	---	1	F300	0
9012	FlexElement™ 1 Target	0 to 2	---	1	F109	0 (Self-reset)
9013	FlexElement™ 1 Events	0 to 1	---	1	F102	0 (Disabled)
9014	...Repeated for FlexElement™ 2					
9028	...Repeated for FlexElement™ 3					
903C	...Repeated for FlexElement™ 4					
9050	...Repeated for FlexElement™ 5					

Table B–9: MODBUS MEMORY MAP (Sheet 23 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
9064	...Repeated for FlexElement™ 6					
9078	...Repeated for FlexElement™ 7					
908C	...Repeated for FlexElement™ 8					
90A0	...Repeated for FlexElement™ 9					
90B4	...Repeated for FlexElement™ 10					
90C8	...Repeated for FlexElement™ 11					
90DC	...Repeated for FlexElement™ 12					
90F0	...Repeated for FlexElement™ 13					
9104	...Repeated for FlexElement™ 14					
9118	...Repeated for FlexElement™ 15					
912C	...Repeated for FlexElement™ 16					
DCMA Outputs (Read/Write Setting) (24 modules)						
9300	dcmA Output 1 Source	0 to 65535	---	1	F600	0
9301	dcmA Output 1 Range	0 to 2	---	1	F522	0 (–1 to 1 mA)
9302	dcmA Output 1 Minimum	–90 to 90	pu	0.001	F004	0
9304	dcmA Output 1 Maximum	–90 to 90	pu	0.001	F004	1000
9306	...Repeated for dcmA Output 2					
930C	...Repeated for dcmA Output 3					
9312	...Repeated for dcmA Output 4					
9318	...Repeated for dcmA Output 5					
931E	...Repeated for dcmA Output 6					
9324	...Repeated for dcmA Output 7					
932A	...Repeated for dcmA Output 8					
9330	...Repeated for dcmA Output 9					
9336	...Repeated for dcmA Output 10					
933C	...Repeated for dcmA Output 11					
9342	...Repeated for dcmA Output 12					
9348	...Repeated for dcmA Output 13					
934E	...Repeated for dcmA Output 14					
9354	...Repeated for dcmA Output 15					
935A	...Repeated for dcmA Output 16					
9360	...Repeated for dcmA Output 17					
9366	...Repeated for dcmA Output 18					
936C	...Repeated for dcmA Output 19					
9372	...Repeated for dcmA Output 20					
9378	...Repeated for dcmA Output 21					
937E	...Repeated for dcmA Output 22					
9384	...Repeated for dcmA Output 23					
938A	...Repeated for dcmA Output 24					
Direct Input/Output Names (Read/Write Setting) (96 modules)						
9400	Direct Input 1 Name	0 to 96	---	1	F205	"Dir Ip 1"
9406	Direct Output 1 Name	1 to 96	---	1	F205	"Dir Out 1"
940C	...Repeated for Direct Input/Output 2					
9418	...Repeated for Direct Input/Output 3					
9424	...Repeated for Direct Input/Output 4					
9430	...Repeated for Direct Input/Output 5					
943C	...Repeated for Direct Input/Output 6					
9448	...Repeated for Direct Input/Output 7					
9454	...Repeated for Direct Input/Output 8					
9460	...Repeated for Direct Input/Output 9					
946C	...Repeated for Direct Input/Output 10					
9478	...Repeated for Direct Input/Output 11					
9484	...Repeated for Direct Input/Output 12					
9490	...Repeated for Direct Input/Output 13					

Table B-9: MODBUS MEMORY MAP (Sheet 24 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
949C	...Repeated for Direct Input/Output 14					
94A8	...Repeated for Direct Input/Output 15					
94B4	...Repeated for Direct Input/Output 16					
94C0	...Repeated for Direct Input/Output 17					
94CC	...Repeated for Direct Input/Output 18					
94D8	...Repeated for Direct Input/Output 19					
94E4	...Repeated for Direct Input/Output 20					
94F0	...Repeated for Direct Input/Output 21					
94FC	...Repeated for Direct Input/Output 22					
9508	...Repeated for Direct Input/Output 23					
9514	...Repeated for Direct Input/Output 24					
9520	...Repeated for Direct Input/Output 25					
952C	...Repeated for Direct Input/Output 26					
9538	...Repeated for Direct Input/Output 27					
9544	...Repeated for Direct Input/Output 28					
9550	...Repeated for Direct Input/Output 29					
955C	...Repeated for Direct Input/Output 30					
9568	...Repeated for Direct Input/Output 31					
9574	...Repeated for Direct Input/Output 32					
9580	...Repeated for Direct Input/Output 33					
958C	...Repeated for Direct Input/Output 34					
9598	...Repeated for Direct Input/Output 35					
95A4	...Repeated for Direct Input/Output 36					
95B0	...Repeated for Direct Input/Output 37					
95BC	...Repeated for Direct Input/Output 38					
95C8	...Repeated for Direct Input/Output 39					
95D4	...Repeated for Direct Input/Output 40					
95E0	...Repeated for Direct Input/Output 41					
95EC	...Repeated for Direct Input/Output 42					
95F8	...Repeated for Direct Input/Output 43					
9604	...Repeated for Direct Input/Output 44					
9610	...Repeated for Direct Input/Output 45					
961C	...Repeated for Direct Input/Output 46					
9628	...Repeated for Direct Input/Output 47					
9634	...Repeated for Direct Input/Output 48					
9640	...Repeated for Direct Input/Output 49					
964C	...Repeated for Direct Input/Output 50					
9658	...Repeated for Direct Input/Output 51					
9664	...Repeated for Direct Input/Output 52					
9670	...Repeated for Direct Input/Output 53					
967C	...Repeated for Direct Input/Output 54					
9688	...Repeated for Direct Input/Output 55					
9694	...Repeated for Direct Input/Output 56					
96A0	...Repeated for Direct Input/Output 57					
96AC	...Repeated for Direct Input/Output 58					
96B8	...Repeated for Direct Input/Output 59					
96C4	...Repeated for Direct Input/Output 60					
96D0	...Repeated for Direct Input/Output 61					
96DC	...Repeated for Direct Input/Output 62					
96E8	...Repeated for Direct Input/Output 63					
96F4	...Repeated for Direct Input/Output 64					
FlexElement Actuals (Read Only) (16 modules)						
9A01	FlexElement™ 1 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A03	FlexElement™ 2 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0

Table B–9: MODBUS MEMORY MAP (Sheet 25 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
9A05	FlexElement™ 3 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A07	FlexElement™ 4 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A09	FlexElement™ 5 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A0B	FlexElement™ 6 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A0D	FlexElement™ 7 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A0F	FlexElement™ 8 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A11	FlexElement™ 9 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A13	FlexElement™ 10 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A15	FlexElement™ 11 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A17	FlexElement™ 12 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A19	FlexElement™ 13 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A1B	FlexElement™ 14 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A1D	FlexElement™ 15 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
9A1F	FlexElement™ 16 Actual	-2147483.647 to 2147483.647	---	0.001	F004	0
VT Fuse Failure (Read/Write Setting) (6 modules)						
A040	VT Fuse Failure Function	0 to 1	---	1	F102	0 (Disabled)
A041	...Repeated for module number 2					
A042	...Repeated for module number 3					
A043	...Repeated for module number 4					
A044	...Repeated for module number 5					
A045	...Repeated for module number 6					
Selector Switch Actuals (Read Only)						
A400	Selector 1 Position	1 to 7	---	1	F001	0
A401	Selector 2 Position	1 to 7	---	1	F001	1
Selector Switch (Read/Write Setting) (2 modules)						
A410	Selector 1 Function	0 to 1	---	1	F102	0 (Disabled)
A411	Selector 1 Range	1 to 7	---	1	F001	7
A412	Selector 1 Timeout	3 to 60	s	0.1	F001	50
A413	Selector 1 Step Up	0 to 65535	---	1	F300	0
A414	Selector 1 Step Mode	0 to 1	---	1	F083	0 (Time-out)
A415	Selector 1 Acknowledge	0 to 65535	---	1	F300	0
A416	Selector 1 Bit0	0 to 65535	---	1	F300	0
A417	Selector 1 Bit1	0 to 65535	---	1	F300	0
A418	Selector 1 Bit2	0 to 65535	---	1	F300	0
A419	Selector 1 Bit Mode	0 to 1	---	1	F083	0 (Time-out)
A41A	Selector 1 Bit Acknowledge	0 to 65535	---	1	F300	0
A41B	Selector 1 Power Up Mode	0 to 2	---	1	F084	0 (Restore)
A41C	Selector 1 Target	0 to 2	---	1	F109	0 (Self-reset)
A41D	Selector 1 Events	0 to 1	---	1	F102	0 (Disabled)
A41E	Reserved (10 items)	---	---	1	F001	0
A428	...Repeated for Selector 2					
DNP/IEC Points (Read/Write Setting)						
A500	DNP/IEC 60870-5-104 Binary Input Points (256 items)	0 to 65535	---	1	F300	0
A600	DNP/IEC 60870-5-104 Analog Input Points (256 items)	0 to 65535	---	1	F300	0
Non Volatile Latches (Read/Write Setting) (16 modules)						
AA00	Non-Volatile Latch 1 Function	0 to 1	---	1	F102	0 (Disabled)
AA01	Non-Volatile Latch 1 Type	0 to 1	---	1	F519	0 (Reset Dominant)
AA02	Non-Volatile Latch 1 Set	0 to 65535	---	1	F300	0
AA03	Non-Volatile Latch 1 Reset	0 to 65535	---	1	F300	0
AA04	Non-Volatile Latch 1 Target	0 to 2	---	1	F109	0 (Self-reset)
AA05	Non-Volatile Latch 1 Events	0 to 1	---	1	F102	0 (Disabled)
AA06	Reserved (4 items)	---	---	---	F001	0
AA0A	...Repeated for Non-Volatile Latch 2					
AA14	...Repeated for Non-Volatile Latch 3					

Table B–9: MODBUS MEMORY MAP (Sheet 26 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
AA1E	...Repeated for Non-Volatile Latch 4					
AA28	...Repeated for Non-Volatile Latch 5					
AA32	...Repeated for Non-Volatile Latch 6					
AA3C	...Repeated for Non-Volatile Latch 7					
AA46	...Repeated for Non-Volatile Latch 8					
AA50	...Repeated for Non-Volatile Latch 9					
AA5A	...Repeated for Non-Volatile Latch 10					
AA64	...Repeated for Non-Volatile Latch 11					
AA6E	...Repeated for Non-Volatile Latch 12					
AA78	...Repeated for Non-Volatile Latch 13					
AA82	...Repeated for Non-Volatile Latch 14					
AA8C	...Repeated for Non-Volatile Latch 15					
AA96	...Repeated for Non-Volatile Latch 16					
Digital Counter (Read/Write Setting) (8 modules)						
AB00	Digital Counter 1 Function	0 to 1	---	1	F102	0 (Disabled)
AB01	Digital Counter 1 Name	---	---	---	F205	"Counter 1"
AB07	Digital Counter 1 Units	---	---	---	F206	(none)
AB0A	Digital Counter 1 Block	0 to 65535	---	1	F300	0
AB0B	Digital Counter 1 Up	0 to 65535	---	1	F300	0
AB0C	Digital Counter 1 Down	0 to 65535	---	1	F300	0
AB0D	Digital Counter 1 Preset	-2147483647 to 2147483647	---	1	F004	0
AB0F	Digital Counter 1 Compare	-2147483647 to 2147483647	---	1	F004	0
AB11	Digital Counter 1 Reset	0 to 65535	---	1	F300	0
AB12	Digital Counter 1 Freeze/Reset	0 to 65535	---	1	F300	0
AB13	Digital Counter 1 Freeze/Count	0 to 65535	---	1	F300	0
AB14	Digital Counter 1 Set To Preset	0 to 65535	---	1	F300	0
AB15	Reserved (11 items)	---	---	---	F001	0
AB20	...Repeated for Digital Counter 2					
AB40	...Repeated for Digital Counter 3					
AB60	...Repeated for Digital Counter 4					
AB80	...Repeated for Digital Counter 5					
ABA0	...Repeated for Digital Counter 6					
ABC0	...Repeated for Digital Counter 7					
ABE0	...Repeated for Digital Counter 8					
Frequency Rate of Change (Read/Write Setting) (4 modules)						
AC00	Frequency Rate of Change 1 Function	0 to 1	---	1	F102	0 (Disabled)
AC01	Frequency Rate of Change 1 OC Supervision	0 to 30	pu	0.001	F001	200
AC02	Frequency Rate of Change 1 Min	20 to 80	Hz	0.01	F001	4500
AC03	Frequency Rate of Change 1 Max	20 to 80	Hz	0.01	F001	6500
AC04	Frequency Rate of Change 1 Pickup Delay	0 to 65.535	s	0.001	F001	0
AC05	Frequency Rate of Change 1 Reset Delay	0 to 65.535	s	0.001	F001	0
AC06	Frequency Rate of Change 1 Block	0 to 65535	---	1	F300	0
AC07	Frequency Rate of Change 1 Target	0 to 2	---	1	F109	0 (Self-reset)
AC08	Frequency Rate of Change 1 Events	0 to 1	---	1	F102	0 (Disabled)
AC09	Frequency Rate of Change 1 Source	0 to 5	---	1	F167	0 (SRC 1)
AC0A	Frequency Rate of Change 1 Trend	0 to 2	---	1	F224	0 (Increasing)
AC0B	Frequency Rate of Change 1 Pickup	0.1 to 15	Hz/s	0.01	F001	50
AC0C	Frequency Rate of Change 1 OV Supervision	0.1 to 3	pu	0.001	F001	700
AC0D	Frequency Rate of Change 1 Reserved (3 items)	0 to 1	---	1	F001	0
AC10	...Repeated for Frequency Rate of Change 2					
AC20	...Repeated for Frequency Rate of Change 3					
AC30	...Repeated for Frequency Rate of Change 4					
IEC 61850 GSSE Configuration (Read/Write Setting)						
AD80	Default GSSE Update Time	1 to 60	s	1	F001	60

Table B–9: MODBUS MEMORY MAP (Sheet 27 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
AD81	Remote Input/Output Transfer Method	0 to 2	---	1	F226	1 (GSSE)
AD82	IEC 61850 GOOSE VLAN Transmit Priority	0 to 7	---	1	F001	4
AD83	IEC 61850 GOOSE VLAN ID	0 to 4095	---	1	F001	0
AD84	IEC 61850 GOOSE ETYPE APPID	0 to 16383	---	1	F001	0
AD85	Reserved (22 items)	0 to 1	---	1	F001	0
IEC 61850 Server Configuration (Read/Write Settings/Commands)						
ADA0	TCP Port Number for the IEC 61850 Protocol	1 to 65535	---	1	F001	102
ADA1	IEC 61850 Logical Device Name	---	---	---	F213	"IECDevice"
ADB1	Include Non-IEC 61850 Data	0 to 1	---	1	F102	1 (Enabled)
ADB2	Number of Status Indications in GGIO1	8 to 128	---	8	F001	8
ADB3	IEC 61850 Server Data Scanning Function	0 to 1	---	1	F102	0 (Disabled)
ADB4	Command to Clear XCBR1 OpCnt Counter	0 to 1	---	1	F126	0 (No)
ADB5	Command to Clear XCBR2 OpCnt Counter	0 to 1	---	1	F126	0 (No)
ADB6	Reserved (10 items)	0 to 1	---	1	F001	0
IEC 61850 Logical Node Name Prefixes (Read/Write Setting)						
AE00	IEC 61850 Logical Node PIOCx Name Prefix (72 items)	0 to 65534	---	1	F206	(None)
AED8	IEC 61850 Logical Node PTOCx Name Prefix (24 items)	0 to 65534	---	1	F206	(None)
AF20	IEC 61850 Logical Node PTUVx Name Prefix (12 items)	0 to 65534	---	1	F206	(None)
AF44	IEC 61850 Logical Node PTOVx Name Prefix (8 items)	0 to 65534	---	1	F206	(None)
AF5C	IEC 61850 Logical Node PDISx Name Prefix (10 items)	0 to 65534	---	1	F206	(None)
AF7A	IEC 61850 Logical Node RRBfx Name Prefix (24 items)	0 to 65534	---	1	F206	(None)
AFC2	IEC 61850 Logical Node RPSBx Name Prefix	0 to 65534	---	1	F206	(None)
AFC5	IEC 61850 Logical Node RREcx Name Prefix (6 items)	0 to 65534	---	1	F206	(None)
AFD7	IEC 61850 Logical Node MMXUx Name Prefix (6 items)	0 to 65534	---	1	F206	(None)
AFE9	IEC 61850 Logical Node GGIOx Name Prefix (2 items)	0 to 65534	---	1	F206	(None)
AFEF	IEC 61850 Logical Node RFLOx Name Prefix (5 items)	0 to 65534	---	1	F206	(None)
AFFE	IEC 61850 Logical Node XCBRx Name Prefix (2 items)	0 to 65534	---	1	F206	(None)
B004	IEC 61850 Logical Node PTRCx Name Prefix (2 items)	0 to 65534	---	1	F206	(None)
B00A	IEC 61850 Logical Node PDIFx Name Prefix (4 items)	0 to 65534	---	1	F206	(None)
B016	IEC 61850 Logical Node MMXNx Name Prefix (37 items)	0 to 65534	---	1	F206	(None)
IEC 61850 MMXU Deadbands (Read/Write Setting) (6 modules)						
B100	IEC 61850 MMXU TotW Deadband 1	0.001 to 100	%	0.001	F003	10000
B102	IEC 61850 MMXU TotVAr Deadband 1	0.001 to 100	%	0.001	F003	10000
B104	IEC 61850 MMXU TotVA Deadband 1	0.001 to 100	%	0.001	F003	10000
B106	IEC 61850 MMXU TotPF Deadband 1	0.001 to 100	%	0.001	F003	10000
B108	IEC 61850 MMXU Hz Deadband 1	0.001 to 100	%	0.001	F003	10000
B10A	IEC 61850 MMXU PPV.phsAB Deadband 1	0.001 to 100	%	0.001	F003	10000
B10C	IEC 61850 MMXU PPV.phsBC Deadband 1	0.001 to 100	%	0.001	F003	10000
B10E	IEC 61850 MMXU PPV.phsCA Deadband 1	0.001 to 100	%	0.001	F003	10000
B110	IEC 61850 MMXU PhV.phsADeaband 1	0.001 to 100	%	0.001	F003	10000
B112	IEC 61850 MMXU PhV.phsB Deadband 1	0.001 to 100	%	0.001	F003	10000
B114	IEC 61850 MMXU PhV.phsC Deadband 1	0.001 to 100	%	0.001	F003	10000
B116	IEC 61850 MMXU A.phsA Deadband 1	0.001 to 100	%	0.001	F003	10000
B118	IEC 61850 MMXU A.phsB Deadband 1	0.001 to 100	%	0.001	F003	10000
B11A	IEC 61850 MMXU A.phsC Deadband 1	0.001 to 100	%	0.001	F003	10000
B11C	IEC 61850 MMXU A.neut Deadband 1	0.001 to 100	%	0.001	F003	10000
B11E	IEC 61850 MMXU W.phsA Deadband 1	0.001 to 100	%	0.001	F003	10000
B120	IEC 61850 MMXU W.phsB Deadband 1	0.001 to 100	%	0.001	F003	10000
B122	IEC 61850 MMXU W.phsC Deadband 1	0.001 to 100	%	0.001	F003	10000
B124	IEC 61850 MMXU VAr.phsA Deadband 1	0.001 to 100	%	0.001	F003	10000
B126	IEC 61850 MMXU VAr.phsB Deadband 1	0.001 to 100	%	0.001	F003	10000
B128	IEC 61850 MMXU VAr.phsC Deadband 1	0.001 to 100	%	0.001	F003	10000
B12A	IEC 61850 MMXU VA.phsA Deadband 1	0.001 to 100	%	0.001	F003	10000
B12C	IEC 61850 MMXU VA.phsB Deadband 1	0.001 to 100	%	0.001	F003	10000

Table B-9: MODBUS MEMORY MAP (Sheet 28 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
B12E	IEC 61850 MMXU VA.phsC Deadband 1	0.001 to 100	%	0.001	F003	10000
B130	IEC 61850 MMXU PF.phsA Deadband 1	0.001 to 100	%	0.001	F003	10000
B132	IEC 61850 MMXU PF.phsB Deadband 1	0.001 to 100	%	0.001	F003	10000
B134	IEC 61850 MMXU PF.phsC Deadband 1	0.001 to 100	%	0.001	F003	10000
B136	...Repeated for Deadband 2					
B16C	...Repeated for Deadband 3					
B1A2	...Repeated for Deadband 4					
B1D8	...Repeated for Deadband 5					
B20E	...Repeated for Deadband 6					
IEC 61850 GGIO2 Control Configuration (Read/Write Setting) (64 modules)						
B300	IEC 61850 GGIO2.CF.SPCSO1.cttlModel Value	0 to 2	---	1	F001	2
B301	IEC 61850 GGIO2.CF.SPCSO2.cttlModel Value	0 to 2	---	1	F001	2
B302	IEC 61850 GGIO2.CF.SPCSO3.cttlModel Value	0 to 2	---	1	F001	2
B303	IEC 61850 GGIO2.CF.SPCSO4.cttlModel Value	0 to 2	---	1	F001	2
B304	IEC 61850 GGIO2.CF.SPCSO5.cttlModel Value	0 to 2	---	1	F001	2
B305	IEC 61850 GGIO2.CF.SPCSO6.cttlModel Value	0 to 2	---	1	F001	2
B306	IEC 61850 GGIO2.CF.SPCSO7.cttlModel Value	0 to 2	---	1	F001	2
B307	IEC 61850 GGIO2.CF.SPCSO8.cttlModel Value	0 to 2	---	1	F001	2
B308	IEC 61850 GGIO2.CF.SPCSO9.cttlModel Value	0 to 2	---	1	F001	2
B309	IEC 61850 GGIO2.CF.SPCSO10.cttlModel Value	0 to 2	---	1	F001	2
B30A	IEC 61850 GGIO2.CF.SPCSO11.cttlModel Value	0 to 2	---	1	F001	2
B30B	IEC 61850 GGIO2.CF.SPCSO12.cttlModel Value	0 to 2	---	1	F001	2
B30C	IEC 61850 GGIO2.CF.SPCSO13.cttlModel Value	0 to 2	---	1	F001	2
B30D	IEC 61850 GGIO2.CF.SPCSO14.cttlModel Value	0 to 2	---	1	F001	2
B30E	IEC 61850 GGIO2.CF.SPCSO15.cttlModel Value	0 to 2	---	1	F001	2
B30F	IEC 61850 GGIO2.CF.SPCSO16.cttlModel Value	0 to 2	---	1	F001	2
B310	IEC 61850 GGIO2.CF.SPCSO17.cttlModel Value	0 to 2	---	1	F001	2
B311	IEC 61850 GGIO2.CF.SPCSO18.cttlModel Value	0 to 2	---	1	F001	2
B312	IEC 61850 GGIO2.CF.SPCSO19.cttlModel Value	0 to 2	---	1	F001	2
B313	IEC 61850 GGIO2.CF.SPCSO20.cttlModel Value	0 to 2	---	1	F001	2
B314	IEC 61850 GGIO2.CF.SPCSO21.cttlModel Value	0 to 2	---	1	F001	2
B315	IEC 61850 GGIO2.CF.SPCSO22.cttlModel Value	0 to 2	---	1	F001	2
B316	IEC 61850 GGIO2.CF.SPCSO23.cttlModel Value	0 to 2	---	1	F001	2
B317	IEC 61850 GGIO2.CF.SPCSO24.cttlModel Value	0 to 2	---	1	F001	2
B318	IEC 61850 GGIO2.CF.SPCSO25.cttlModel Value	0 to 2	---	1	F001	2
B319	IEC 61850 GGIO2.CF.SPCSO26.cttlModel Value	0 to 2	---	1	F001	2
B31A	IEC 61850 GGIO2.CF.SPCSO27.cttlModel Value	0 to 2	---	1	F001	2
B31B	IEC 61850 GGIO2.CF.SPCSO28.cttlModel Value	0 to 2	---	1	F001	2
B31C	IEC 61850 GGIO2.CF.SPCSO29.cttlModel Value	0 to 2	---	1	F001	2
B31D	IEC 61850 GGIO2.CF.SPCSO30.cttlModel Value	0 to 2	---	1	F001	2
B31E	IEC 61850 GGIO2.CF.SPCSO31.cttlModel Value	0 to 2	---	1	F001	2
B31F	IEC 61850 GGIO2.CF.SPCSO32.cttlModel Value	0 to 2	---	1	F001	2
BC20	IEC 61850 GGIO2.CF.SPCSO33.cttlModel Value	0 to 2	---	1	F001	2
BC21	IEC 61850 GGIO2.CF.SPCSO34.cttlModel Value	0 to 2	---	1	F001	2
BC22	IEC 61850 GGIO2.CF.SPCSO35.cttlModel Value	0 to 2	---	1	F001	2
BC23	IEC 61850 GGIO2.CF.SPCSO36.cttlModel Value	0 to 2	---	1	F001	2
BC24	IEC 61850 GGIO2.CF.SPCSO37.cttlModel Value	0 to 2	---	1	F001	2
BC25	IEC 61850 GGIO2.CF.SPCSO38.cttlModel Value	0 to 2	---	1	F001	2
BC26	IEC 61850 GGIO2.CF.SPCSO39.cttlModel Value	0 to 2	---	1	F001	2
BC27	IEC 61850 GGIO2.CF.SPCSO40.cttlModel Value	0 to 2	---	1	F001	2
BC28	IEC 61850 GGIO2.CF.SPCSO41.cttlModel Value	0 to 2	---	1	F001	2
BC29	IEC 61850 GGIO2.CF.SPCSO42.cttlModel Value	0 to 2	---	1	F001	2
BC2A	IEC 61850 GGIO2.CF.SPCSO43.cttlModel Value	0 to 2	---	1	F001	2
BC2B	IEC 61850 GGIO2.CF.SPCSO44.cttlModel Value	0 to 2	---	1	F001	2

Table B–9: MODBUS MEMORY MAP (Sheet 29 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
BC2C	IEC 61850 GGIO2.CF.SPCSO45.ctilModel Value	0 to 2	---	1	F001	2
BC2D	IEC 61850 GGIO2.CF.SPCSO46.ctilModel Value	0 to 2	---	1	F001	2
BC2E	IEC 61850 GGIO2.CF.SPCSO47.ctilModel Value	0 to 2	---	1	F001	2
BC2F	IEC 61850 GGIO2.CF.SPCSO48.ctilModel Value	0 to 2	---	1	F001	2
BC30	IEC 61850 GGIO2.CF.SPCSO49.ctilModel Value	0 to 2	---	1	F001	2
BC31	IEC 61850 GGIO2.CF.SPCSO50.ctilModel Value	0 to 2	---	1	F001	2
BC32	IEC 61850 GGIO2.CF.SPCSO51.ctilModel Value	0 to 2	---	1	F001	2
BC33	IEC 61850 GGIO2.CF.SPCSO52.ctilModel Value	0 to 2	---	1	F001	2
BC34	IEC 61850 GGIO2.CF.SPCSO53.ctilModel Value	0 to 2	---	1	F001	2
BC35	IEC 61850 GGIO2.CF.SPCSO54.ctilModel Value	0 to 2	---	1	F001	2
BC36	IEC 61850 GGIO2.CF.SPCSO55.ctilModel Value	0 to 2	---	1	F001	2
BC37	IEC 61850 GGIO2.CF.SPCSO56.ctilModel Value	0 to 2	---	1	F001	2
BC38	IEC 61850 GGIO2.CF.SPCSO57.ctilModel Value	0 to 2	---	1	F001	2
BC39	IEC 61850 GGIO2.CF.SPCSO58.ctilModel Value	0 to 2	---	1	F001	2
BC3A	IEC 61850 GGIO2.CF.SPCSO59.ctilModel Value	0 to 2	---	1	F001	2
BC3B	IEC 61850 GGIO2.CF.SPCSO60.ctilModel Value	0 to 2	---	1	F001	2
BC3C	IEC 61850 GGIO2.CF.SPCSO61.ctilModel Value	0 to 2	---	1	F001	2
BC3D	IEC 61850 GGIO2.CF.SPCSO62.ctilModel Value	0 to 2	---	1	F001	2
BC3E	IEC 61850 GGIO2.CF.SPCSO63.ctilModel Value	0 to 2	---	1	F001	2
BC3F	IEC 61850 GGIO2.CF.SPCSO64.ctilModel Value	0 to 2	---	1	F001	2
Contact Inputs (Read/Write Setting) (96 modules)						
BB00	Contact Input 1 Name	---	---	---	F205	"Cont Ip 1"
BB06	Contact Input 1 Events	0 to 1	---	1	F102	0 (Disabled)
BB07	Contact Input 1 Debounce Time	0 to 16	ms	0.5	F001	20
BB08	...Repeated for Contact Input 2					
BB10	...Repeated for Contact Input 3					
BB18	...Repeated for Contact Input 4					
BB20	...Repeated for Contact Input 5					
BB28	...Repeated for Contact Input 6					
BB30	...Repeated for Contact Input 7					
BB38	...Repeated for Contact Input 8					
BB40	...Repeated for Contact Input 9					
BB48	...Repeated for Contact Input 10					
BB50	...Repeated for Contact Input 11					
BB58	...Repeated for Contact Input 12					
BB60	...Repeated for Contact Input 13					
BB68	...Repeated for Contact Input 14					
BB70	...Repeated for Contact Input 15					
BB78	...Repeated for Contact Input 16					
BB80	...Repeated for Contact Input 17					
BB88	...Repeated for Contact Input 18					
BB90	...Repeated for Contact Input 19					
BB98	...Repeated for Contact Input 20					
BBA0	...Repeated for Contact Input 21					
BBA8	...Repeated for Contact Input 22					
BBB0	...Repeated for Contact Input 23					
BBB8	...Repeated for Contact Input 24					
BBC0	...Repeated for Contact Input 25					
BBC8	...Repeated for Contact Input 26					
BBD0	...Repeated for Contact Input 27					
BBD8	...Repeated for Contact Input 28					
BBE0	...Repeated for Contact Input 29					
BBE8	...Repeated for Contact Input 30					
BBF0	...Repeated for Contact Input 31					

Table B-9: MODBUS MEMORY MAP (Sheet 30 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
BBF8	...Repeated for Contact Input 32					
BC00	...Repeated for Contact Input 33					
BC08	...Repeated for Contact Input 34					
BC10	...Repeated for Contact Input 35					
BC18	...Repeated for Contact Input 36					
BC20	...Repeated for Contact Input 37					
BC28	...Repeated for Contact Input 38					
BC30	...Repeated for Contact Input 39					
BC38	...Repeated for Contact Input 40					
BC40	...Repeated for Contact Input 41					
BC48	...Repeated for Contact Input 42					
BC50	...Repeated for Contact Input 43					
BC58	...Repeated for Contact Input 44					
BC60	...Repeated for Contact Input 45					
BC68	...Repeated for Contact Input 46					
BC70	...Repeated for Contact Input 47					
BC78	...Repeated for Contact Input 48					
BC80	...Repeated for Contact Input 49					
BC88	...Repeated for Contact Input 50					
BC90	...Repeated for Contact Input 51					
BC98	...Repeated for Contact Input 52					
BCA0	...Repeated for Contact Input 53					
BCA8	...Repeated for Contact Input 54					
BCB0	...Repeated for Contact Input 55					
BCB8	...Repeated for Contact Input 56					
BCC0	...Repeated for Contact Input 57					
BCC8	...Repeated for Contact Input 58					
BCD0	...Repeated for Contact Input 59					
BCD8	...Repeated for Contact Input 60					
BCE0	...Repeated for Contact Input 61					
BCE8	...Repeated for Contact Input 62					
BCF0	...Repeated for Contact Input 63					
BCF8	...Repeated for Contact Input 64					
BD00	...Repeated for Contact Input 65					
BD08	...Repeated for Contact Input 66					
BD10	...Repeated for Contact Input 67					
BD18	...Repeated for Contact Input 68					
BD20	...Repeated for Contact Input 69					
BD28	...Repeated for Contact Input 70					
BD30	...Repeated for Contact Input 71					
BD38	...Repeated for Contact Input 72					
BD40	...Repeated for Contact Input 73					
BD48	...Repeated for Contact Input 74					
BD50	...Repeated for Contact Input 75					
BD58	...Repeated for Contact Input 76					
BD60	...Repeated for Contact Input 77					
BD68	...Repeated for Contact Input 78					
BD70	...Repeated for Contact Input 79					
BD78	...Repeated for Contact Input 80					
BD80	...Repeated for Contact Input 81					
BD88	...Repeated for Contact Input 82					
BD90	...Repeated for Contact Input 83					
BD98	...Repeated for Contact Input 84					
BDA0	...Repeated for Contact Input 85					

Table B–9: MODBUS MEMORY MAP (Sheet 31 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
BDA8	...Repeated for Contact Input 86					
BDB0	...Repeated for Contact Input 87					
BDB8	...Repeated for Contact Input 88					
BDC0	...Repeated for Contact Input 89					
BDC8	...Repeated for Contact Input 90					
BDD0	...Repeated for Contact Input 91					
BDD8	...Repeated for Contact Input 92					
BDE0	...Repeated for Contact Input 93					
BDE8	...Repeated for Contact Input 94					
BDF0	...Repeated for Contact Input 95					
BDF8	...Repeated for Contact Input 96					
Contact Input Thresholds (Read/Write Setting)						
BE00	Contact Input n Threshold, $n = 1$ to 24 (24 items)	0 to 3	---	1	F128	1 (33 Vdc)
Virtual Inputs (Read/Write Setting) (64 modules)						
BE90	Virtual Input 1 Function	0 to 1	---	1	F102	0 (Disabled)
BE91	Virtual Input 1 Name	---	---	---	F205	"Virt Ip 1"
BE9B	Virtual Input 1 Programmed Type	0 to 1	---	1	F127	0 (Latched)
BE9C	Virtual Input 1 Events	0 to 1	---	1	F102	0 (Disabled)
BE9D	Reserved (3 items)	---	---	---	F001	0
BEA0	...Repeated for Virtual Input 2					
BEB0	...Repeated for Virtual Input 3					
BEC0	...Repeated for Virtual Input 4					
BED0	...Repeated for Virtual Input 5					
BEE0	...Repeated for Virtual Input 6					
BEF0	...Repeated for Virtual Input 7					
BF00	...Repeated for Virtual Input 8					
BF10	...Repeated for Virtual Input 9					
BF20	...Repeated for Virtual Input 10					
BF30	...Repeated for Virtual Input 11					
BF40	...Repeated for Virtual Input 12					
BF50	...Repeated for Virtual Input 13					
BF60	...Repeated for Virtual Input 14					
BF70	...Repeated for Virtual Input 15					
BF80	...Repeated for Virtual Input 16					
BF90	...Repeated for Virtual Input 17					
BFA0	...Repeated for Virtual Input 18					
BFB0	...Repeated for Virtual Input 19					
BFC0	...Repeated for Virtual Input 20					
bfd0	...Repeated for Virtual Input 21					
BFE0	...Repeated for Virtual Input 22					
BFF0	...Repeated for Virtual Input 23					
C000	...Repeated for Virtual Input 24					
C010	...Repeated for Virtual Input 25					
C020	...Repeated for Virtual Input 26					
C030	...Repeated for Virtual Input 27					
C040	...Repeated for Virtual Input 28					
C050	...Repeated for Virtual Input 29					
C060	...Repeated for Virtual Input 30					
C070	...Repeated for Virtual Input 31					
C080	...Repeated for Virtual Input 32					
C090	...Repeated for Virtual Input 33					
C0A0	...Repeated for Virtual Input 34					
C0B0	...Repeated for Virtual Input 35					
C0C0	...Repeated for Virtual Input 36					

Table B-9: MODBUS MEMORY MAP (Sheet 32 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
C0D0	...Repeated for Virtual Input 37					
C0E0	...Repeated for Virtual Input 38					
C0F0	...Repeated for Virtual Input 39					
C100	...Repeated for Virtual Input 40					
C110	...Repeated for Virtual Input 41					
C120	...Repeated for Virtual Input 42					
C130	...Repeated for Virtual Input 43					
C140	...Repeated for Virtual Input 44					
C150	...Repeated for Virtual Input 45					
C160	...Repeated for Virtual Input 46					
C170	...Repeated for Virtual Input 47					
C180	...Repeated for Virtual Input 48					
C190	...Repeated for Virtual Input 49					
C1A0	...Repeated for Virtual Input 50					
C1B0	...Repeated for Virtual Input 51					
C1C0	...Repeated for Virtual Input 52					
C1D0	...Repeated for Virtual Input 53					
C1E0	...Repeated for Virtual Input 54					
C1F0	...Repeated for Virtual Input 55					
C200	...Repeated for Virtual Input 56					
C210	...Repeated for Virtual Input 57					
C220	...Repeated for Virtual Input 58					
C230	...Repeated for Virtual Input 59					
C240	...Repeated for Virtual Input 60					
C250	...Repeated for Virtual Input 61					
C260	...Repeated for Virtual Input 62					
C270	...Repeated for Virtual Input 63					
C280	...Repeated for Virtual Input 64					
Virtual Outputs (Read/Write Setting) (96 modules)						
C130	Virtual Output 1 Name	---	---	---	F205	"Virt Op 1 "
C136	Virtual Output 1 Events	0 to 1	---	1	F102	0 (Disabled)
C137	Reserved	---	---	---	F001	0
C138	...Repeated for Virtual Output 2					
C140	...Repeated for Virtual Output 3					
C148	...Repeated for Virtual Output 4					
C150	...Repeated for Virtual Output 5					
C158	...Repeated for Virtual Output 6					
C160	...Repeated for Virtual Output 7					
C168	...Repeated for Virtual Output 8					
C170	...Repeated for Virtual Output 9					
C178	...Repeated for Virtual Output 10					
C180	...Repeated for Virtual Output 11					
C188	...Repeated for Virtual Output 12					
C190	...Repeated for Virtual Output 13					
C198	...Repeated for Virtual Output 14					
C1A0	...Repeated for Virtual Output 15					
C1A8	...Repeated for Virtual Output 16					
C1B0	...Repeated for Virtual Output 17					
C1B8	...Repeated for Virtual Output 18					
C1C0	...Repeated for Virtual Output 19					
C1C8	...Repeated for Virtual Output 20					
C1D0	...Repeated for Virtual Output 21					
C1D8	...Repeated for Virtual Output 22					
C1E0	...Repeated for Virtual Output 23					

Table B–9: MODBUS MEMORY MAP (Sheet 33 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
C1E8	...Repeated for Virtual Output 24					
C1F0	...Repeated for Virtual Output 25					
C1F8	...Repeated for Virtual Output 26					
C200	...Repeated for Virtual Output 27					
C208	...Repeated for Virtual Output 28					
C210	...Repeated for Virtual Output 29					
C218	...Repeated for Virtual Output 30					
C220	...Repeated for Virtual Output 31					
C228	...Repeated for Virtual Output 32					
C230	...Repeated for Virtual Output 33					
C238	...Repeated for Virtual Output 34					
C240	...Repeated for Virtual Output 35					
C248	...Repeated for Virtual Output 36					
C250	...Repeated for Virtual Output 37					
C258	...Repeated for Virtual Output 38					
C260	...Repeated for Virtual Output 39					
C268	...Repeated for Virtual Output 40					
C270	...Repeated for Virtual Output 41					
C278	...Repeated for Virtual Output 42					
C280	...Repeated for Virtual Output 43					
C288	...Repeated for Virtual Output 44					
C290	...Repeated for Virtual Output 45					
C298	...Repeated for Virtual Output 46					
C2A0	...Repeated for Virtual Output 47					
C2A8	...Repeated for Virtual Output 48					
C2B0	...Repeated for Virtual Output 49					
C2B8	...Repeated for Virtual Output 50					
C2C0	...Repeated for Virtual Output 51					
C2C8	...Repeated for Virtual Output 52					
C2D0	...Repeated for Virtual Output 53					
C2D8	...Repeated for Virtual Output 54					
C2E0	...Repeated for Virtual Output 55					
C2E8	...Repeated for Virtual Output 56					
C2F0	...Repeated for Virtual Output 57					
C2F8	...Repeated for Virtual Output 58					
C300	...Repeated for Virtual Output 59					
C308	...Repeated for Virtual Output 60					
C310	...Repeated for Virtual Output 61					
C318	...Repeated for Virtual Output 62					
C320	...Repeated for Virtual Output 63					
C328	...Repeated for Virtual Output 64					
C330	...Repeated for Virtual Output 65					
C338	...Repeated for Virtual Output 66					
C340	...Repeated for Virtual Output 67					
C348	...Repeated for Virtual Output 68					
C350	...Repeated for Virtual Output 69					
C358	...Repeated for Virtual Output 70					
C360	...Repeated for Virtual Output 71					
C368	...Repeated for Virtual Output 72					
C370	...Repeated for Virtual Output 73					
C378	...Repeated for Virtual Output 74					
C380	...Repeated for Virtual Output 75					
C388	...Repeated for Virtual Output 76					
C390	...Repeated for Virtual Output 77					

Table B-9: MODBUS MEMORY MAP (Sheet 34 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
C398	...Repeated for Virtual Output 78					
C3A0	...Repeated for Virtual Output 79					
C3A8	...Repeated for Virtual Output 80					
C3B0	...Repeated for Virtual Output 81					
C3B8	...Repeated for Virtual Output 82					
C3C0	...Repeated for Virtual Output 83					
C3C8	...Repeated for Virtual Output 84					
C3D0	...Repeated for Virtual Output 85					
C3D8	...Repeated for Virtual Output 86					
C3E0	...Repeated for Virtual Output 87					
C3E8	...Repeated for Virtual Output 88					
C3F0	...Repeated for Virtual Output 89					
C3F8	...Repeated for Virtual Output 90					
C400	...Repeated for Virtual Output 91					
C408	...Repeated for Virtual Output 92					
C410	...Repeated for Virtual Output 93					
C418	...Repeated for Virtual Output 94					
C420	...Repeated for Virtual Output 95					
C428	...Repeated for Virtual Output 96					
Mandatory (Read/Write Setting)						
C430	Test Mode Function	0 to 1	---	1	F102	0 (Disabled)
C431	Force VFD and LED	0 to 1	---	1	F126	0 (No)
C432	Test Mode Initiate	0 to 65535	---	1	F300	1
Mandatory (Read/Write Command)						
C433	Clear All Relay Records Command	0 to 1	---	1	F126	0 (No)
Contact Outputs (Read/Write Setting) (64 modules)						
C440	Contact Output 1 Name	---	---	---	F205	"Cont Op 1"
C446	Contact Output 1 Operation	0 to 65535	---	1	F300	0
C447	Contact Output 1 Seal In	0 to 65535	---	1	F300	0
C448	Latching Output 1 Reset	0 to 65535	---	1	F300	0
C449	Contact Output 1 Events	0 to 1	---	1	F102	1 (Enabled)
C44A	Latching Output 1 Type	0 to 1	---	1	F090	0 (Operate-dominant)
C44B	Reserved	---	---	---	F001	0
C44C	...Repeated for Contact Output 2					
C458	...Repeated for Contact Output 3					
C464	...Repeated for Contact Output 4					
C470	...Repeated for Contact Output 5					
C47C	...Repeated for Contact Output 6					
C488	...Repeated for Contact Output 7					
C494	...Repeated for Contact Output 8					
C4A0	...Repeated for Contact Output 9					
C4AC	...Repeated for Contact Output 10					
C4B8	...Repeated for Contact Output 11					
C4C4	...Repeated for Contact Output 12					
C4D0	...Repeated for Contact Output 13					
C4DC	...Repeated for Contact Output 14					
C4E8	...Repeated for Contact Output 15					
C4F4	...Repeated for Contact Output 16					
C500	...Repeated for Contact Output 17					
C50C	...Repeated for Contact Output 18					
C518	...Repeated for Contact Output 19					
C524	...Repeated for Contact Output 20					
C530	...Repeated for Contact Output 21					
C53C	...Repeated for Contact Output 22					

Table B–9: MODBUS MEMORY MAP (Sheet 35 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
C548	...Repeated for Contact Output 23					
C554	...Repeated for Contact Output 24					
C560	...Repeated for Contact Output 25					
C56C	...Repeated for Contact Output 26					
C578	...Repeated for Contact Output 27					
C584	...Repeated for Contact Output 28					
C590	...Repeated for Contact Output 29					
C59C	...Repeated for Contact Output 30					
C5A8	...Repeated for Contact Output 31					
C5B4	...Repeated for Contact Output 32					
C5C0	...Repeated for Contact Output 33					
C5CC	...Repeated for Contact Output 34					
C5D8	...Repeated for Contact Output 35					
C5E4	...Repeated for Contact Output 36					
C5F0	...Repeated for Contact Output 37					
C5FC	...Repeated for Contact Output 38					
C608	...Repeated for Contact Output 39					
C614	...Repeated for Contact Output 40					
C620	...Repeated for Contact Output 41					
C62C	...Repeated for Contact Output 42					
C638	...Repeated for Contact Output 43					
C644	...Repeated for Contact Output 44					
C650	...Repeated for Contact Output 45					
C65C	...Repeated for Contact Output 46					
C668	...Repeated for Contact Output 47					
C674	...Repeated for Contact Output 48					
C680	...Repeated for Contact Output 49					
C68C	...Repeated for Contact Output 50					
C698	...Repeated for Contact Output 51					
C6A4	...Repeated for Contact Output 52					
C6B0	...Repeated for Contact Output 53					
C6BC	...Repeated for Contact Output 54					
C6C8	...Repeated for Contact Output 55					
C6D4	...Repeated for Contact Output 56					
C6E0	...Repeated for Contact Output 57					
C6EC	...Repeated for Contact Output 58					
C6F8	...Repeated for Contact Output 59					
C704	...Repeated for Contact Output 60					
C710	...Repeated for Contact Output 61					
C71C	...Repeated for Contact Output 62					
C728	...Repeated for Contact Output 63					
C734	...Repeated for Contact Output 64					
Reset (Read/Write Setting)						
C750	FlexLogic™ operand which initiates a reset	0 to 65535	---	1	F300	0
Control Pushbuttons (Read/Write Setting) (7 modules)						
C760	Control Pushbutton 1 Function	0 to 1	---	1	F102	0 (Disabled)
C761	Control Pushbutton 1 Events	0 to 1	---	1	F102	0 (Disabled)
C762	...Repeated for Control Pushbutton 2					
C764	...Repeated for Control Pushbutton 3					
C766	...Repeated for Control Pushbutton 4					
C768	...Repeated for Control Pushbutton 5					
C76A	...Repeated for Control Pushbutton 6					
C76C	...Repeated for Control Pushbutton 7					

Table B–9: MODBUS MEMORY MAP (Sheet 36 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
Clear Records (Read/Write Setting)						
C772	Clear Event Records operand	0 to 65535	---	1	F300	0
C773	Clear Oscillography operand	0 to 65535	---	1	F300	0
C774	Clear Data Logger operand	0 to 65535	---	1	F300	0
C77B	Clear Demand operand	0 to 65535	---	1	F300	0
C77D	Clear Energy operand	0 to 65535	---	1	F300	0
C77F	Clear Unauthorized Access operand	0 to 65535	---	1	F300	0
C781	Clear Platform Direct Input/Output Statistics operand	0 to 65535	---	1	F300	0
C782	Reserved (13 items)	---	---	---	F001	0
Force Contact Inputs/Outputs (Read/Write Settings)						
C7A0	Force Contact Input x State (96 items)	0 to 2	---	1	F144	0 (Disabled)
C800	Force Contact Output x State (64 items)	0 to 3	---	1	F131	0 (Disabled)
Direct Inputs/Outputs (Read/Write Setting)						
C880	Direct Device ID	1 to 16	---	1	F001	1
C881	Direct I/O Channel 1 Ring Configuration Function	0 to 1	---	1	F126	0 (No)
C882	Platform Direct I/O Data Rate	64 to 128	kbps	64	F001	64
C883	Direct I/O Channel 2 Ring Configuration Function	0 to 1	---	1	F126	0 (No)
C884	Platform Direct I/O Crossover Function	0 to 1	---	1	F102	0 (Disabled)
Direct input/output commands (Read/Write Command)						
C888	Direct input/output clear counters command	0 to 1	---	1	F126	0 (No)
Direct inputs (Read/Write Setting) (96 modules)						
C890	Direct Input 1 Device Number	0 to 16	---	1	F001	0
C891	Direct Input 1 Number	0 to 96	---	1	F001	0
C892	Direct Input 1 Default State	0 to 3	---	1	F086	0 (Off)
C893	Direct Input 1 Events	0 to 1	---	1	F102	0 (Disabled)
C894	...Repeated for Direct Input 2					
C898	...Repeated for Direct Input 3					
C89C	...Repeated for Direct Input 4					
C8A0	...Repeated for Direct Input 5					
C8A4	...Repeated for Direct Input 6					
C8A8	...Repeated for Direct Input 7					
C8AC	...Repeated for Direct Input 8					
C8B0	...Repeated for Direct Input 9					
C8B4	...Repeated for Direct Input 10					
C8B8	...Repeated for Direct Input 11					
C8BC	...Repeated for Direct Input 12					
C8C0	...Repeated for Direct Input 13					
C8C4	...Repeated for Direct Input 14					
C8C8	...Repeated for Direct Input 15					
C8CC	...Repeated for Direct Input 16					
C8D0	...Repeated for Direct Input 17					
C8D4	...Repeated for Direct Input 18					
C8D8	...Repeated for Direct Input 19					
C8DC	...Repeated for Direct Input 20					
C8E0	...Repeated for Direct Input 21					
C8E4	...Repeated for Direct Input 22					
C8E8	...Repeated for Direct Input 23					
C8EC	...Repeated for Direct Input 24					
C8F0	...Repeated for Direct Input 25					
C8F4	...Repeated for Direct Input 26					
C8F8	...Repeated for Direct Input 27					
C8FC	...Repeated for Direct Input 28					
C900	...Repeated for Direct Input 29					
C904	...Repeated for Direct Input 30					

Table B–9: MODBUS MEMORY MAP (Sheet 37 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
C908	...Repeated for Direct Input 31					
C90C	...Repeated for Direct Input 32					
C910	...Repeated for Direct Input 33					
C914	...Repeated for Direct Input 34					
C918	...Repeated for Direct Input 35					
C91C	...Repeated for Direct Input 36					
C920	...Repeated for Direct Input 37					
C924	...Repeated for Direct Input 38					
C928	...Repeated for Direct Input 39					
C92C	...Repeated for Direct Input 40					
C930	...Repeated for Direct Input 41					
C934	...Repeated for Direct Input 42					
C938	...Repeated for Direct Input 43					
C93C	...Repeated for Direct Input 44					
C940	...Repeated for Direct Input 45					
C944	...Repeated for Direct Input 46					
C948	...Repeated for Direct Input 47					
C94C	...Repeated for Direct Input 48					
C950	...Repeated for Direct Input 49					
C954	...Repeated for Direct Input 50					
C958	...Repeated for Direct Input 51					
C95C	...Repeated for Direct Input 52					
C960	...Repeated for Direct Input 53					
C964	...Repeated for Direct Input 54					
C968	...Repeated for Direct Input 55					
C96C	...Repeated for Direct Input 56					
C970	...Repeated for Direct Input 57					
C974	...Repeated for Direct Input 58					
C978	...Repeated for Direct Input 59					
C97C	...Repeated for Direct Input 60					
C980	...Repeated for Direct Input 61					
C984	...Repeated for Direct Input 62					
C988	...Repeated for Direct Input 63					
C98C	...Repeated for Direct Input 64					
Platform Direct Outputs (Read/Write Setting) (96 modules)						
CA10	Direct Output 1 Operand	0 to 65535	---	1	F300	0
CA11	Direct Output 1 Events	0 to 1	---	1	F102	0 (Disabled)
CA12	...Repeated for Direct Output 2					
CA14	...Repeated for Direct Output 3					
CA16	...Repeated for Direct Output 4					
CA18	...Repeated for Direct Output 5					
CA1A	...Repeated for Direct Output 6					
CA1C	...Repeated for Direct Output 7					
CA1E	...Repeated for Direct Output 8					
CA20	...Repeated for Direct Output 9					
CA22	...Repeated for Direct Output 10					
CA24	...Repeated for Direct Output 11					
CA26	...Repeated for Direct Output 12					
CA28	...Repeated for Direct Output 13					
CA2A	...Repeated for Direct Output 14					
CA2C	...Repeated for Direct Output 15					
CA2E	...Repeated for Direct Output 16					
CA30	...Repeated for Direct Output 17					
CA32	...Repeated for Direct Output 18					

Table B-9: MODBUS MEMORY MAP (Sheet 38 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
CA34	...Repeated for Direct Output 19					
CA36	...Repeated for Direct Output 20					
CA38	...Repeated for Direct Output 21					
CA3A	...Repeated for Direct Output 22					
CA3C	...Repeated for Direct Output 23					
CA3E	...Repeated for Direct Output 24					
CA40	...Repeated for Direct Output 25					
CA42	...Repeated for Direct Output 26					
CA44	...Repeated for Direct Output 27					
CA46	...Repeated for Direct Output 28					
CA48	...Repeated for Direct Output 29					
CA4A	...Repeated for Direct Output 30					
CA4C	...Repeated for Direct Output 31					
CA4E	...Repeated for Direct Output 32					
CA50	...Repeated for Direct Output 33					
CA52	...Repeated for Direct Output 34					
CA54	...Repeated for Direct Output 35					
CA56	...Repeated for Direct Output 36					
CA58	...Repeated for Direct Output 37					
CA5A	...Repeated for Direct Output 38					
CA5C	...Repeated for Direct Output 39					
CA5E	...Repeated for Direct Output 40					
CA60	...Repeated for Direct Output 41					
CA62	...Repeated for Direct Output 42					
CA64	...Repeated for Direct Output 43					
CA66	...Repeated for Direct Output 44					
CA68	...Repeated for Direct Output 45					
CA6A	...Repeated for Direct Output 46					
CA6C	...Repeated for Direct Output 47					
CA6E	...Repeated for Direct Output 48					
CA70	...Repeated for Direct Output 49					
CA72	...Repeated for Direct Output 50					
CA74	...Repeated for Direct Output 51					
CA76	...Repeated for Direct Output 52					
CA78	...Repeated for Direct Output 53					
CA7A	...Repeated for Direct Output 54					
CA7C	...Repeated for Direct Output 55					
CA7E	...Repeated for Direct Output 56					
CA80	...Repeated for Direct Output 57					
CA82	...Repeated for Direct Output 58					
CA84	...Repeated for Direct Output 59					
CA86	...Repeated for Direct Output 60					
CA88	...Repeated for Direct Output 61					
CA8A	...Repeated for Direct Output 62					
CA8C	...Repeated for Direct Output 63					
CA8E	...Repeated for Direct Output 64					
Direct Input/Output Alarms (Read/Write Setting)						
CAD0	Direct Input/Output Channel 1 CRC Alarm Function	0 to 1	---	1	F102	0 (Disabled)
CAD1	Direct I/O Channel 1 CRC Alarm Message Count	100 to 10000	---	1	F001	600
CAD2	Direct Input/Output Channel 1 CRC Alarm Threshold	1 to 1000	---	1	F001	10
CAD3	Direct Input/Output Channel 1 CRC Alarm Events	0 to 1	---	1	F102	0 (Disabled)
CAD4	Reserved (4 items)	1 to 1000	---	1	F001	10
CAD8	Direct Input/Output Channel 2 CRC Alarm Function	0 to 1	---	1	F102	0 (Disabled)
CAD9	Direct I/O Channel 2 CRC Alarm Message Count	100 to 10000	---	1	F001	600

Table B–9: MODBUS MEMORY MAP (Sheet 39 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
CADA	Direct Input/Output Channel 2 CRC Alarm Threshold	1 to 1000	---	1	F001	10
CADB	Direct Input/Output Channel 2 CRC Alarm Events	0 to 1	---	1	F102	0 (Disabled)
CADC	Reserved (4 items)	1 to 1000	---	1	F001	10
CAE0	Direct I/O Ch 1 Unreturned Messages Alarm Function	0 to 1	---	1	F102	0 (Disabled)
CAE1	Direct I/O Ch 1 Unreturned Messages Alarm Msg Count	100 to 10000	---	1	F001	600
CAE2	Direct I/O Ch 1 Unreturned Messages Alarm Threshold	1 to 1000	---	1	F001	10
CAE3	Direct I/O Ch 1 Unreturned Messages Alarm Events	0 to 1	---	1	F102	0 (Disabled)
CAE4	Reserved (4 items)	1 to 1000	---	1	F001	10
CAE8	Direct IO Ch 2 Unreturned Messages Alarm Function	0 to 1	---	1	F102	0 (Disabled)
CAE9	Direct I/O Ch 2 Unreturned Messages Alarm Msg Count	100 to 10000	---	1	F001	600
CAEA	Direct I/O Ch 2 Unreturned Messages Alarm Threshold	1 to 1000	---	1	F001	10
CAEB	Direct I/O Channel 2 Unreturned Messages Alarm Events	0 to 1	---	1	F102	0 (Disabled)
CAEC	Reserved (4 items)	1 to 1000	---	1	F001	10
Remote Devices (Read/Write Setting) (16 modules)						
CB00	Remote Device 1 ID	---	---	---	F202	"Remote Device 1"
CB08	Remote Device 1 Virtual LAN Identifier	0 to 4095	---	1	F001	0
CB09	Remote Device 1 Ethernet APPID	0 to 16383	---	1	F001	0
CB0A	...Repeated for Device 2					
CB14	...Repeated for Device 3					
CB1E	...Repeated for Device 4					
CB28	...Repeated for Device 5					
CB32	...Repeated for Device 6					
CB3C	...Repeated for Device 7					
CB46	...Repeated for Device 8					
CB50	...Repeated for Device 9					
CB5A	...Repeated for Device 10					
CB64	...Repeated for Device 11					
CB6E	...Repeated for Device 12					
CB78	...Repeated for Device 13					
CB82	...Repeated for Device 14					
CB8C	...Repeated for Device 15					
CB96	...Repeated for Device 16					
Remote Inputs (Read/Write Setting) (64 modules)						
CBA0	Remote Input 1 Device	1 to 16	---	1	F001	1
CBA1	Remote Input 1 Bit Pair	0 to 64	---	1	F156	0 (None)
CBA2	Remote Input 1 Default State	0 to 3	---	1	F086	0 (Off)
CBA3	Remote Input 1 Events	0 to 1	---	1	F102	0 (Disabled)
CBA4	Remote Input 1 Name	1 to 64	---	1	F205	"Rem Ip 1"
CBAA	...Repeated for Remote Input 2					
CBB4	...Repeated for Remote Input 3					
CBBE	...Repeated for Remote Input 4					
CBC8	...Repeated for Remote Input 5					
CBD2	...Repeated for Remote Input 6					
CBD8	...Repeated for Remote Input 7					
CBE6	...Repeated for Remote Input 8					
CBF0	...Repeated for Remote Input 9					
CBFA	...Repeated for Remote Input 10					
CC04	...Repeated for Remote Input 11					
CC0E	...Repeated for Remote Input 12					
CC18	...Repeated for Remote Input 13					
CC22	...Repeated for Remote Input 14					
CC2C	...Repeated for Remote Input 15					
CC36	...Repeated for Remote Input 16					
CC40	...Repeated for Remote Input 17					

Table B-9: MODBUS MEMORY MAP (Sheet 40 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
CC4A	...Repeated for Remote Input 18					
CC54	...Repeated for Remote Input 19					
CC5E	...Repeated for Remote Input 20					
CC68	...Repeated for Remote Input 21					
CC72	...Repeated for Remote Input 22					
CC7C	...Repeated for Remote Input 23					
CC86	...Repeated for Remote Input 24					
CC90	...Repeated for Remote Input 25					
CC9A	...Repeated for Remote Input 26					
CCA4	...Repeated for Remote Input 27					
CCAE	...Repeated for Remote Input 28					
CCB8	...Repeated for Remote Input 29					
CCC2	...Repeated for Remote Input 30					
CCCC	...Repeated for Remote Input 31					
CCD6	...Repeated for Remote Input 32					
CCE0	...Repeated for Remote Input 33					
CCEA	...Repeated for Remote Input 34					
CCF4	...Repeated for Remote Input 35					
CCFE	...Repeated for Remote Input 36					
CD08	...Repeated for Remote Input 37					
CD12	...Repeated for Remote Input 38					
CD1C	...Repeated for Remote Input 39					
CD26	...Repeated for Remote Input 40					
CD30	...Repeated for Remote Input 41					
CD3A	...Repeated for Remote Input 42					
CD44	...Repeated for Remote Input 43					
CD4E	...Repeated for Remote Input 44					
CD58	...Repeated for Remote Input 45					
CD62	...Repeated for Remote Input 46					
CD6C	...Repeated for Remote Input 47					
CD76	...Repeated for Remote Input 48					
CD80	...Repeated for Remote Input 49					
CD8A	...Repeated for Remote Input 50					
CD94	...Repeated for Remote Input 51					
CD9E	...Repeated for Remote Input 52					
CDA8	...Repeated for Remote Input 53					
CDB2	...Repeated for Remote Input 54					
CDBC	...Repeated for Remote Input 55					
CDC6	...Repeated for Remote Input 56					
CDD0	...Repeated for Remote Input 57					
CDDA	...Repeated for Remote Input 58					
CDE4	...Repeated for Remote Input 59					
CDEE	...Repeated for Remote Input 60					
CDF8	...Repeated for Remote Input 61					
CE02	...Repeated for Remote Input 62					
CE0C	...Repeated for Remote Input 63					
CE16	...Repeated for Remote Input 64					
Remote Output DNA Pairs (Read/Write Setting) (32 modules)						
CE20	Remote Output DNA 1 Operand	0 to 65535	---	1	F300	0
CE21	Remote Output DNA 1 Events	0 to 1	---	1	F102	0 (Disabled)
CE22	Reserved (2 items)	0 to 1	---	1	F001	0
CE24	...Repeated for Remote Output 2					
CE28	...Repeated for Remote Output 3					
CE2C	...Repeated for Remote Output 4					

Table B-9: MODBUS MEMORY MAP (Sheet 41 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
CE30	...Repeated for Remote Output 5					
CE34	...Repeated for Remote Output 6					
CE38	...Repeated for Remote Output 7					
CE3C	...Repeated for Remote Output 8					
CE40	...Repeated for Remote Output 9					
CE44	...Repeated for Remote Output 10					
CE48	...Repeated for Remote Output 11					
CE4C	...Repeated for Remote Output 12					
CE50	...Repeated for Remote Output 13					
CE54	...Repeated for Remote Output 14					
CE58	...Repeated for Remote Output 15					
CE5C	...Repeated for Remote Output 16					
CE60	...Repeated for Remote Output 17					
CE64	...Repeated for Remote Output 18					
CE68	...Repeated for Remote Output 19					
CE6C	...Repeated for Remote Output 20					
CE70	...Repeated for Remote Output 21					
CE74	...Repeated for Remote Output 22					
CE78	...Repeated for Remote Output 23					
CE7C	...Repeated for Remote Output 24					
CE80	...Repeated for Remote Output 25					
CE84	...Repeated for Remote Output 26					
CE88	...Repeated for Remote Output 27					
CE8C	...Repeated for Remote Output 28					
CE90	...Repeated for Remote Output 29					
CE94	...Repeated for Remote Output 30					
CE98	...Repeated for Remote Output 31					
CE9C	...Repeated for Remote Output 32					
Remote Output UserSt Pairs (Read/Write Setting) (32 modules)						
CEA0	Remote Output UserSt 1 Operand	0 to 65535	---	1	F300	0
CEA1	Remote Output UserSt 1 Events	0 to 1	---	1	F102	0 (Disabled)
CEA2	Reserved (2 items)	0 to 1	---	1	F001	0
CEA4	...Repeated for Remote Output 2					
CEA8	...Repeated for Remote Output 3					
CEAC	...Repeated for Remote Output 4					
CEB0	...Repeated for Remote Output 5					
CEB4	...Repeated for Remote Output 6					
CEB8	...Repeated for Remote Output 7					
CEBC	...Repeated for Remote Output 8					
CEC0	...Repeated for Remote Output 9					
CEC4	...Repeated for Remote Output 10					
CEC8	...Repeated for Remote Output 11					
CECC	...Repeated for Remote Output 12					
CED0	...Repeated for Remote Output 13					
CED4	...Repeated for Remote Output 14					
CED8	...Repeated for Remote Output 15					
CEDC	...Repeated for Remote Output 16					
CEE0	...Repeated for Remote Output 17					
CEE4	...Repeated for Remote Output 18					
CEE8	...Repeated for Remote Output 19					
CEEC	...Repeated for Remote Output 20					
CEF0	...Repeated for Remote Output 21					
CEF4	...Repeated for Remote Output 22					
CEF8	...Repeated for Remote Output 23					

Table B-9: MODBUS MEMORY MAP (Sheet 42 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
CEFC	...Repeated for Remote Output 24					
CF00	...Repeated for Remote Output 25					
CF04	...Repeated for Remote Output 26					
CF08	...Repeated for Remote Output 27					
CF0C	...Repeated for Remote Output 28					
CF10	...Repeated for Remote Output 29					
CF14	...Repeated for Remote Output 30					
CF18	...Repeated for Remote Output 31					
CF1C	...Repeated for Remote Output 32					
Phasor Measurement Unit Communication (Read/Write Setting) (4 modules)						
D000	PMU 1 Com Port 1 Type	0 to 3	---	1	F545	0 (Network)
D001	PMU 1 Com Port 2 Type	0 to 3	---	1	F545	0 (Network)
D002	PMU 1 Com Port 3 Type	0 to 3	---	1	F545	0 (Network)
D003	PMU 1 Port 1 PHS-x (14 items)	0 to 14	---	1	F543	1 (Va)
D011	PMU 1 Port 2 PHS-x (14 items)	0 to 14	---	1	F543	1 (Va)
D01F	PMU 1 Port 3 PHS-x (14 items)	0 to 14	---	1	F543	1 (Va)
D02D	PMU 1 Com Port 1 PHS-x Name (14 items)	---	---	---	F203	"GE-UR-PMU-PHS 1"
D09D	PMU 1 Port 2 PHS-x Name (14 items)	---	---	---	F203	"GE-UR-PMU-PHS 1"
D10D	PMU 1 Port 3 PHS-x Name (14 items)	---	---	---	F203	"GE-UR-PMU-PHS 1"
D17D	PMU 1 Port 1 A-CH-x (8 items)	0 to 65535	---	1	F600	0
D185	PMU 1 Port 2 A-CH-x (8 items)	0 to 65535	---	1	F600	0
D18D	PMU 1 Port 3 A-CH-x (8 items)	0 to 65535	---	1	F600	0
D195	PMU 1 Port 1 A-CH-x Name (8 items)	---	---	---	F203	"AnalogChannel 1"
D1D5	PMU 1 Port 2 A-CH-x Name (8 items)	---	---	---	F203	"AnalogChannel 1"
D215	PMU 1 Port 3 A-CH-x Name (8 items)	---	---	---	F203	"AnalogChannel 1"
D255	PMU 1 Port 1 D-CH-x (16 items)	0 to 65535	---	1	F300	0
D265	PMU 1 Port 2 D-CH-x (16 items)	0 to 65535	---	1	F300	0
D275	PMU 1 Port 3 D-CH-x (16 items)	0 to 65535	---	1	F300	0
D285	PMU 1 Port 1 D-CH-x Name (16 items)	---	---	---	F203	"Dig Channel 1"
D305	PMU 1 Port 2 D-CH-x Name (16 items)	---	---	---	F203	"Dig Channel 1"
D385	PMU 1 Port 3 D-CH-x Name (16 items)	---	---	---	F203	"Dig Channel 1"
D405	PMU 1 Port 1 D-CH-x Normal State (16 items)	0 to 1	---	1	F108	0 (Off)
D415	PMU 1 Port 2 D-CH-x Normal State (16 items)	0 to 1	---	1	F108	0 (Off)
D425	PMU 1 Port 3 D-CH-x Normal State (16 items)	0 to 1	---	1	F108	0 (Off)
D435	...Repeated for PMU 2					
D86A	...Repeated for PMU 3					
DC9F	...Repeated for PMU 4					
Phasor Measurement Unit Recording Command (Read/Write Command) (4 modules)						
E0D4	PMU 1 Recording Clear Command	0 to 1	---	1	F126	0 (No)
E0D5	PMU 1 Recording Force Trigger	0 to 1	---	1	F126	0 (No)
E0D6	...Repeated for PMU 2					
E0D8	...Repeated for PMU 3					
E0DA	...Repeated for PMU 4					
Phasor Measurement Unit Recording (Read/Write Setting) (4 modules)						
E0DC	PMU 1 Recording Rate	0 to 10	---	1	F544	3 (10/sec)
E0DD	PMU 1 Triggering Style	0 to 1	---	1	F541	0 (Timed)
E0DE	PMU 1 No Of Timed Records	1 to 128	---	1	F001	3
E0DF	PMU 1 Trigger Mode	0 to 1	---	1	F542	0 (Auto Overwrite)
E0E0	PMU 1 Timed Trigger Position	1 to 50	%	1	F001	10
E0E1	PMU 1 Forced Trigger Position	1 to 500	---	1	F001	100
E0E2	PMU 1 Rec PHS-1 (14 items)	0 to 14	---	1	F543	1 (Va)
E0F0	PMU 1 Rec PHS-x Name (14 items)	---	---	---	F203	GE-UR-PMU-PHS 1
E160	PMU 1 Rec A-CH-x (8 items)	0 to 65535	---	1	F600	0
E168	PMU 1 Rec A-CH-x Name (8 items)	---	---	---	F203	AnalogChannel 1

Table B-9: MODBUS MEMORY MAP (Sheet 43 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
E1A8	PMU 1 Rec D-CH-x (16 items)	0 to 65535	---	1	F300	0
E1B8	PMU 1 Rec D-CH-x Name (16 items)	---	---	---	F203	Dig Channel 1
E238	...Repeated for PMU 2					
E394	...Repeated for PMU 3					
E4F0	...Repeated for PMU 4					
Phasor Measurement Unit Basic Configuration (Read/Write Setting) (4 modules)						
E658	PMU 1 Function	0 to 1	---	1	F102	0 (Disabled)
E659	PMU 1 IDcode	1 to 65534	---	1	F001	1
E65A	PMU 1 STN	---	---	---	F203	"GE-UR-PMU"
E662	PMU 1 Source	0 to 5	---	1	F167	0 (SRC 1)
E663	PMU 1 Post-Filter	0 to 3	---	1	F540	1 (Symm-3-point)
E664	...Repeated for PMU 2					
E670	...Repeated for PMU 3					
E67C	...Repeated for PMU 4					
Phasor Measurement Unit Calibration (Read/Write Setting) (4 modules)						
E688	PMU 1 Va Calibration Angle	-5 to 5	°	0.05	F002	0
E689	PMU 1 Vb Calibration Angle	-5 to 5	°	0.05	F002	0
E68A	PMU 1 Vc Calibration Angle	-5 to 5	°	0.05	F002	0
E68B	PMU 1 Vx Calibration Angle	-5 to 5	°	0.05	F002	0
E68C	PMU 1 Ia Calibration Angle	-5 to 5	°	0.05	F002	0
E68D	PMU 1 Ib Calibration Angle	-5 to 5	°	0.05	F002	0
E68E	PMU 1 Ic Calibration Angle	-5 to 5	°	0.05	F002	0
E68F	PMU 1 Ig Calibration Angle	-5 to 5	°	0.05	F002	0
E690	PMU 1 Seq Volt Shift Angle	-180 to 180	°	30	F002	0
E691	PMU 1 Seq Curr Shift Angle	-180 to 180	°	30	F002	0
E692	...Repeated for PMU 2					
E69C	...Repeated for PMU 3					
E6A6	...Repeated for PMU 4					
Phasor Measurement Unit Triggering (Read/Write Setting) (4 modules)						
E6B0	PMU 1 User Trigger	0 to 65535	---	1	F300	0
E6B1	...Repeated for PMU 2					
E6B2	...Repeated for PMU 3					
E6B3	...Repeated for PMU 4					
Phasor Measurement Unit Current Trigger (Read/Write Setting) (4 modules)						
E6B4	PMU 1 Current Trigger Function	0 to 1	---	1	F102	0 (Disabled)
E6B5	PMU 1 Current Trigger Pickup	0.1 to 30	pu	0.001	F001	1800
E6B6	PMU 1 Current Trigger Pkp Time	0 to 600	s	0.01	F001	10
E6B7	PMU 1 Current Trigger Dpo Time	0 to 600	s	0.01	F001	100
E6B8	PMU 1 Current Trigger Block (3 items)	0 to 65535	---	1	F300	0
E6BB	PMU 1 Current Trigger Target	0 to 2	---	1	F109	0 (Self-reset)
E6BC	PMU 1 Current Trigger Events	0 to 1	---	1	F102	0 (Disabled)
E6BD	...Repeated for PMU 2					
E6C6	...Repeated for PMU 3					
E6CF	...Repeated for PMU 4					
Phasor Measurement Unit df/dt Trigger (Read/Write Setting) (4 modules)						
E6D8	PMU 1 df/dt Trigger Function	0 to 1	---	1	F102	0 (Disabled)
E6D9	PMU 1 df/dt Triger Raise	0.1 to 15	Hz/s	0.01	F001	25
E6DA	PMU 1 df/dt Triger Fall	0.1 to 15	Hz/s	0.01	F001	25
E6DB	PMU 1 df/dt Trigger Pkp Time	0 to 600	s	0.01	F001	10
E6DC	PMU 1 df/dt Trigger Dpo Time	0 to 600	s	0.01	F001	100
E6DD	PMU 1 df/dt Trigger Block (3 items)	0 to 65535	---	1	F300	0
E6E0	PMU 1 df/dt Trigger Target	0 to 2	---	1	F109	0 (Self-reset)
E6E1	PMU 1 df/dt Trigger Events	0 to 1	---	1	F102	0 (Disabled)
E6E2	...Repeated for PMU 2					

Table B-9: MODBUS MEMORY MAP (Sheet 44 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
E6EC	...Repeated for PMU 3					
E6F6	...Repeated for PMU 4					
Phasor Measurement Unit Frequency Trigger (Read/Write Setting) (4 modules)						
E700	PMU 1 Freq Trigger Function	0 to 1	---	1	F102	0 (Disabled)
E701	PMU 1 Freq Trigger Low Freq	20 to 70	Hz	0.01	F001	4900
E702	PMU 1 Freq Trigger High Freq	20 to 70	Hz	0.01	F001	6100
E703	PMU 1 Freq Trigger Pkp Time	0 to 600	s	0.01	F001	10
E704	PMU 1 Freq Trigger Dpo Time	0 to 600	s	0.01	F001	100
E705	PMU 1 Freq Trigger Block (3 items)	0 to 65535	---	1	F300	0
E708	PMU 1 Freq Trigger Target	0 to 2	---	1	F109	0 (Self-reset)
E709	PMU 1 Freq Trigger Events	0 to 1	---	1	F102	0 (Disabled)
E70A	...Repeated for PMU 2					
E714	...Repeated for PMU 3					
E71E	...Repeated for PMU 4					
Phasor Measurement Unit Power Trigger (Read/Write Setting) (4 modules)						
E728	PMU 1 Power Trigger Function	0 to 1	---	1	F102	0 (Disabled)
E729	PMU 1 Power Trigger Active	0.25 to 3	pu	0.001	F001	1250
E72A	PMU 1 Power Trigger Reactive	0.25 to 3	pu	0.001	F001	1250
E72B	PMU 1 Power Trigger Apparent	0.25 to 3	pu	0.001	F001	1250
E72C	PMU 1 Power Trigger Pkp Time	0 to 600	s	0.01	F001	10
E72D	PMU 1 Power Trigger Dpo Time	0 to 600	s	0.01	F001	100
E72E	PMU 1 Power Trigger Block (3 items)	0 to 65535	---	1	F300	0
E731	PMU 1 Power Trigger Target	0 to 2	---	1	F109	0 (Self-reset)
E732	PMU 1 Power Trigger Events	0 to 1	---	1	F102	0 (Disabled)
E733	...Repeated for PMU 2					
E73E	...Repeated for PMU 3					
E749	...Repeated for PMU 4					
Phasor Measurement Unit Voltage Trigger (Read/Write Setting) (4 modules)						
E754	PMU 1 Volt Trigger Function	0 to 1	---	1	F102	0 (Disabled)
E755	PMU 1 Voltage Trigger Low Volt	0.25 to 1.25	pu	0.001	F001	800
E756	PMU 1 Voltage Trigger High Volt	0.75 to 1.75	pu	0.001	F001	1200
E757	PMU 1 Voltage Trigger Pkp Time	0 to 600	s	0.01	F001	10
E758	PMU 1 Voltage Trigger Dpo Time	0 to 600	s	0.01	F001	100
E759	PMU 1 Voltage Trigger Block (3 items)	0 to 65535	---	1	F300	0
E75C	PMU 1 Voltage Trigger Target	0 to 2	---	1	F109	0 (Self-reset)
E75D	PMU 1 Voltage Trigger Events	0 to 1	---	1	F102	0 (Disabled)
E75E	...Repeated for PMU 2					
E768	...Repeated for PMU 3					
E772	...Repeated for PMU 4					
Phasor Measurement Unit Network Reporting Configuration (Read/Write Setting)						
E77C	PMU Network Reporting Function	0 to 1	---	1	F102	0 (Disabled)
E77D	PMU Network Reporting ID Code	1 to 65534	---	1	F001	1
E77E	PMU Network Reporting Rate	0 to 10	---	1	F544	3 (10/sec)
E77F	PMU Network Reporting Style	0 to 1	---	1	F546	0 (Polar)
E780	PMU Network Reporting Format	0 to 1	---	1	F547	0 (Integer)
E781	PMU Network PDC Control	0 to 1	---	1	F102	0 (Disabled)
Phasor Measurement Unit One-shot Command (Read/Write Setting)						
E782	PMU One-shot Function	0 to 1	---	1	F102	0 (Disabled)
E783	PMU One-shot Sequence Number	0 to 59	---	1	F001	1
E784	PMU One-shot Time	0 to 235959	---	1	F050	0
Phasor Measurement Unit Test Values (Read/Write Setting) (4 modules)						
E786	PMU 1 Test Function	0 to 1	---	1	F102	0 (Disabled)
E787	PMU 1 Phase A Voltage Test Magnitude	0 to 700	kV	0.01	F003	50000
E789	PMU 1 Phase A Voltage Test Angle	-180 to 180	°	0.05	F002	0

Table B-9: MODBUS MEMORY MAP (Sheet 45 of 45)

ADDR	REGISTER NAME	RANGE	UNITS	STEP	FORMAT	DEFAULT
E78A	PMU 1 Phase B Voltage Test Magnitude	0 to 700	kV	0.01	F003	50000
E78C	PMU 1 Phase B Voltage Test Angle	-180 to 180	°	0.05	F002	-120
E78D	PMU 1 Phase C Voltage Test Magnitude	0 to 700	kV	0.01	F003	50000
E78F	PMU 1 Phase C Voltage Test Angle	-180 to 180	°	0.05	F002	120
E790	PMU 1 Auxiliary Voltage Test Magnitude	0 to 700	kV	0.01	F003	50000
E792	PMU 1 Auxiliary Voltage Test Angle	-180 to 180	°	0.05	F002	0
E793	PMU 1 Phase A Current Test Magnitude	0 to 9.999	kA	0.001	F004	1000
E795	PMU 1 Phase A Current Test Angle	-180 to 180	°	0.05	F002	-10
E796	PMU 1 Phase B Current Test Magnitude	0 to 9.999	kA	0.001	F004	1000
E798	PMU 1 Phase B Current Test Angle	-180 to 180	°	0.05	F002	-130
E799	PMU 1 Phase C Current Test Magnitude	0 to 9.999	kA	0.001	F003	1000
E79B	PMU 1 Phase C Current Test Angle	-180 to 180	°	0.05	F002	110
E79C	PMU 1 Ground Current Test Magnitude	0 to 9.999	kA	0.001	F004	0
E79E	PMU 1 Ground Current Test Angle	-180 to 180	°	0.05	F002	0
E79F	PMU 1 Test Frequency	20 to 70	Hz	0.001	F003	60000
E7A1	PMU 1 Test df/dt	-10 to 10	Hz/s	0.001	F002	0
E7A2	...Repeated for PMU 2					
E7BE	...Repeated for PMU 3					
E7DA	...Repeated for PMU 4					
Phasor Measurement Unit Recorder Config Counter Command (Read/Write Command) (4 modules)						
E7F6	PMU 1 Recorder Clear Config Counter	0 to 1	---	1	F126	0 (No)
E7F7	...Repeated for PMU 2					
E7F8	...Repeated for PMU 3					
E7F9	...Repeated for PMU 4					
Phasor Measurement Unit Recording Values (Read Only) (4 modules)						
E7FA	PMU 1 Available Records	0 to 65535	---	1	F001	0
E7FB	PMU 1 Second Per Record	0 to 6553.5	---	0.1	F001	0
E7FC	PMU 1 Last Cleared Date	0 to 400000000	---	1	F050	0
E7FE	...Repeated for module number 2					
E802	...Repeated for module number 3					
E806	...Repeated for module number 4					
Phasor Measurement Unit Records (Read Only)						
EEEE	PMU Recording Number of Triggers	0 to 65535	samples	1	F001	0

B.4.2 DATA FORMATS

F001
UR_UINT16 UNSIGNED 16 BIT INTEGER

F002
UR_SINT16 SIGNED 16 BIT INTEGER

F003
UR_UINT32 UNSIGNED 32 BIT INTEGER (2 registers)

High order word is stored in the first register.
Low order word is stored in the second register.

F004
UR_SINT32 SIGNED 32 BIT INTEGER (2 registers)

High order word is stored in the first register/
Low order word is stored in the second register.

F005
UR_UINT8 UNSIGNED 8 BIT INTEGER

F006
UR_SINT8 SIGNED 8 BIT INTEGER

F011
UR_UINT16 FLEXCURVE DATA (120 points)

A FlexCurve is an array of 120 consecutive data points (x, y) which are interpolated to generate a smooth curve. The y-axis is the user defined trip or operation time setting; the x-axis is the pickup ratio and is pre-defined. Refer to format F119 for a listing of the pickup ratios; the enumeration value for the pickup ratio indicates the offset into the FlexCurve base address where the corresponding time value is stored.

F012**DISPLAY_SCALE DISPLAY SCALING
(unsigned 16-bit integer)**

MSB indicates the SI units as a power of ten. LSB indicates the number of decimal points to display.

Example: Current values are stored as 32 bit numbers with three decimal places and base units in Amps. If the retrieved value is 12345.678 A and the display scale equals 0x0302 then the displayed value on the unit is 12.35 kA.

F013**POWER_FACTOR (SIGNED 16 BIT INTEGER)**

Positive values indicate lagging power factor; negative values indicate leading.

F040**UR_UINT48 48-BIT UNSIGNED INTEGER****F050****UR_UINT32 TIME and DATE (UNSIGNED 32 BIT INTEGER)**

Gives the current time in seconds elapsed since 00:00:00 January 1, 1970.

F051**UR_UINT32 DATE in SR format (alternate format for F050)**

First 16 bits are Month/Day (MM/DD/xxxx). Month: 1=January, 2=February,...,12=December; Day: 1 to 31 in steps of 1
Last 16 bits are Year (xx/xx/YYYY): 1970 to 2106 in steps of 1

F052**UR_UINT32 TIME in SR format (alternate format for F050)**

First 16 bits are Hours/Minutes (HH:MM:xx.xxx).
Hours: 0=12am, 1=1am,...,12=12pm,...23=11pm;
Minutes: 0 to 59 in steps of 1

Last 16 bits are Seconds (xx:xx:SS.SSS): 0=00.000s, 1=00.001,...,59999=59.999s)

F060**FLOATING_POINT IEEE FLOATING POINT (32 bits)****F070****HEX2 2 BYTES - 4 ASCII DIGITS****F071****HEX4 4 BYTES - 8 ASCII DIGITS****F072****HEX6 6 BYTES - 12 ASCII DIGITS****F073****HEX8 8 BYTES - 16 ASCII DIGITS****F074****HEX20 20 BYTES - 40 ASCII DIGITS****F083****ENUMERATION: SELECTOR MODES**

0 = Time-Out, 1 = Acknowledge

F084**ENUMERATION: SELECTOR POWER UP**

0 = Restore, 1 = Synchronize, 2 = Sync/Restore

F085**ENUMERATION: POWER SWING SHAPE**

0 = Mho Shape, 1 = Quad Shape

F086**ENUMERATION: DIGITAL INPUT DEFAULT STATE**

0 = Off, 1 = On, 2 = Latest/Off, 3 = Latest/On

F090**ENUMERATION: LATCHING OUTPUT TYPE**

0 = Operate-dominant, 1 = Reset-dominant

F100**ENUMERATION: VT CONNECTION TYPE**

0 = Wye; 1 = Delta

F101**ENUMERATION: MESSAGE DISPLAY INTENSITY**

0 = 25%, 1 = 50%, 2 = 75%, 3 = 100%

F102**ENUMERATION: DISABLED/ENABLED**

0 = Disabled; 1 = Enabled

F104**ENUMERATION: RESET TYPE**

0 = Instantaneous, 1 = Timed, 2 = Linear

F105**ENUMERATION: LOGIC INPUT**

0 = Disabled, 1 = Input 1, 2 = Input 2

F106**ENUMERATION: PHASE ROTATION**

0 = ABC, 1 = ACB

F108**ENUMERATION: OFF/ON**

0 = Off, 1 = On

F109**ENUMERATION: CONTACT OUTPUT OPERATION**

0 = Self-reset, 1 = Latched, 2 = Disabled

F110**ENUMERATION: CONTACT OUTPUT LED CONTROL**

0 = Trip, 1 = Alarm, 2 = None

F112**ENUMERATION: RS485 BAUD RATES**

bitmask	value	bitmask	value	bitmask	value
0	300	4	9600	8	115200
1	1200	5	19200	9	14400
2	2400	6	38400	10	28800
3	4800	7	57600	11	33600

F113**ENUMERATION: PARITY**

0 = None, 1 = Odd, 2 = Even

F114**ENUMERATION: IRIG-B SIGNAL TYPE**

0 = None, 1 = DC Shift, 2 = Amplitude Modulated

F115**ENUMERATION: BREAKER STATUS**

0 = Auxiliary A, 1 = Auxiliary B

F116**ENUMERATION: NEUTRAL OVERVOLTAGE CURVES**

0 = Definite Time, 1 = FlexCurve™ A, 2 = FlexCurve™ B, 3 = FlexCurve™ C

F117**ENUMERATION: NUMBER OF OSCILLOGRAPHY RECORDS**

0 = 1×72 cycles, 1 = 3×36 cycles, 2 = 7×18 cycles, 3 = 15×9 cycles

F118**ENUMERATION: OSCILLOGRAPHY MODE**

0 = Automatic Overwrite, 1 = Protected

F119**ENUMERATION: FLEXCURVE™ PICKUP RATIOS**

mask	value	mask	value	mask	value	mask	value
0	0.00	30	0.88	60	2.90	90	5.90
1	0.05	31	0.90	61	3.00	91	6.00
2	0.10	32	0.91	62	3.10	92	6.50
3	0.15	33	0.92	63	3.20	93	7.00
4	0.20	34	0.93	64	3.30	94	7.50
5	0.25	35	0.94	65	3.40	95	8.00
6	0.30	36	0.95	66	3.50	96	8.50
7	0.35	37	0.96	67	3.60	97	9.00
8	0.40	38	0.97	68	3.70	98	9.50
9	0.45	39	0.98	69	3.80	99	10.00
10	0.48	40	1.03	70	3.90	100	10.50
11	0.50	41	1.05	71	4.00	101	11.00
12	0.52	42	1.10	72	4.10	102	11.50
13	0.54	43	1.20	73	4.20	103	12.00
14	0.56	44	1.30	74	4.30	104	12.50
15	0.58	45	1.40	75	4.40	105	13.00
16	0.60	46	1.50	76	4.50	106	13.50
17	0.62	47	1.60	77	4.60	107	14.00
18	0.64	48	1.70	78	4.70	108	14.50
19	0.66	49	1.80	79	4.80	109	15.00
20	0.68	50	1.90	80	4.90	110	15.50
21	0.70	51	2.00	81	5.00	111	16.00
22	0.72	52	2.10	82	5.10	112	16.50
23	0.74	53	2.20	83	5.20	113	17.00
24	0.76	54	2.30	84	5.30	114	17.50
25	0.78	55	2.40	85	5.40	115	18.00
26	0.80	56	2.50	86	5.50	116	18.50
27	0.82	57	2.60	87	5.60	117	19.00
28	0.84	58	2.70	88	5.70	118	19.50
29	0.86	59	2.80	89	5.80	119	20.00

F122**ENUMERATION: ELEMENT INPUT SIGNAL TYPE**

0 = Phasor, 1 = RMS

F123**ENUMERATION: CT SECONDARY**

0 = 1 A, 1 = 5 A

F124**ENUMERATION: LIST OF ELEMENTS**

bitmask	element
0	Phase Instantaneous Overcurrent 1
1	Phase Instantaneous Overcurrent 2
2	Phase Instantaneous Overcurrent 3
3	Phase Instantaneous Overcurrent 4
4	Phase Instantaneous Overcurrent 5
5	Phase Instantaneous Overcurrent 6
6	Phase Instantaneous Overcurrent 7
7	Phase Instantaneous Overcurrent 8
8	Phase Instantaneous Overcurrent 9
9	Phase Instantaneous Overcurrent 10
10	Phase Instantaneous Overcurrent 11
11	Phase Instantaneous Overcurrent 12
102	8-bit Comparator 1
103	8-bit Comparator 2
104	8-bit Comparator 3
105	8-bit Comparator 4
106	8-bit Comparator 5
107	8-bit Comparator 6
144	Phase Undervoltage 1
145	Phase Undervoltage 2
152	Phase Overvoltage 1
214	Sensitive Directional Power 1
215	Sensitive Directional Power 2
224	SRC1 VT Fuse Failure
225	SRC2 VT Fuse Failure
226	SRC3 VT Fuse Failure
227	SRC4 VT Fuse Failure
228	SRC5 VT Fuse Failure
229	SRC6 VT Fuse Failure
232	SRC1 50DD (Disturbance Detection)
233	SRC2 50DD (Disturbance Detection)
234	SRC3 50DD (Disturbance Detection)
235	SRC4 50DD (Disturbance Detection)
236	SRC5 50DD (Disturbance Detection)
237	SRC6 50DD (Disturbance Detection)
242	Open Pole Detector
266	Digitizer 1
267	Digitizer 2
268	Digitizer 3
269	Digitizer 4
270	Digitizer 5
272	Breaker 1
273	Breaker 2
311	PMU One-Shot
312	Synchrocheck 1

bitmask	element
313	Synchrocheck 2
336	Setting Group
337	Reset
344	Overfrequency 1
345	Overfrequency 2
346	Overfrequency 3
347	Overfrequency 4
352	Underfrequency 1
353	Underfrequency 2
354	Underfrequency 3
355	Underfrequency 4
356	Underfrequency 5
357	Underfrequency 6
385	Selector 1
386	Selector 2
390	Control Pushbutton 1
391	Control Pushbutton 2
392	Control Pushbutton 3
393	Control Pushbutton 4
394	Control Pushbutton 5
395	Control Pushbutton 6
396	Control Pushbutton 7
400	FlexElement™ 1
401	FlexElement™ 2
402	FlexElement™ 3
403	FlexElement™ 4
404	FlexElement™ 5
405	FlexElement™ 6
406	FlexElement™ 7
407	FlexElement™ 8
408	FlexElement™ 9
409	FlexElement™ 10
410	FlexElement™ 11
411	FlexElement™ 12
412	FlexElement™ 13
413	FlexElement™ 14
414	FlexElement™ 15
415	FlexElement™ 16
420	Non-volatile Latch 1
421	Non-volatile Latch 2
422	Non-volatile Latch 3
423	Non-volatile Latch 4
424	Non-volatile Latch 5
425	Non-volatile Latch 6
426	Non-volatile Latch 7
427	Non-volatile Latch 8
428	Non-volatile Latch 9
429	Non-volatile Latch 10
430	Non-volatile Latch 11
431	Non-volatile Latch 12
432	Non-volatile Latch 13
433	Non-volatile Latch 14
434	Non-volatile Latch 15

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bitmask	element
435	Non-volatile Latch 16
530	Frequency Rate of Change 1
531	Frequency Rate of Change 2
532	Frequency Rate of Change 3
533	Frequency Rate of Change 4
536	8-bit Switch 1
537	8-bit Switch 2
538	8-bit Switch 3
539	8-bit Switch 4
540	8-bit Switch 5
541	8-bit Switch 6
544	Digital Counter 1
545	Digital Counter 2
546	Digital Counter 3
547	Digital Counter 4
548	Digital Counter 5
549	Digital Counter 6
550	Digital Counter 7
551	Digital Counter 8
680	User-Programmable Pushbutton 1
681	User-Programmable Pushbutton 2
682	User-Programmable Pushbutton 3
683	User-Programmable Pushbutton 4
684	User-Programmable Pushbutton 5
685	User-Programmable Pushbutton 6
686	User-Programmable Pushbutton 7
687	User-Programmable Pushbutton 8
688	User-Programmable Pushbutton 9
689	User-Programmable Pushbutton 10
690	User-Programmable Pushbutton 11
691	User-Programmable Pushbutton 12
692	Digital Element 1
693	Digital Element 2
694	Digital Element 3
695	Digital Element 4
696	Digital Element 5
697	Digital Element 6
698	Digital Element 7
699	Digital Element 8
700	Digital Element 9
701	Digital Element 10
702	Digital Element 11
703	Digital Element 12
704	Digital Element 13
705	Digital Element 14
706	Digital Element 15
707	Digital Element 16
708	Digital Element 17
709	Digital Element 18
710	Digital Element 19
711	Digital Element 20
712	Digital Element 21
713	Digital Element 22

bitmask	element
714	Digital Element 23
715	Digital Element 24
716	Digital Element 25
717	Digital Element 26
718	Digital Element 27
719	Digital Element 28
720	Digital Element 29
721	Digital Element 30
722	Digital Element 31
723	Digital Element 32
724	Digital Element 33
725	Digital Element 34
726	Digital Element 35
727	Digital Element 36
728	Digital Element 37
729	Digital Element 38
730	Digital Element 39
731	Digital Element 40
732	Digital Element 41
733	Digital Element 42
734	Digital Element 43
735	Digital Element 44
736	Digital Element 45
737	Digital Element 46
738	Digital Element 47
739	Digital Element 48
740	PMU 1 Frequency
741	PMU 2 Frequency
742	PMU 3 Frequency
743	PMU 4 Frequency
746	PMU 1 Voltage
747	PMU 2 Voltage
748	PMU 3 Voltage
749	PMU 4 Voltage
752	PMU 1 Current
753	PMU 2 Current
754	PMU 3 Current
755	PMU 4 Current
758	PMU 1 Power
759	PMU 2 Power
760	PMU 3 Power
761	PMU 4 Power
764	PMU 1 Rate of Change of Frequency
765	PMU 2 Rate of Change of Frequency
766	PMU 3 Rate of Change of Frequency
767	PMU 4 Rate of Change of Frequency
770	PMU 1
771	PMU 2
772	PMU 3
773	PMU 4

F125**ENUMERATION: ACCESS LEVEL**

0 = Restricted; 1 = Command, 2 = Setting, 3 = Factory Service

F126**ENUMERATION: NO/YES CHOICE**

0 = No, 1 = Yes

F127**ENUMERATION: LATCHED OR SELF-RESETTING**

0 = Latched, 1 = Self-Reset

F128**ENUMERATION: CONTACT INPUT THRESHOLD**

0 = 17 V DC, 1 = 33 V DC, 2 = 84 V DC, 3 = 166 V DC

F129**ENUMERATION: FLEXLOGIC TIMER TYPE**

0 = millisecond, 1 = second, 2 = minute

F130**ENUMERATION: SIMULATION MODE**

0 = Off. 1 = Pre-Fault, 2 = Fault, 3 = Post-Fault

F131**ENUMERATION: FORCED CONTACT OUTPUT STATE**

0 = Disabled, 1 = Energized, 2 = De-energized, 3 = Freeze

F132**ENUMERATION: DEMAND INTERVAL**

0 = 5 min, 1 = 10 min, 2 = 15 min, 3 = 20 min, 4 = 30 min, 5 = 60 min

F133**ENUMERATION: PROGRAM STATE**

0 = Not Programmed, 1 = Programmed

F134**ENUMERATION: PASS/FAIL**

0 = Fail, 1 = OK, 2 = n/a

F135**ENUMERATION: GAIN CALIBRATION**

0 = 0x1, 1 = 1x16

F136**ENUMERATION: NUMBER OF OSCILLOGRAPHY RECORDS**

0 = 31 x 8 cycles, 1 = 15 x 16 cycles, 2 = 7 x 32 cycles
3 = 3 x 64 cycles, 4 = 1 x 128 cycles

F138**ENUMERATION: OSCILLOGRAPHY FILE TYPE**

0 = Data File, 1 = Configuration File, 2 = Header File

F139**ENUMERATION: DEMAND CALCULATIONS**

0 = Thermal Exponential, 1 = Block Interval, 2 = Rolling Demand

F140**ENUMERATION: CURRENT, SENS CURRENT, VOLTAGE, DISABLED**

0 = Disabled, 1 = Current 46 A, 2 = Voltage 280 V,
3 = Current 4.6 A, 4 = Current 2 A, 5 = Notched 4.6 A,
6 = Notched 2 A

F141**ENUMERATION: SELF TEST ERROR**

bitmask	error
0	Any Self Tests
1	IRIG-B Failure
2	DSP Error
4	No DSP Interrupts
5	Unit Not Calibrated
9	Prototype Firmware
10	Flexlogic Error Token
11	Equipment Mismatch
13	Unit Not Programmed
14	System Exception
15	Latching Out Error
18	SNTP Failure
19	Battery Failure
20	Primary Ethernet Failure
21	Secondary Ethernet Failure
22	EEPROM Data Error
23	SRAM Data Error
24	Program Memory
25	Watchdog Error
26	Low On Memory
27	Remote Device Off
28	Direct Device Off
29	Direct Ring Break
30	Any Minor Error
31	Any Major Error

F142**ENUMERATION: EVENT RECORDER ACCESS FILE TYPE**

0 = All Record Data, 1 = Headers Only, 2 = Numeric Event Cause

F143**UR_UINT32: 32 BIT ERROR CODE (F141 specifies bit number)**

A bit value of 0 = no error, 1 = error

F144**ENUMERATION: FORCED CONTACT INPUT STATE**

0 = Disabled, 1 = Open, 2 = Closed

F145**ENUMERATION: ALPHABET LETTER**

bitmask	type	bitmask	type	bitmask	type	bitmask	type
0	null	7	G	14	N	21	U
1	A	8	H	15	O	22	V
2	B	9	I	16	P	23	W
3	C	10	J	17	Q	24	X
4	D	11	K	18	R	25	Y
5	E	12	L	19	S	26	Z
6	F	13	M	20	T		

F146**ENUMERATION: MISCELLANEOUS EVENT CAUSES**

bitmask	definition
0	Events Cleared
1	Oscillography Triggered
2	Date/time Changed
3	Default Settings Loaded
4	Test Mode On
5	Test Mode Off
6	Power On
7	Power Off
8	Relay In Service
9	Relay Out Of Service
10	Watchdog Reset
11	Oscillography Clear
12	Reboot Command

bitmask	definition
13	Led Test Initiated
14	Flash Programming
15	Fault Report Trigger
16	User Programmable Fault Report Trigger
17	Corrupt DSP Program
18	Reload DSP Settings

F151**ENUMERATION: RTD SELECTION**

bitmask	RTD#	bitmask	RTD#	bitmask	RTD#
0	NONE	17	RTD 17	33	RTD 33
1	RTD 1	18	RTD 18	34	RTD 34
2	RTD 2	19	RTD 19	35	RTD 35
3	RTD 3	20	RTD 20	36	RTD 36
4	RTD 4	21	RTD 21	37	RTD 37
5	RTD 5	22	RTD 22	38	RTD 38
6	RTD 6	23	RTD 23	39	RTD 39
7	RTD 7	24	RTD 24	40	RTD 40
8	RTD 8	25	RTD 25	41	RTD 41
9	RTD 9	26	RTD 26	42	RTD 42
10	RTD 10	27	RTD 27	43	RTD 43
11	RTD 11	28	RTD 28	44	RTD 44
12	RTD 12	29	RTD 29	45	RTD 45
13	RTD 13	30	RTD 30	46	RTD 46
14	RTD 14	31	RTD 31	47	RTD 47
15	RTD 15	32	RTD 32	48	RTD 48
16	RTD 16				

F152**ENUMERATION: SETTING GROUP**

0 = Active Group, 1 = Group 1, 2 = Group 2, 3 = Group 3
4 = Group 4, 5 = Group 5, 6 = Group 6

F155**ENUMERATION: REMOTE DEVICE STATE**

0 = Offline, 1 = Online

F156**ENUMERATION: REMOTE INPUT BIT PAIRS**

bitmask	value	bitmask	value	bitmask	value
0	NONE	22	DNA-22	44	UserSt-12
1	DNA-1	23	DNA-23	45	UserSt-13
2	DNA-2	24	DNA-24	46	UserSt-14
3	DNA-3	25	DNA-25	47	UserSt-15
4	DNA-4	26	DNA-26	48	UserSt-16
5	DNA-5	27	DNA-27	49	UserSt-17
6	DNA-6	28	DNA-28	50	UserSt-18
7	DNA-7	29	DNA-29	51	UserSt-19
8	DNA-8	30	DNA-30	52	UserSt-20
9	DNA-9	31	DNA-31	53	UserSt-21
10	DNA-10	32	DNA-32	54	UserSt-22
11	DNA-11	33	UserSt-1	55	UserSt-23
12	DNA-12	34	UserSt-2	56	UserSt-24
13	DNA-13	35	UserSt-3	57	UserSt-25
14	DNA-14	36	UserSt-4	58	UserSt-26
15	DNA-15	37	UserSt-5	59	UserSt-27
16	DNA-16	38	UserSt-6	60	UserSt-28
17	DNA-17	39	UserSt-7	61	UserSt-29
18	DNA-18	40	UserSt-8	62	UserSt-30
19	DNA-19	41	UserSt-9	63	UserSt-31
20	DNA-20	42	UserSt-10	64	UserSt-32
21	DNA-21	43	UserSt-11		

F157**ENUMERATION: BREAKER MODE**

0 = 3-Pole, 1 = 1-Pole

F159**ENUMERATION: BREAKER AUX CONTACT KEYING**

0 = 52a, 1 = 52b, 2 = None

F166**ENUMERATION: AUXILIARY VT CONNECTION TYPE**

0 = Vn, 1 = Vag, 2 = Vbg, 3 = Vcg, 4 = Vab, 5 = Vbc, 6 = Vca

F167**ENUMERATION: SIGNAL SOURCE**

0 = SRC 1, 1 = SRC 2, 2 = SRC 3, 3 = SRC 4, 4 = SRC 5, 5 = SRC 6

F168**ENUMERATION: INRUSH INHIBIT FUNCTION**

0 = Disabled, 1 = Adapt. 2nd, 2 = Trad. 2nd

F170**ENUMERATION: LOW/HIGH OFFSET and GAIN
TRANSDUCER INPUT/OUTPUT SELECTION**

0 = LOW, 1 = HIGH

F171**ENUMERATION: TRANSDUCER CHANNEL INPUT TYPE**

0 = dcmA IN, 1 = Ohms IN, 2 = RTD IN, 3 = dcmA OUT

F172**ENUMERATION: SLOT LETTERS**

bitmask	slot	bitmask	slot	bitmask	slot	bitmask	slot
0	F	4	K	8	P	12	U
1	G	5	L	9	R	13	V
2	H	6	M	10	S	14	W
3	J	7	N	11	T	15	X

F173**ENUMERATION: DCMA INPUT/OUTPUT RANGE**

bitmask	dcmA input/output range
0	0 to -1 mA
1	0 to 1 mA
2	-1 to 1 mA
3	0 to 5 mA
4	0 to 10 mA
5	0 to 20 mA
6	4 to 20 mA

F174**ENUMERATION: TRANSDUCER RTD INPUT TYPE**

0 = 100 Ohm Platinum, 1 = 120 Ohm Nickel,
2 = 100 Ohm Nickel, 3 = 10 Ohm Copper

F175**ENUMERATION: PHASE LETTERS**

0 = A, 1 = B, 2 = C

F176**ENUMERATION: SYNCHROCHECK DEAD SOURCE SELECT**

bitmask	synchrocheck dead source
0	None
1	LV1 and DV2
2	DV1 and LV2
3	DV1 or DV2
4	DV1 Xor DV2
5	DV1 and DV2

F177**ENUMERATION: COMMUNICATION PORT**

0 = None, 1 = COM1-RS485, 2 = COM2-RS485,
3 = Front Panel-RS232, 4 = Network - TCP, 5 = Network - UDP

F178**ENUMERATION: DATA LOGGER RATES**

0 = 1 sec, 1 = 1 min, 2 = 5 min, 3 = 10 min, 4 = 15 min,
5 = 20 min, 6 = 30 min, 7 = 60 min, 8 = 15 ms, 9 = 30 ms,
10 = 100 ms, 11 = 500 ms

F180**ENUMERATION: PHASE/GROUND**

0 = PHASE, 1 = GROUND

F181**ENUMERATION: ODD/EVEN/NONE**

0 = ODD, 1 = EVEN, 2 = NONE

F183**ENUMERATION: AC INPUT WAVEFORMS**

bitmask	definition
0	Off
1	8 samples/cycle
2	16 samples/cycle
3	32 samples/cycle
4	64 samples/cycle

F185**ENUMERATION: PHASE A,B,C, GROUND SELECTOR**

0 = A, 1 = B, 2 = C, 3 = G

F186**ENUMERATION: MEASUREMENT MODE**

0 = Phase to Ground, 1 = Phase to Phase

F190**ENUMERATION: SIMULATED KEYPRESS**

bitmsk	keypress	bitmsk	keypress
0	--- use between real keys	21	Escape
1	1	22	Enter
2	2	23	Reset
3	3	24	User 1
4	4	25	User 2
5	5	26	User 3
6	6	27	User-programmable key 1
7	7	28	User-programmable key 2
8	8	29	User-programmable key 3
9	9	30	User-programmable key 4
10	0	31	User-programmable key 5
11	Decimal Pt	32	User-programmable key 6
12	Plus/Minus	33	User-programmable key 7
13	Value Up	34	User-programmable key 8
14	Value Down	35	User-programmable key 9
15	Message Up	36	User-programmable key 10
16	Message Down	37	User-programmable key 11
17	Message Left	38	User-programmable key 12
18	Message Right	39	User 4 (control pushbutton)
19	Menu	40	User 5 (control pushbutton)
20	Help	41	User 6 (control pushbutton)
		42	User 7 (control pushbutton)

F192**ENUMERATION: ETHERNET OPERATION MODE**

0 = Half-Duplex, 1 = Full-Duplex

F194**ENUMERATION: DNP SCALE**

A bitmask of 0 = 0.01, 1 = 0.1, 2 = 1, 3 = 10, 4 = 100, 5 = 1000,
6 = 10000, 7 = 100000, 8 = 0.001

F199**ENUMERATION: DISABLED/ENABLED/CUSTOM**

0 = Disabled, 1 = Enabled, 2 = Custom

F200**TEXT40: 40-CHARACTER ASCII TEXT**

20 registers, 16 Bits: 1st Char MSB, 2nd Char. LSB

F201**TEXT8: 8-CHARACTER ASCII PASSCODE**

4 registers, 16 Bits: 1st Char MSB, 2nd Char. LSB

F202**TEXT20: 20-CHARACTER ASCII TEXT**

10 registers, 16 Bits: 1st Char MSB, 2nd Char. LSB

F203**TEXT16: 16-CHARACTER ASCII TEXT****F204****TEXT80: 80-CHARACTER ASCII TEXT****F205****TEXT12: 12-CHARACTER ASCII TEXT****F206****TEXT6: 6-CHARACTER ASCII TEXT****F207****TEXT4: 4-CHARACTER ASCII TEXT****F208****TEXT2: 2-CHARACTER ASCII TEXT****F222****ENUMERATION: TEST ENUMERATION**

0 = Test Enumeration 0, 1 = Test Enumeration 1

F224**ENUMERATION: RATE TREND FOR FREQ RATE OF CHANGE**

0 = Increasing, 1 = Decreasing, 2 = Bidirectional

F226**ENUMERATION: REMOTE INPUT/OUTPUT TRANSFER METHOD**

0 = None, 1 = GSSE, 2 = GOOSE

F227**ENUMERATION: RELAY SERVICE STATUS**

0 = Unknown, 1 = Relay In Service, 2 = Relay Out Of Service

F260**ENUMERATION: DATA LOGGER MODE**

0 = Continuous, 1 = Trigger

F300**UR_UINT16: FLEXLOGIC™ BASE TYPE (6-bit type)**

The FlexLogic™ BASE type is 6 bits and is combined with a 9 bit descriptor and 1 bit for protection element to form a 16 bit value. The combined bits are of the form: PTTTTTDDDDDDDDDD,

where P bit if set, indicates that the FlexLogic™ type is associated with a protection element state and T represents bits for the BASE type, and D represents bits for the descriptor.

The values in square brackets indicate the base type with P prefix [PTTTTTT] and the values in round brackets indicate the descriptor range.

[0] Off(0) – this is boolean FALSE value

[0] On (1) – this is boolean TRUE value

[2] CONTACT INPUTS (1 to 96)

[3] CONTACT INPUTS OFF (1 to 96)

[4] VIRTUAL INPUTS (1 to 64)

[6] VIRTUAL OUTPUTS (1 to 96)

[10] CONTACT OUTPUTS VOLTAGE DETECTED (1 to 64)

[11] CONTACT OUTPUTS VOLTAGE OFF DETECTED (1 to 64)

[12] CONTACT OUTPUTS CURRENT DETECTED (1 to 64)

[13] CONTACT OUTPUTS CURRENT OFF DETECTED (1 to 64)

[14] REMOTE INPUTS (1 to 32)

[28] INSERT (via keypad only)

[32] END

[34] NOT (1 INPUT)

[36] 2 INPUT XOR (0)

[38] LATCH SET/RESET (2 inputs)

[40] OR (2 to 16 inputs)

[42] AND (2 to 16 inputs)

[44] NOR (2 to 16 inputs)

[46] NAND (2 to 16 inputs)

[48] TIMER (1 to 32)

[50] ASSIGN VIRTUAL OUTPUT (1 to 96)

[52] SELF-TEST ERROR (see F141 for range)

[56] ACTIVE SETTING GROUP (1 to 6)

[62] MISCELLANEOUS EVENTS (see F146 for range)

[64 to 127] ELEMENT STATES

F400**UR_UINT16: CT/VT BANK SELECTION**

bitmask	bank selection
0	Card 1 Contact 1 to 4
1	Card 1 Contact 5 to 8
2	Card 2 Contact 1 to 4
3	Card 2 Contact 5 to 8
4	Card 3 Contact 1 to 4
5	Card 3 Contact 5 to 8

F500**UR_UINT16: PACKED BITFIELD**

First register indicates input/output state with bits 0 (MSB) to 15 (LSB) corresponding to input/output state 1 to 16. The second register indicates input/output state with bits 0 to 15 corresponding to input/output state 17 to 32 (if required). The third register indicates input/output state with bits 0 to 15 corresponding to input/output state 33 to 48 (if required). The fourth register indicates input/output state with bits 0 to 15 corresponding to input/output state 49 to 64 (if required).

The number of registers required is determined by the specific data item. A bit value of 0 = Off and 1 = On.

F501**UR_UINT16: LED STATUS**

Low byte of register indicates LED status with bit 0 representing the top LED and bit 7 the bottom LED. A bit value of 1 indicates the LED is on, 0 indicates the LED is off.

F502**BITFIELD: ELEMENT OPERATE STATES**

Each bit contains the operate state for an element. See the F124 format code for a list of element IDs. The operate bit for element ID X is bit $[X \bmod 16]$ in register $[X/16]$.

F504**BITFIELD: 3-PHASE ELEMENT STATE**

bitmask	element state
0	Pickup
1	Operate
2	Pickup Phase A
3	Pickup Phase B
4	Pickup Phase C
5	Operate Phase A
6	Operate Phase B
7	Operate Phase C

F505**BITFIELD: CONTACT OUTPUT STATE**

0 = Contact State, 1 = Voltage Detected, 2 = Current Detected

F506|**BITFIELD: 1 PHASE ELEMENT STATE**

0 = Pickup, 1 = Operate

F507**BITFIELD: COUNTER ELEMENT STATE**

0 = Count Greater Than, 1 = Count Equal To, 2 = Count Less Than

F509**BITFIELD: SIMPLE ELEMENT STATE**

0 = Operate

F511**BITFIELD: 3-PHASE SIMPLE ELEMENT STATE**

0 = Operate, 1 = Operate A, 2 = Operate B, 3 = Operate C

F513**ENUMERATION: POWER SWING MODE**

0 = Two Step, 1 = Three Step

F514**ENUMERATION: POWER SWING TRIP MODE**

0 = Delayed, 1 = Early

F515**ENUMERATION ELEMENT INPUT MODE**

0 = Signed, 1 = Absolute

F516**ENUMERATION ELEMENT COMPARE MODE**

0 = Level, 1 = Delta

F518**ENUMERATION: FLEXELEMENT™ UNITS**

0 = Milliseconds, 1 = Seconds, 2 = Minutes

F519**ENUMERATION: NON-VOLATILE LATCH**

0 = Reset-Dominant, 1 = Set-Dominant

F522**ENUMERATION: TRANSDUCER DCMA OUTPUT RANGE**

0 = -1 to 1 mA; 1 = 0 to 1 mA; 2 = 4 to 20 mA

F523**ENUMERATION: DNP OBJECTS 20, 22, AND 23 DEFAULT VARIATION**

bitmask	default variation
0	1
1	2
2	5
3	6

F524**ENUMERATION: DNP OBJECT 21 DEFAULT VARIATION**

bitmask	Default Variation
0	1
1	2
2	9
3	10

F525**ENUMERATION: DNP OBJECT 32 DEFAULT VARIATION**

bitmask	default variation
0	1
1	2
2	3
3	4
4	5
5	7

F530**ENUMERATION: FRONT PANEL INTERFACE KEYPRESS**

bitmask	keypress	bitmask	keypress
0	None	22	Value Down
1	Menu	23	Reset
2	Message Up	24	User 1
3	7	25	User 2
4	8	26	User 3
5	9	31	User PB 1
6	Help	32	User PB 2
7	Message Left	33	User PB 3
8	4	34	User PB 4
9	5	35	User PB 5
10	6	36	User PB 6
11	Escape	37	User PB 7
12	Message Right	38	User PB 8
13	1	39	User PB 9
14	2	40	User PB 10
15	3	41	User PB 11
16	Enter	42	User PB 12
17	Message Down	44	User 4
18	0	45	User 5
19	Decimal	46	User 6
20	+/-	47	User 7
21	Value Up		

F531**ENUMERATION: LANGUAGE**

0 = English, 1 = French, 2 = Chinese, 3 = Russian

F540**ENUMERATION: PMU POST-FILTER**

0 = None, 1 = Symm-3-Point, 2 = Symm-5-Point, 3 = Symm-7-Point, 4 = Asymm-2-Point, 5 = Asymm-4-Point, 6 = Asymm-5-Point, 7 = Asymm-6-Point

F541**ENUMERATION: PMU TRIGGERING STYLE**

0 = Timed, 1 = Forced

F542**ENUMERATION: PMU TRIGGERING MODE**

0 = Automatic Overwrite, 1 = Protected

F543**ENUMERATION: PMU PHASORS**

value	phasor	value	phasor
0	Off	8	Ig
1	Va	9	V_1
2	Vb	10	V_2
3	Vc	11	V_0
4	Vx	12	I_1
5	Ia	13	I_2
6	Ib	14	I_0
7	Ic		

F544**ENUMERATION: PMU RECORDING/REPORTING RATE**

value	rate	value	rate
0	1/second	6	20/second
1	2/second	7	25/second
2	5/second	8	30/second
3	10/second	9	50/second
4	12/second	10	60/second
5	15/second		

F545**ENUMERATION: PMU COM PORT TYPE**

0 = Network, 1 = RS485, 2 = Dir Comm Ch1, 3 = Dir Comm Ch2

F545**ENUMERATION: PMU REPORTING STYLE**

0 = Polar, 1 = Rectangular

F545**ENUMERATION: PMU REPORTING FORMAT**

0 = Integer, 1 = Floating

F600**UR_UINT16: FLEXANALOG PARAMETER**

Corresponds to the modbus address of the value used when this parameter is selected. Only certain values may be used as Flex-Analogs (basically all metering quantities used in protection).

B

C.1.1 OVERVIEW

The IEC 61850 standard is the result of years of work by electric utilities and vendors of electronic equipment to produce standardized communications systems. IEC 61850 is a series of standards describing client/server and peer-to-peer communications, substation design and configuration, testing, environmental and project standards. The complete set includes:

- IEC 61850-1: Introduction and overview
- IEC 61850-2: Glossary
- IEC 61850-3: General requirements
- IEC 61850-4: System and project management
- IEC 61850-5: Communications and requirements for functions and device models
- IEC 61850-6: Configuration description language for communication in electrical substations related to IEDs
- IEC 61850-7-1: Basic communication structure for substation and feeder equipment - Principles and models
- IEC 61850-7-2: Basic communication structure for substation and feeder equipment - Abstract communication service interface (ACSI)
- IEC 61850-7-3: Basic communication structure for substation and feeder equipment – Common data classes
- IEC 61850-7-4: Basic communication structure for substation and feeder equipment – Compatible logical node classes and data classes
- IEC 61850-8-1: Specific Communication Service Mapping (SCSM) – Mappings to MMS (ISO 9506-1 and ISO 9506-2) and to ISO/IEC 8802-3
- IEC 61850-9-1: Specific Communication Service Mapping (SCSM) – Sampled values over serial unidirectional multi-drop point to point link
- IEC 61850-9-2: Specific Communication Service Mapping (SCSM) – Sampled values over ISO/IEC 8802-3
- IEC 61850-10: Conformance testing

These documents can be obtained from the IEC (<http://www.iec.ch>). It is strongly recommended that all those involved with any IEC 61850 implementation obtain this document set.

C.1.2 COMMUNICATION PROFILES

The N60 relay supports IEC 61850 server services over both TCP/IP and TP4/CLNP (OSI) communication protocol stacks. The TP4/CLNP profile requires the N60 to have a network address or Network Service Access Point (NSAP) to establish a communication link. The TCP/IP profile requires the N60 to have an IP address to establish communications. These addresses are located in the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **NETWORK** menu. Note that the N60 supports IEC 61850 over the TP4/CLNP or TCP/IP stacks, and also operation over both stacks simultaneously. It is possible to have up to four simultaneous connections (in addition to DNP and Modbus/TCP (non-IEC 61850) connections).

C.1.3 MMS PROTOCOL

IEC 61850 specifies the use of the Manufacturing Message Specification (MMS) at the upper (application) layer for transfer of real-time data. This protocol has been in existence for a number of years and provides a set of services suitable for the transfer of data within a substation LAN environment. Actual MMS protocol services are mapped to IEC 61850 abstract services in IEC 61850-8-1.

C.1.4 PEER-TO-PEER COMMUNICATION

Peer-to-peer communication of digital state information (remote inputs/outputs) is supported using the IEC 61850 GSSE and GOOSE services. This feature allows digital points to be exchanged between IEC 61850 conforming devices.

C.1.5 FILE SERVICES

MMS file services are supported to allow transfer of oscillography, event record, or other files from a N60 relay.

C.1.6 COMMUNICATION SOFTWARE UTILITIES

The exact structure and values of the supported IEC 61850 logical nodes can be seen by connecting to a N60 relay with an MMS browser, such as the “MMS Object Explorer and AXS4-MMS” DDE/OPC server from Sisco Inc.

C.1.7 NON-IEC 61850 DATA

The N60 relay makes available a number of non-IEC 61850 data items. These data items can be accessed through the “UR” MMS domain. IEC 61850 data can be accessed through the “IECDevice” MMS domain (IEC 61850 logical device).

C.1.8 TCP CONNECTION TIMING

A built-in TCP/IP connection timeout of two minutes is employed by the N60 to detect ‘dead’ connections. If there is no data traffic on a TCP connection for greater than two minutes, the connection will be aborted by the N60. This frees up the connection to be used by other clients. Therefore, when using IEC 61850 reporting, clients should configure report control block items such that an integrity report will be issued at least every 2 minutes (120000 ms). This ensures that the N60 will not abort the connection. If other MMS data is being polled on the same connection at least once every 2 minutes, this timeout will not apply.

C.1.9 LOGICAL NODE MMXU DATA MAPPING

The mapping of N60 relay data to IEC 61850 MMXU data is performed on a per-source basis. MMXU1 data originates from N60 source 1, MMXU2 data originates from N60 source 2, etc.

C.1.10 LOGICAL NODE GGIO DATA MAPPING

Logical node GGIO1 data is mapped using the N60 Flexstate parameters. Each single point indication in GGIO1 can be selected using the corresponding Flexstate parameter setting. For example, the value of GGIO1 point “Ind3” is determined from the FlexLogic™ operand selected in the Flexstate parameter 3 setting. Thus, GGIO1 data can originate as any FlexLogic™ parameter.

Logical node GGIO2 data is mapped to the N60 virtual inputs. Each single point control in GGIO2 is mapped to a virtual input. For example, GGIO2 control point SPCSO3 is mapped to virtual input 3.

C.1.11 OTHER LOGICAL NODE MAPPING

All other IEC 61850 logical nodes (with the exception of PTRC) are associated with standard UR-series relay protection elements and features. The following mapping is used (for applicable elements):

- PIOC: phase instantaneous overcurrent, neutral instantaneous overcurrent, ground instantaneous overcurrent, negative sequence instantaneous overcurrent
- PTOC: phase time overcurrent, neutral time overcurrent, ground time overcurrent, negative sequence time overcurrent, neutral directional overcurrent, negative sequence directional overcurrent
- PTUV: phase undervoltage, auxiliary undervoltage, third harmonic neutral undervoltage
- PTOV: phase overvoltage, neutral overvoltage, auxiliary overvoltage, negative sequence overvoltage
- RPSB: power swing detection
- XCBR: breaker control

C.2.1 ACSI BASIC CONFORMANCE STATEMENT

SERVICES		SERVER/ PUBLISHER	UR-FAMILY
CLIENT-SERVER ROLES			
B11	Server side (of two-party application-association)	c1	Yes
B12	Client side (of two-party application-association)	---	
SCSMS SUPPORTED			
B21	SCSM : IEC 61850-8-1 used		Yes
B22	SCSM : IEC 61850-9-1 used		
B23	SCSM : IEC 61850-9-2 used		
B24	SCSM : other		
GENERIC SUBSTATION EVENT MODEL (GSE)			
B31	Publisher side	O	Yes
B32	Subscriber side	---	Yes
TRANSMISSION OF SAMPLED VALUE MODEL (SVC)			
B41	Publisher side	O	
B42	Subscriber side	---	



c1: shall be "M" if support for LOGICAL-DEVICE model has been declared

O: Optional

M: Mandatory

C.2.2 ACSI MODELS CONFORMANCE STATEMENT

SERVICES		SERVER/ PUBLISHER	UR-FAMILY
IF SERVER SIDE (B11) SUPPORTED			
M1	Logical device	c2	Yes
M2	Logical node	c3	Yes
M3	Data	c4	Yes
M4	Data set	c5	Yes
M5	Substitution	O	
M6	Setting group control	O	
REPORTING			
M7	Buffered report control	O	Yes
M7-1	sequence-number		
M7-2	report-time-stamp		
M7-3	reason-for-inclusion		
M7-4	data-set-name		
M7-5	data-reference		
M7-6	buffer-overflow		
M7-7	entryID		
M7-8	BufTm		
M7-9	IntgPd		
M7-10	GI		
M8	Unbuffered report control	O	Yes
M8-1	sequence-number		
M8-2	report-time-stamp		
M8-3	reason-for-inclusion		

SERVICES		SERVER/ PUBLISHER	UR-FAMILY
M8-4	data-set-name		
M8-5	data-reference		
M8-6	BufTm		
M8-7	IntgPd		
M8-8	GI		
	Logging	O	
M9	Log control	O	
M9-1	IntgPd		
M10	Log	O	
M11	Control	M	Yes
IF GSE (B31/32) IS SUPPORTED			
	GOOSE	O	Yes
M12-1	entryID		
M12-2	DataRefInc		
M13	GSSE	O	Yes
IF SVC (B41/B42) IS SUPPORTED			
M14	Multicast SVC	O	
M15	Unicast SVC	O	
M16	Time	M	Yes
M17	File transfer	O	Yes



NOTE

c2: shall be "M" if support for LOGICAL-NODE model has been declared

c3: shall be "M" if support for DATA model has been declared

c4: shall be "M" if support for DATA-SET, Substitution, Report, Log Control, or Time models has been declared

c5: shall be "M" if support for Report, GSE, or SMV models has been declared

M: Mandatory

C.2.3 ACSI SERVICES CONFORMANCE STATEMENT

In the table below, the acronym AA refers to Application Associations (TP: Two Party / MC: Multicast). The c6 to c10 entries are defined in the notes following the table.

SERVICES		AA: TP/MC	SERVER/ PUBLISHER	UR FAMILY
SERVER (CLAUSE 6)				
S1	ServerDirectory	TP	M	Yes
APPLICATION ASSOCIATION (CLAUSE 7)				
S2	Associate		M	Yes
S3	Abort		M	Yes
S4	Release		M	Yes
LOGICAL DEVICE (CLAUSE 8)				
S5	LogicalDeviceDirectory	TP	M	Yes
LOGICAL NODE (CLAUSE 9)				
S6	LogicalNodeDirectory	TP	M	Yes
S7	GetAllDataValues	TP	M	Yes
DATA (CLAUSE 10)				
S8	GetDataValues	TP	M	Yes
S9	SetDataValues	TP	O	Yes
S10	GetDataDirectory	TP	M	Yes
S11	GetDataDefinition	TP	M	Yes

SERVICES		AA: TP/MC	SERVER/ PUBLISHER	UR FAMILY
DATA SET (CLAUSE 11)				
S12	GetDataSetValues	TP	M	Yes
S13	SetDataSetValues	TP	O	
S14	CreateDataSet	TP	O	
S15	DeleteDataSet	TP	O	
S16	GetDataSetDirectory	TP	O	Yes
SUBSTITUTION (CLAUSE 12)				
S17	SetDataValues	TP	M	
SETTING GROUP CONTROL (CLAUSE 13)				
S18	SelectActiveSG	TP	O	
S19	SelectEditSG	TP	O	
S20	SetSGValues	TP	O	
S21	ConfirmEditSGValues	TP	O	
S22	GetSGValues	TP	O	
S23	GetSGCBValues	TP	O	
REPORTING (CLAUSE 14)				
BUFFERED REPORT CONTROL BLOCK (BRCB)				
S24	Report	TP	c6	Yes
S24-1	data-change (dchg)			Yes
S24-2	qchg-change (qchg)			
S24-3	data-update (dupd)			
S25	GetBRCBValues	TP	c6	Yes
S26	SetBRCBValues	TP	c6	Yes
UNBUFFERED REPORT CONTROL BLOCK (URCB)				
S27	Report	TP	c6	Yes
S27-1	data-change (dchg)			Yes
S27-2	qchg-change (qchg)			
S27-3	data-update (dupd)			
S28	GetURCBValues	TP	c6	Yes
S29	SetURCBValues	TP	c6	Yes
LOGGING (CLAUSE 14)				
LOG CONTROL BLOCK				
S30	GetLCBValues	TP	M	
S31	SetLCBValues	TP	M	
LOG				
S32	QueryLogByTime	TP	M	
S33	QueryLogByEntry	TP	M	
S34	GetLogStatusValues	TP	M	
GENERIC SUBSTATION EVENT MODEL (GSE) (CLAUSE 14.3.5.3.4)				
GOOSE-CONTROL-BLOCK				
S35	SendGOOSEMessage	MC	c8	Yes
S36	GetReference	TP	c9	
S37	GetGOOSEElementNumber	TP	c9	
S38	GetGoCBValues	TP	O	Yes
S39	SetGoCBValues	TP	O	Yes
GSSE-CONTROL-BLOCK				
S40	SendGSSEMessage	MC	c8	Yes
S41	GetReference	TP	c9	

SERVICES		AA: TP/MC	SERVER/ PUBLISHER	UR FAMILY
S42	GetGSSEElementNumber	TP	c9	
S43	GetGsCBValues	TP	O	Yes
S44	SetGsCBValues	TP	O	Yes
TRANSMISSION OF SAMPLE VALUE MODEL (SVC) (CLAUSE 16)				
MULTICAST SVC				
S45	SendMSVMessage	MC	c10	
S46	GetMSVCBValues	TP	O	
S47	SetMSVCBValues	TP	O	
UNICAST SVC				
S48	SendUSVMessage	MC	c10	
S49	GetUSVCBValues	TP	O	
S50	SetUSVCBValues	TP	O	
CONTROL (CLAUSE 16.4.8)				
S51	Select		O	Yes
S52	SelectWithValue	TP	O	
S53	Cancel	TP	O	Yes
S54	Operate	TP	M	Yes
S55	Command-Termination	TP	O	
S56	TimeActivated-Operate	TP	O	
FILE TRANSFER (CLAUSE 20)				
S57	GetFile	TP	M	Yes
S58	SetFile	TP	O	
S59	DeleteFile	TP	O	
S60	GetFileAttributeValues	TP	M	Yes
TIME (CLAUSE 5.5)				
T1	Time resolution of internal clock (nearest negative power of 2 in seconds)			20
T2	Time accuracy of internal clock			
T3	supported TimeStamp resolution (nearest value of 2^{-n} in seconds, according to 5.5.3.7.3.3)			20



NOTE

- c6:** shall declare support for at least one (BRCB or URCB)
c7: shall declare support for at least one (QueryLogByTime or QueryLogAfter)
c8: shall declare support for at least one (SendGOOSEMessage or SendGSSEMessage)
c9: shall declare support if TP association is available
c10: shall declare support for at least one (SendMSVMessage or SendUSVMessage)

C.3.1 LOGICAL NODES TABLE

The UR-series of relays supports IEC 61850 logical nodes as indicated in the following table. Note that the actual instantiation of each logical node is determined by the product order code. For example, the logical node “PDIS” (distance protection) is available only in the D60 Line Distance Relay.

NODES	UR-FAMILY
L: SYSTEM LOGICAL NODES	
LPHD: Physical device information	Yes
LLN0: Logical node zero	Yes
P: LOGICAL NODES FOR PROTECTION FUNCTIONS	
PDIF: Differential	Yes
PDIR: Direction comparison	---
PDIS: Distance	Yes
PDOP: Directional overpower	---
PDUP: Directional underpower	---
PFRC: Rate of change of frequency	---
PHAR: Harmonic restraint	---
PHIZ: Ground detector	---
PIOC: Instantaneous overcurrent	Yes
PMRI: Motor restart inhibition	---
PMSS: Motor starting time supervision	---
POPF: Over power factor	---
PPAM: Phase angle measuring	---
PSCH: Protection scheme	---
PSDE: Sensitive directional earth fault	---
PTEF: Transient earth fault	---
PTOC: Time overcurrent	Yes
PTOF: Overfrequency	---
PTOV: Overvoltage	Yes
PTRC: Protection trip conditioning	Yes
PTTR: Thermal overload	Yes
PTUC: Undercurrent	---
PTUV: Undervoltage	Yes
PUPF: Underpower factor	---
PTUF: Underfrequency	---
PVOC: Voltage controlled time overcurrent	---
PVPH: Volts per Hz	---
PZSU: Zero speed or underspeed	---
R: LOGICAL NODES FOR PROTECTION RELATED FUNCTIONS	
RDRE: Disturbance recorder function	---
RADR: Disturbance recorder channel analogue	---
RBDR: Disturbance recorder channel binary	---
RDRS: Disturbance record handling	---
RBRF: Breaker failure	Yes
RDIR: Directional element	---
RFLO: Fault locator	Yes
RPSB: Power swing detection/blocking	Yes
RREC: Autoreclosing	Yes

NODES	UR-FAMILY
RSYN: Synchronism-check or synchronizing	---
C: LOGICAL NODES FOR CONTROL	
CALH: Alarm handling	---
CCGR: Cooling group control	---
CILO: Interlocking	---
CPOW: Point-on-wave switching	---
CSWI: Switch controller	---
G: LOGICAL NODES FOR GENERIC REFERENCES	
GAPC: Generic automatic process control	---
GGIO: Generic process I/O	Yes
GSAL: Generic security application	---
I: LOGICAL NODES FOR INTERFACING AND ARCHIVING	
IARC: Archiving	---
IHMI: Human machine interface	---
ITCI: Telecontrol interface	---
ITMI: Telemonitoring interface	---
A: LOGICAL NODES FOR AUTOMATIC CONTROL	
ANCR: Neutral current regulator	---
ARCO: Reactive power control	---
ATCC: Automatic tap changer controller	---
AVCO: Voltage control	---
M: LOGICAL NODES FOR METERING AND MEASUREMENT	
MDIF: Differential measurements	---
MHAI: Harmonics or interharmonics	---
MHAN: Non phase related harmonics or interharmonic	---
MMTR: Metering	---
MMXN: Non phase related measurement	Yes
MMXU: Measurement	Yes
MSQI: Sequence and imbalance	---
MSTA: Metering statistics	---
S: LOGICAL NODES FOR SENSORS AND MONITORING	
SARC: Monitoring and diagnostics for arcs	---
SIMG: Insulation medium supervision (gas)	---
SIML: Insulation medium supervision (liquid)	---
SPDC: Monitoring and diagnostics for partial discharges	---
X: LOGICAL NODES FOR SWITCHGEAR	
XCBR: Circuit breaker	Yes
XSWI: Circuit switch	---
T: LOGICAL NODES FOR INSTRUMENT TRANSFORMERS	
TCTR: Current transformer	---
TVTR: Voltage transformer	---

NODES	UR-FAMILY
Y: LOGICAL NODES FOR POWER TRANSFORMERS	
YEFN: Earth fault neutralizer (Peterson coil)	---
YLTC: Tap changer	---
YPSH: Power shunt	---
YPTR: Power transformer	---
Z: LOGICAL NODES FOR FURTHER POWER SYSTEM EQUIPMENT	
ZAXN: Auxiliary network	---
ZBAT: Battery	---
ZBSH: Bushing	---
ZCAB: Power cable	---
ZCAP: Capacitor bank	---
ZCON: Converter	---
ZGEN: Generator	---
ZGIL: Gas insulated line	---
ZLIN: Power overhead line	---
ZMOT: Motor	---
ZREA: Reactor	---
ZRRC: Rotating reactive component	---
ZSAR: Surge arrestor	---
ZTCF: Thyristor controlled frequency converter	---
ZTRC: Thyristor controlled reactive component	---

D.1.1 INTEROPERABILITY DOCUMENT

This document is adapted from the IEC 60870-5-104 standard. For this section the boxes indicate the following: ☒ – used in standard direction; ☐ – not used; ☐ – cannot be selected in IEC 60870-5-104 standard.

1. SYSTEM OR DEVICE:

- ☐ System Definition
☐ Controlling Station Definition (Master)
☒ Controlled Station Definition (Slave)

2. NETWORK CONFIGURATION:

- ☐ Point-to-Point ☐ Multipoint
☐ Multiple Point-to-Point ☐ Multipoint Star

3. PHYSICAL LAYER

Transmission Speed (control direction):

Unbalanced Interchange Circuit V.24/V.28 Standard:	Unbalanced Interchange Circuit V.24/V.28 Recommended if >1200 bits/s:	Balanced Interchange Circuit X.24/X.27:
<input type="checkbox"/> 100 bits/sec. <input type="checkbox"/> 200 bits/sec. <input type="checkbox"/> 300 bits/sec. <input type="checkbox"/> 600 bits/sec. <input type="checkbox"/> 1200 bits/sec.	<input type="checkbox"/> 2400 bits/sec. <input type="checkbox"/> 4800 bits/sec. <input type="checkbox"/> 9600 bits/sec.	<input type="checkbox"/> 2400 bits/sec. <input type="checkbox"/> 4800 bits/sec. <input type="checkbox"/> 9600 bits/sec. <input type="checkbox"/> 19200 bits/sec. <input type="checkbox"/> 38400 bits/sec. <input type="checkbox"/> 56000 bits/sec. <input type="checkbox"/> 64000 bits/sec.

Transmission Speed (monitor direction):

Unbalanced Interchange Circuit V.24/V.28 Standard:	Unbalanced Interchange Circuit V.24/V.28 Recommended if >1200 bits/s:	Balanced Interchange Circuit X.24/X.27:
<input type="checkbox"/> 100 bits/sec. <input type="checkbox"/> 200 bits/sec. <input type="checkbox"/> 300 bits/sec. <input type="checkbox"/> 600 bits/sec. <input type="checkbox"/> 1200 bits/sec.	<input type="checkbox"/> 2400 bits/sec. <input type="checkbox"/> 4800 bits/sec. <input type="checkbox"/> 9600 bits/sec.	<input type="checkbox"/> 2400 bits/sec. <input type="checkbox"/> 4800 bits/sec. <input type="checkbox"/> 9600 bits/sec. <input type="checkbox"/> 19200 bits/sec. <input type="checkbox"/> 38400 bits/sec. <input type="checkbox"/> 56000 bits/sec. <input type="checkbox"/> 64000 bits/sec.

4. LINK LAYER

Link Transmission Procedure:	Address Field of the Link:
<input type="checkbox"/> Balanced Transmission <input type="checkbox"/> Unbalanced Transmission	<input type="checkbox"/> Not Present (Balanced Transmission Only) <input type="checkbox"/> One Octet <input type="checkbox"/> Two Octets <input type="checkbox"/> Structured <input type="checkbox"/> Unstructured
Frame Length (maximum length, number of octets): Not selectable in companion IEC 60870-5-104 standard	

When using an unbalanced link layer, the following ADSU types are returned in class 2 messages (low priority) with the indicated causes of transmission:

■ The standard assignment of ADSUs to class 2 messages is used as follows:

■ A special assignment of ADSUs to class 2 messages is used as follows:

5. APPLICATION LAYER

Transmission Mode for Application Data:

Mode 1 (least significant octet first), as defined in Clause 4.10 of IEC 60870-5-4, is used exclusively in this companion standard.

Common Address of ADSU:

■ One Octet

☒ Two Octets

Information Object Address:

■ One Octet

☒ Structured

■ Two Octets

☒ Unstructured

☒ Three Octets

Cause of Transmission:

■ One Octet

☒ Two Octets (with originator address). Originator address is set to zero if not used.

Maximum Length of APDU: 253 (the maximum length may be reduced by the system).

Selection of standard ASDUs:

For the following lists, the boxes indicate the following: ☒ – used in standard direction; ☐ – not used; ■ – cannot be selected in IEC 60870-5-104 standard.

Process information in monitor direction

<input checked="" type="checkbox"/> <1> := Single-point information	M_SP_NA_1
■ <2> := Single-point information with time tag	M_SP_TA_1
<input type="checkbox"/> <3> := Double-point information	M_DP_NA_1
■ <4> := Double-point information with time tag	M_DP_TA_1
<input type="checkbox"/> <5> := Step position information	M_ST_NA_1
■ <6> := Step position information with time tag	M_ST_TA_1
<input type="checkbox"/> <7> := Bitstring of 32 bits	M_BO_NA_1
■ <8> := Bitstring of 32 bits with time tag	M_BO_TA_1
<input type="checkbox"/> <9> := Measured value, normalized value	M_ME_NA_1
■ <10> := Measured value, normalized value with time tag	M_ME_TA_1
<input type="checkbox"/> <11> := Measured value, scaled value	M_ME_NB_1
■ <12> := Measured value, scaled value with time tag	M_ME_TB_1
<input checked="" type="checkbox"/> <13> := Measured value, short floating point value	M_ME_NC_1
■ <14> := Measured value, short floating point value with time tag	M_ME_TC_1
<input checked="" type="checkbox"/> <15> := Integrated totals	M_IT_NA_1
■ <16> := Integrated totals with time tag	M_IT_TA_1
■ <17> := Event of protection equipment with time tag	M_EP_TA_1
■ <18> := Packed start events of protection equipment with time tag	M_EP_TB_1
■ <19> := Packed output circuit information of protection equipment with time tag	M_EP_TC_1
<input type="checkbox"/> <20> := Packed single-point information with status change detection	M_SP_NA_1

<input type="checkbox"/> <21> := Measured value, normalized value without quantity descriptor	M_ME_ND_1
<input checked="" type="checkbox"/> <30> := Single-point information with time tag CP56Time2a	M_SP_TB_1
<input type="checkbox"/> <31> := Double-point information with time tag CP56Time2a	M_DP_TB_1
<input type="checkbox"/> <32> := Step position information with time tag CP56Time2a	M_ST_TB_1
<input type="checkbox"/> <33> := Bitstring of 32 bits with time tag CP56Time2a	M_BO_TB_1
<input type="checkbox"/> <34> := Measured value, normalized value with time tag CP56Time2a	M_ME_TD_1
<input type="checkbox"/> <35> := Measured value, scaled value with time tag CP56Time2a	M_ME_TE_1
<input type="checkbox"/> <36> := Measured value, short floating point value with time tag CP56Time2a	M_ME_TF_1
<input checked="" type="checkbox"/> <37> := Integrated totals with time tag CP56Time2a	M_IT_TB_1
<input type="checkbox"/> <38> := Event of protection equipment with time tag CP56Time2a	M_EP_TD_1
<input type="checkbox"/> <39> := Packed start events of protection equipment with time tag CP56Time2a	M_EP_TE_1
<input type="checkbox"/> <40> := Packed output circuit information of protection equipment with time tag CP56Time2a	M_EP_TF_1

Either the ASDUs of the set <2>, <4>, <6>, <8>, <10>, <12>, <14>, <16>, <17>, <18>, and <19> or of the set <30> to <40> are used.

Process information in control direction

<input checked="" type="checkbox"/> <45> := Single command	C_SC_NA_1
<input type="checkbox"/> <46> := Double command	C_DC_NA_1
<input type="checkbox"/> <47> := Regulating step command	C_RC_NA_1
<input type="checkbox"/> <48> := Set point command, normalized value	C_SE_NA_1
<input type="checkbox"/> <49> := Set point command, scaled value	C_SE_NB_1
<input type="checkbox"/> <50> := Set point command, short floating point value	C_SE_NC_1
<input type="checkbox"/> <51> := Bitstring of 32 bits	C_BO_NA_1
<input checked="" type="checkbox"/> <58> := Single command with time tag CP56Time2a	C_SC_TA_1
<input type="checkbox"/> <59> := Double command with time tag CP56Time2a	C_DC_TA_1
<input type="checkbox"/> <60> := Regulating step command with time tag CP56Time2a	C_RC_TA_1
<input type="checkbox"/> <61> := Set point command, normalized value with time tag CP56Time2a	C_SE_TA_1
<input type="checkbox"/> <62> := Set point command, scaled value with time tag CP56Time2a	C_SE_TB_1
<input type="checkbox"/> <63> := Set point command, short floating point value with time tag CP56Time2a	C_SE_TC_1
<input type="checkbox"/> <64> := Bitstring of 32 bits with time tag CP56Time2a	C_BO_TA_1

Either the ASDUs of the set <45> to <51> or of the set <58> to <64> are used.

System information in monitor direction

<input checked="" type="checkbox"/> <70> := End of initialization	M_EI_NA_1
---	-----------

System information in control direction

<input checked="" type="checkbox"/> <100> := Interrogation command	C_IC_NA_1
<input checked="" type="checkbox"/> <101> := Counter interrogation command	C_CI_NA_1
<input checked="" type="checkbox"/> <102> := Read command	C_RD_NA_1
<input checked="" type="checkbox"/> <103> := Clock synchronization command (see Clause 7.6 in standard)	C_CS_NA_1
<input checked="" type="checkbox"/> <104> := Test command	C_TS_NA_1
<input checked="" type="checkbox"/> <105> := Reset process command	C_RP_NA_1
<input checked="" type="checkbox"/> <106> := Delay acquisition command	C_CD_NA_1
<input checked="" type="checkbox"/> <107> := Test command with time tag CP56Time2a	C_TS_TA_1

Parameter in control direction

<input type="checkbox"/> <110> := Parameter of measured value, normalized value	PE_ME_NA_1
<input type="checkbox"/> <111> := Parameter of measured value, scaled value	PE_ME_NB_1
<input checked="" type="checkbox"/> <112> := Parameter of measured value, short floating point value	PE_ME_NC_1
<input type="checkbox"/> <113> := Parameter activation	PE_AC_NA_1

File transfer

<input type="checkbox"/> <120> := File Ready	F_FR_NA_1
<input type="checkbox"/> <121> := Section Ready	F_SR_NA_1
<input type="checkbox"/> <122> := Call directory, select file, call file, call section	F_SC_NA_1
<input type="checkbox"/> <123> := Last section, last segment	F_LS_NA_1
<input type="checkbox"/> <124> := Ack file, ack section	F_AF_NA_1
<input type="checkbox"/> <125> := Segment	F_SG_NA_1
<input type="checkbox"/> <126> := Directory (blank or X, available only in monitor [standard] direction)	C_CD_NA_1

Type identifier and cause of transmission assignments
 (station-specific parameters)

In the following table:

- Shaded boxes are not required.
- Black boxes are not permitted in this companion standard.
- Blank boxes indicate functions or ASDU not used.
- 'X' if only used in the standard direction

TYPE IDENTIFICATION		CAUSE OF TRANSMISSION																		
		PERIODIC, CYCLIC	BACKGROUND SCAN	SPONTANEOUS	INITIALIZED	REQUEST OR REQUESTED	ACTIVATION	ACTIVATION CONFIRMATION	DEACTIVATION	DEACTIVATION CONFIRMATION	ACTIVATION TERMINATION	RETURN INFO CAUSED BY LOCAL CMD	FILE TRANSFER	INTERROGATED BY GROUP <NUMBER>	REQUEST BY GROUP <N> COUNTER REQ	UNKNOWN TYPE IDENTIFICATION	UNKNOWN CAUSE OF TRANSMISSION	UNKNOWN COMMON ADDRESS OF ADSU	UNKNOWN INFORMATION OBJECT ADDR	UNKNOWN INFORMATION OBJECT ADDR
NO.	MNEMONIC	1	2	3	4	5	6	7	8	9	10	11	12	13	20 to 36	37 to 41	44	45	46	47
<1>	M_SP_NA_1			X		X						X	X		X					
<2>	M_SP_TA_1																			
<3>	M_DP_NA_1																			
<4>	M_DP_TA_1																			
<5>	M_ST_NA_1																			
<6>	M_ST_TA_1																			
<7>	M_BO_NA_1																			
<8>	M_BO_TA_1																			

TYPE IDENTIFICATION		CAUSE OF TRANSMISSION																		
		PERIODIC, CYCLIC	BACKGROUND SCAN	SPONTANEOUS	INITIALIZED	REQUEST OR REQUESTED	ACTIVATION	ACTIVATION CONFIRMATION	DEACTIVATION	DEACTIVATION CONFIRMATION	ACTIVATION TERMINATION	RETURN INFO CAUSED BY LOCAL CMD	FILE TRANSFER	INTERROGATED BY GROUP <NUMBER>	REQUEST BY GROUP <N> COUNTER REQ	UNKNOWN TYPE IDENTIFICATION	UNKNOWN CAUSE OF TRANSMISSION	UNKNOWN COMMON ADDRESS OF ADSU	UNKNOWN INFORMATION OBJECT ADDR	UNKNOWN INFORMATION OBJECT ADDR
NO.	MNEMONIC	1	2	3	4	5	6	7	8	9	10	11	12	13	20 to 36	37 to 41	44	45	46	47
<9>	M_ME_NA_1																			
<10>	M_ME_TA_1																			
<11>	M_ME_NB_1																			
<12>	M_ME_TB_1																			
<13>	M_ME_NC_1	X		X		X									X					
<14>	M_ME_TC_1																			
<15>	M_IT_NA_1			X												X				
<16>	M_IT_TA_1																			
<17>	M_EP_TA_1																			
<18>	M_EP_TB_1																			
<19>	M_EP_TC_1																			
<20>	M_PS_NA_1																			
<21>	M_ME_ND_1																			
<30>	M_SP_TB_1			X								X	X							
<31>	M_DP_TB_1																			
<32>	M_ST_TB_1																			
<33>	M_BO_TB_1																			
<34>	M_ME_TD_1																			
<35>	M_ME_TE_1																			
<36>	M_ME_TF_1																			
<37>	M_IT_TB_1			X												X				
<38>	M_EP_TD_1																			
<39>	M_EP_TE_1																			
<40>	M_EP_TF_1																			
<45>	C_SC_NA_1						X	X	X	X	X									
<46>	C_DC_NA_1																			
<47>	C_RC_NA_1																			
<48>	C_SE_NA_1																			
<49>	C_SE_NB_1																			

D

TYPE IDENTIFICATION		CAUSE OF TRANSMISSION																		
		PERIODIC, CYCLIC	BACKGROUND SCAN	SPONTANEOUS	INITIALIZED	REQUEST OR REQUESTED	ACTIVATION	ACTIVATION CONFIRMATION	DEACTIVATION	DEACTIVATION CONFIRMATION	ACTIVATION TERMINATION	RETURN INFO CAUSED BY LOCAL CMD	FILE TRANSFER	INTERROGATED BY GROUP <NUMBER>	REQUEST BY GROUP <N> COUNTER REQ	UNKNOWN TYPE IDENTIFICATION	UNKNOWN CAUSE OF TRANSMISSION	UNKNOWN COMMON ADDRESS OF ADSU	UNKNOWN INFORMATION OBJECT ADDR	UNKNOWN INFORMATION OBJECT ADDR
NO.	MNEMONIC	1	2	3	4	5	6	7	8	9	10	11	12	13	20 to 36	37 to 41	44	45	46	47
<50>	C_SE_NC_1																			
<51>	C_BO_NA_1																			
<58>	C_SC_TA_1						X	X	X	X	X									
<59>	C_DC_TA_1						X													
<60>	C_RC_TA_1																			
<61>	C_SE_TA_1																			
<62>	C_SE_TB_1																			
<63>	C_SE_TC_1																			
<64>	C_BO_TA_1																			
<70>	M_EI_NA_1*)				X															
<100>	C_IC_NA_1						X	X	X	X	X									
<101>	C_CI_NA_1						X	X			X									
<102>	C_RD_NA_1					X														
<103>	C_CS_NA_1			X			X	X												
<104>	C_TS_NA_1																			
<105>	C_RP_NA_1						X	X												
<106>	C_CD_NA_1																			
<107>	C_TS_TA_1																			
<110>	P_ME_NA_1																			
<111>	P_ME_NB_1																			
<112>	P_ME_NC_1						X	X							X					
<113>	P_AC_NA_1																			
<120>	F_FR_NA_1																			
<121>	F_SR_NA_1																			
<122>	F_SC_NA_1																			
<123>	F_LS_NA_1																			
<124>	F_AF_NA_1																			
<125>	F_SG_NA_1																			
<126>	F_DR_TA_1*)																			

6. BASIC APPLICATION FUNCTIONS

Station Initialization:

- ☒ Remote initialization

Cyclic Data Transmission:

- ☒ Cyclic data transmission

Read Procedure:

- ☒ Read procedure

Spontaneous Transmission:

- ☒ Spontaneous transmission

Double transmission of information objects with cause of transmission spontaneous:

The following type identifications may be transmitted in succession caused by a single status change of an information object. The particular information object addresses for which double transmission is enabled are defined in a project-specific list.

- ☐ Single point information: M_SP_NA_1, M_SP_TA_1, M_SP_TB_1, and M_PS_NA_1
- ☐ Double point information: M_DP_NA_1, M_DP_TA_1, and M_DP_TB_1
- ☐ Step position information: M_ST_NA_1, M_ST_TA_1, and M_ST_TB_1
- ☐ Bitstring of 32 bits: M_BO_NA_1, M_BO_TA_1, and M_BO_TB_1 (if defined for a specific project)
- ☐ Measured value, normalized value: M_ME_NA_1, M_ME_TA_1, M_ME_ND_1, and M_ME_TD_1
- ☐ Measured value, scaled value: M_ME_NB_1, M_ME_TB_1, and M_ME_TE_1
- ☐ Measured value, short floating point number: M_ME_NC_1, M_ME_TC_1, and M_ME_TF_1

Station interrogation:

- | | | | |
|---|---|--|--|
| <input checked="" type="checkbox"/> Global | | | |
| <input checked="" type="checkbox"/> Group 1 | <input checked="" type="checkbox"/> Group 5 | <input checked="" type="checkbox"/> Group 9 | <input checked="" type="checkbox"/> Group 13 |
| <input checked="" type="checkbox"/> Group 2 | <input checked="" type="checkbox"/> Group 6 | <input checked="" type="checkbox"/> Group 10 | <input checked="" type="checkbox"/> Group 14 |
| <input checked="" type="checkbox"/> Group 3 | <input checked="" type="checkbox"/> Group 7 | <input checked="" type="checkbox"/> Group 11 | <input checked="" type="checkbox"/> Group 15 |
| <input checked="" type="checkbox"/> Group 4 | <input checked="" type="checkbox"/> Group 8 | <input checked="" type="checkbox"/> Group 12 | <input checked="" type="checkbox"/> Group 16 |

Clock synchronization:

- ☒ Clock synchronization (optional, see Clause 7.6)

Command transmission:

- ☒ Direct command transmission
- ☐ Direct setpoint command transmission
- ☒ Select and execute command
- ☐ Select and execute setpoint command
- ☒ C_SE ACTTERM used
- ☒ No additional definition
- ☒ Short pulse duration (duration determined by a system parameter in the outstation)
- ☒ Long pulse duration (duration determined by a system parameter in the outstation)
- ☒ Persistent output

- ☒ Supervision of maximum delay in command direction of commands and setpoint commands

Maximum allowable delay of commands and setpoint commands: **10 s**

Transmission of integrated totals:

- ☒ Mode A: Local freeze with spontaneous transmission
- ☒ Mode B: Local freeze with counter interrogation
- ☒ Mode C: Freeze and transmit by counter-interrogation commands
- ☒ Mode D: Freeze by counter-interrogation command, frozen values reported simultaneously

- ☒ Counter read
- ☒ Counter freeze without reset
- ☒ Counter freeze with reset
- ☒ Counter reset

- ☒ General request counter
- ☒ Request counter group 1
- ☒ Request counter group 2
- ☒ Request counter group 3
- ☒ Request counter group 4

Parameter loading:

- ☒ Threshold value
- ☐ Smoothing factor
- ☐ Low limit for transmission of measured values
- ☐ High limit for transmission of measured values

Parameter activation:

- ☐ Activation/deactivation of persistent cyclic or periodic transmission of the addressed object

Test procedure:

- ☐ Test procedure

File transfer:

File transfer in monitor direction:

- ☐ Transparent file
- ☐ Transmission of disturbance data of protection equipment
- ☐ Transmission of sequences of events
- ☐ Transmission of sequences of recorded analog values

File transfer in control direction:

- ☐ Transparent file

Background scan:

- ☐ Background scan

Acquisition of transmission delay:

- ☒ Acquisition of transmission delay

Definition of time outs:

PARAMETER	DEFAULT VALUE	REMARKS	SELECTED VALUE
t_0	30 s	Timeout of connection establishment	120 s
t_1	15 s	Timeout of send or test APDUs	15 s
t_2	10 s	Timeout for acknowledgements in case of no data messages $t_2 < t_1$	10 s
t_3	20 s	Timeout for sending test frames in case of a long idle state	20 s

Maximum range of values for all time outs: 1 to 255 s, accuracy 1 s

Maximum number of outstanding I-format APDUs k and latest acknowledge APDUs (w):

PARAMETER	DEFAULT VALUE	REMARKS	SELECTED VALUE
k	12 APDUs	Maximum difference receive sequence number to send state variable	12 APDUs
w	8 APDUs	Latest acknowledge after receiving w I-format APDUs	8 APDUs

Maximum range of values k : 1 to 32767 ($2^{15} - 1$) APDUs, accuracy 1 APDU

Maximum range of values w : 1 to 32767 APDUs, accuracy 1 APDU

Recommendation: w should not exceed two-thirds of k .

Portnumber:

PARAMETER	VALUE	REMARKS
Portnumber	2404	In all cases

RFC 2200 suite:

RFC 2200 is an official Internet Standard which describes the state of standardization of protocols used in the Internet as determined by the Internet Architecture Board (IAB). It offers a broad spectrum of actual standards used in the Internet. The suitable selection of documents from RFC 2200 defined in this standard for given projects has to be chosen by the user of this standard.

- ☒ Ethernet 802.3
- ☐ Serial X.21 interface
- ☐ Other selection(s) from RFC 2200 (list below if selected)

D.1.2 POINTS LIST

The IEC 60870-5-104 data points are configured through the **SETTINGS** ⇒ **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **DNP / IEC104 POINT LISTS** menu. Refer to the *Communications* section of Chapter 5 for additional details.

D

E.1.1 DNP V3.00 DEVICE PROFILE

The following table provides a 'Device Profile Document' in the standard format defined in the DNP 3.0 Subset Definitions Document.

Table E-1: DNP V3.00 DEVICE PROFILE (Sheet 1 of 3)

(Also see the IMPLEMENTATION TABLE in the following section)	
Vendor Name: General Electric Multilin	
Device Name: UR Series Relay	
Highest DNP Level Supported: For Requests: Level 2 For Responses: Level 2	Device Function: <input type="checkbox"/> Master <input checked="" type="checkbox"/> Slave
Notable objects, functions, and/or qualifiers supported in addition to the Highest DNP Levels Supported (the complete list is described in the attached table): Binary Inputs (Object 1) Binary Input Changes (Object 2) Binary Outputs (Object 10) Binary Counters (Object 20) Frozen Counters (Object 21) Counter Change Event (Object 22) Frozen Counter Event (Object 23) Analog Inputs (Object 30) Analog Input Changes (Object 32) Analog Deadbands (Object 34) File Transfer (Object 70)	
Maximum Data Link Frame Size (octets): Transmitted: 292 Received: 292	Maximum Application Fragment Size (octets): Transmitted: 240 Received: 2048
Maximum Data Link Re-tries: <input type="checkbox"/> None <input checked="" type="checkbox"/> Fixed at 2 <input type="checkbox"/> Configurable	Maximum Application Layer Re-tries: <input checked="" type="checkbox"/> None <input type="checkbox"/> Configurable
Requires Data Link Layer Confirmation: <input checked="" type="checkbox"/> Never <input type="checkbox"/> Always <input type="checkbox"/> Sometimes <input type="checkbox"/> Configurable	

Table E–1: DNP V3.00 DEVICE PROFILE (Sheet 2 of 3)

Requires Application Layer Confirmation:				
<input type="checkbox"/> Never <input type="checkbox"/> Always <input checked="" type="checkbox"/> When reporting Event Data <input checked="" type="checkbox"/> When sending multi-fragment responses <input type="checkbox"/> Sometimes <input type="checkbox"/> Configurable				
Timeouts while waiting for:				
Data Link Confirm:	<input type="checkbox"/> None	<input checked="" type="checkbox"/> Fixed at 3 s	<input type="checkbox"/> Variable	<input type="checkbox"/> Configurable
Complete Appl. Fragment:	<input checked="" type="checkbox"/> None	<input type="checkbox"/> Fixed at ____	<input type="checkbox"/> Variable	<input type="checkbox"/> Configurable
Application Confirm:	<input type="checkbox"/> None	<input checked="" type="checkbox"/> Fixed at 4 s	<input type="checkbox"/> Variable	<input type="checkbox"/> Configurable
Complete Appl. Response:	<input checked="" type="checkbox"/> None	<input type="checkbox"/> Fixed at ____	<input type="checkbox"/> Variable	<input type="checkbox"/> Configurable
Others:				
Transmission Delay:	No intentional delay			
Inter-character Timeout:	50 ms			
Need Time Delay:	Configurable (default = 24 hrs.)			
Select/Operate Arm Timeout:	10 s			
Binary input change scanning period:	8 times per power system cycle			
Packed binary change process period:	1 s			
Analog input change scanning period:	500 ms			
Counter change scanning period:	500 ms			
Frozen counter event scanning period:	500 ms			
Unsolicited response notification delay:	500 ms			
Unsolicited response retry delay	configurable 0 to 60 sec.			
Sends/Executes Control Operations:				
WRITE Binary Outputs	<input checked="" type="checkbox"/> Never	<input type="checkbox"/> Always	<input type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
SELECT/OPERATE	<input type="checkbox"/> Never	<input checked="" type="checkbox"/> Always	<input type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
DIRECT OPERATE	<input type="checkbox"/> Never	<input checked="" type="checkbox"/> Always	<input type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
DIRECT OPERATE – NO ACK	<input type="checkbox"/> Never	<input checked="" type="checkbox"/> Always	<input type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Count > 1	<input checked="" type="checkbox"/> Never	<input type="checkbox"/> Always	<input type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Pulse On	<input type="checkbox"/> Never	<input type="checkbox"/> Always	<input checked="" type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Pulse Off	<input type="checkbox"/> Never	<input type="checkbox"/> Always	<input checked="" type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Latch On	<input type="checkbox"/> Never	<input type="checkbox"/> Always	<input checked="" type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Latch Off	<input type="checkbox"/> Never	<input type="checkbox"/> Always	<input checked="" type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Queue	<input checked="" type="checkbox"/> Never	<input type="checkbox"/> Always	<input type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Clear Queue	<input checked="" type="checkbox"/> Never	<input type="checkbox"/> Always	<input type="checkbox"/> Sometimes	<input type="checkbox"/> Configurable
Explanation of 'Sometimes': Object 12 points are mapped to UR Virtual Inputs. The persistence of Virtual Inputs is determined by the VIRTUAL INPUT X TYPE settings. Both "Pulse On" and "Latch On" operations perform the same function in the UR; that is, the appropriate Virtual Input is put into the "On" state. If the Virtual Input is set to "Self-Reset", it will reset after one pass of FlexLogic™. The On/Off times and Count value are ignored. "Pulse Off" and "Latch Off" operations put the appropriate Virtual Input into the "Off" state. "Trip" and "Close" operations both put the appropriate Virtual Input into the "On" state.				

Table E-1: DNP V3.00 DEVICE PROFILE (Sheet 3 of 3)

Reports Binary Input Change Events when no specific variation requested: <ul style="list-style-type: none"> <input type="checkbox"/> Never <input checked="" type="checkbox"/> Only time-tagged <input type="checkbox"/> Only non-time-tagged <input type="checkbox"/> Configurable 	Reports time-tagged Binary Input Change Events when no specific variation requested: <ul style="list-style-type: none"> <input type="checkbox"/> Never <input checked="" type="checkbox"/> Binary Input Change With Time <input type="checkbox"/> Binary Input Change With Relative Time <input type="checkbox"/> Configurable (attach explanation)
Sends Unsolicited Responses: <ul style="list-style-type: none"> <input type="checkbox"/> Never <input checked="" type="checkbox"/> Configurable <input type="checkbox"/> Only certain objects <input type="checkbox"/> Sometimes (attach explanation) <input checked="" type="checkbox"/> ENABLE/DISABLE unsolicited Function codes supported 	Sends Static Data in Unsolicited Responses: <ul style="list-style-type: none"> <input checked="" type="checkbox"/> Never <input type="checkbox"/> When Device Restarts <input type="checkbox"/> When Status Flags Change <p>No other options are permitted.</p>
Default Counter Object/Variation: <ul style="list-style-type: none"> <input type="checkbox"/> No Counters Reported <input type="checkbox"/> Configurable (attach explanation) <input checked="" type="checkbox"/> Default Object: 20 <input checked="" type="checkbox"/> Default Variation: 1 <input checked="" type="checkbox"/> Point-by-point list attached 	Counters Roll Over at: <ul style="list-style-type: none"> <input type="checkbox"/> No Counters Reported <input type="checkbox"/> Configurable (attach explanation) <input checked="" type="checkbox"/> 16 Bits (Counter 8) <input checked="" type="checkbox"/> 32 Bits (Counters 0 to 7, 9) <input type="checkbox"/> Other Value: _____ <input checked="" type="checkbox"/> Point-by-point list attached
Sends Multi-Fragment Responses: <ul style="list-style-type: none"> <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 	

E.1.2 IMPLEMENTATION TABLE

The following table identifies the variations, function codes, and qualifiers supported by the N60 in both request messages and in response messages. For static (non-change-event) objects, requests sent with qualifiers 00, 01, 06, 07, or 08, will be responded with qualifiers 00 or 01. Static object requests sent with qualifiers 17 or 28 will be responded with qualifiers 17 or 28. For change-event objects, qualifiers 17 or 28 are always responded.

Table E-2: IMPLEMENTATION TABLE (Sheet 1 of 4)

OBJECT			REQUEST		RESPONSE	
OBJECT NO.	VARIATION NO.	DESCRIPTION	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)
1	0	Binary Input (Variation 0 is used to request default variation)	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)		
	1	Binary Input	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	2	Binary Input with Status	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
2	0	Binary Input Change (Variation 0 is used to request default variation)	1 (read)	06 (no range, or all) 07, 08 (limited quantity)		
	1	Binary Input Change without Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	2	Binary Input Change with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	3 (parse only)	Binary Input Change with Relative Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)		
10	0	Binary Output Status (Variation 0 is used to request default variation)	1 (read)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)		
	2	Binary Output Status	1 (read)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
12	1	Control Relay Output Block	3 (select) 4 (operate) 5 (direct op) 6 (dir. op, noack)	00, 01 (start-stop) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	echo of request
20	0	Binary Counter (Variation 0 is used to request default variation)	1 (read) 7 (freeze) 8 (freeze noack) 9 (freeze clear) 10 (frz. cl. noack) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)		
	1	32-Bit Binary Counter	1 (read) 7 (freeze) 8 (freeze noack) 9 (freeze clear) 10 (frz. cl. noack) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)

Note 1: A default variation refers to the variation responded when variation 0 is requested and/or in class 0, 1, 2, or 3 scans. The default variations for object types 1, 2, 20, 21, 22, 23, 30, and 32 are selected via relay settings. Refer to the *Communications* section in Chapter 5 for details. This optimizes the class 0 poll data size.

Note 2: For static (non-change-event) objects, qualifiers 17 or 28 are only responded when a request is sent with qualifiers 17 or 28, respectively. Otherwise, static object requests sent with qualifiers 00, 01, 06, 07, or 08, will be responded with qualifiers 00 or 01 (for change-event objects, qualifiers 17 or 28 are always responded.)

Note 3: Cold restarts are implemented the same as warm restarts – the N60 is not restarted, but the DNP process is restarted.

Table E–2: IMPLEMENTATION TABLE (Sheet 2 of 4)

OBJECT			REQUEST		RESPONSE	
OBJECT NO.	VARIATION NO.	DESCRIPTION	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)
20 cont'd	2	16-Bit Binary Counter	1 (read) 7 (freeze) 8 (freeze noack) 9 (freeze clear) 10 (frz. cl. noack) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	5	32-Bit Binary Counter without Flag	1 (read) 7 (freeze) 8 (freeze noack) 9 (freeze clear) 10 (frz. cl. noack) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	6	16-Bit Binary Counter without Flag	1 (read) 7 (freeze) 8 (freeze noack) 9 (freeze clear) 10 (frz. cl. noack) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
21	0	Frozen Counter (Variation 0 is used to request default variation)	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)		
	1	32-Bit Frozen Counter	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	2	16-Bit Frozen Counter	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	9	32-Bit Frozen Counter without Flag	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	10	16-Bit Frozen Counter without Flag	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
22	0	Counter Change Event (Variation 0 is used to request default variation)	1 (read)	06 (no range, or all) 07, 08 (limited quantity)		
	1	32-Bit Counter Change Event	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	2	16-Bit Counter Change Event	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	5	32-Bit Counter Change Event with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	6	16-Bit Counter Change Event with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
23	0	Frozen Counter Event (Variation 0 is used to request default variation)	1 (read)	06 (no range, or all) 07, 08 (limited quantity)		
	1	32-Bit Frozen Counter Event	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	2	16-Bit Frozen Counter Event	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)

Note 1: A default variation refers to the variation responded when variation 0 is requested and/or in class 0, 1, 2, or 3 scans. The default variations for object types 1, 2, 20, 21, 22, 23, 30, and 32 are selected via relay settings. Refer to the *Communications* section in Chapter 5 for details. This optimizes the class 0 poll data size.

Note 2: For static (non-change-event) objects, qualifiers 17 or 28 are only responded when a request is sent with qualifiers 17 or 28, respectively. Otherwise, static object requests sent with qualifiers 00, 01, 06, 07, or 08, will be responded with qualifiers 00 or 01 (for change-event objects, qualifiers 17 or 28 are always responded.)

Note 3: Cold restarts are implemented the same as warm restarts – the N60 is not restarted, but the DNP process is restarted.

Table E–2: IMPLEMENTATION TABLE (Sheet 3 of 4)

OBJECT			REQUEST		RESPONSE	
OBJECT NO.	VARIATION NO.	DESCRIPTION	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)
23 cont'd	5	32-Bit Frozen Counter Event with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	6	16-Bit Frozen Counter Event with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
30	0	Analog Input (Variation 0 is used to request default variation)	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)		
	1	32-Bit Analog Input	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	2	16-Bit Analog Input	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	3	32-Bit Analog Input without Flag	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	4	16-Bit Analog Input without Flag	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
	5	short floating point	1 (read) 22 (assign class)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
32	0	Analog Change Event (Variation 0 is used to request default variation)	1 (read)	06 (no range, or all) 07, 08 (limited quantity)		
	1	32-Bit Analog Change Event without Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	2	16-Bit Analog Change Event without Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	3	32-Bit Analog Change Event with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	4	16-Bit Analog Change Event with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	5	short floating point Analog Change Event without Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
	7	short floating point Analog Change Event with Time	1 (read)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	17, 28 (index)
34	0	Analog Input Reporting Deadband (Variation 0 is used to request default variation)	1 (read)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)		
	1	16-bit Analog Input Reporting Deadband (default – see Note 1)	1 (read)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
			2 (write)	00, 01 (start-stop) 07, 08 (limited quantity) 17, 28 (index)		

Note 1: A default variation refers to the variation responded when variation 0 is requested and/or in class 0, 1, 2, or 3 scans. The default variations for object types 1, 2, 20, 21, 22, 23, 30, and 32 are selected via relay settings. Refer to the *Communications* section in Chapter 5 for details. This optimizes the class 0 poll data size.

Note 2: For static (non-change-event) objects, qualifiers 17 or 28 are only responded when a request is sent with qualifiers 17 or 28, respectively. Otherwise, static object requests sent with qualifiers 00, 01, 06, 07, or 08, will be responded with qualifiers 00 or 01 (for change-event objects, qualifiers 17 or 28 are always responded.)

Note 3: Cold restarts are implemented the same as warm restarts – the N60 is not restarted, but the DNP process is restarted.

Table E–2: IMPLEMENTATION TABLE (Sheet 4 of 4)

OBJECT			REQUEST		RESPONSE	
OBJECT NO.	VARIATION NO.	DESCRIPTION	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)	FUNCTION CODES (DEC)	QUALIFIER CODES (HEX)
34 cont'd	2	32-bit Analog Input Reporting Deadband	1 (read)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
			2 (write)	00, 01 (start-stop) 07, 08 (limited quantity) 17, 28 (index)		
	3	Short floating point Analog Input Reporting Deadband	1 (read)	00, 01 (start-stop) 06 (no range, or all) 07, 08 (limited quantity) 17, 28 (index)	129 (response)	00, 01 (start-stop) 17, 28 (index) (see Note 2)
50	1	Time and Date (default – see Note 1)	1 (read)	00, 01 (start-stop)	129 (response)	00, 01 (start-stop)
			2 (write)	06 (no range, or all) 07 (limited qty=1) 08 (limited quantity) 17, 28 (index)		17, 28 (index) (see Note 2)
52	2	Time Delay Fine			129 (response)	07 (limited quantity) (quantity = 1)
60	0	Class 0, 1, 2, and 3 Data	1 (read) 20 (enable unsol) 21 (disable unsol) 22 (assign class)	06 (no range, or all)		
	1	Class 0 Data	1 (read) 22 (assign class)	06 (no range, or all)		
	2	Class 1 Data	1 (read)	06 (no range, or all)		
	3	Class 2 Data	20 (enable unsol)	07, 08 (limited quantity)		
	4	Class 3 Data	21 (disable unsol) 22 (assign class)			
70	1	File identifier	2 (write)	1b (free format)	129 (response)	1b (free format)
	3	File command	25 (open) 27 (delete)	5b (free format)		
	4	File command status	1 (read) 22 (assign class) 26 (close) 30 (abort)	06 (no range, or all) 07, 08 (limited quantity) 5b (free format)	129 (response) 130 (unsol. resp.)	5b (free format)
	5	File transfer	1 (read) 2 (write) 22 (assign class)	06 (no range, or all) 07, 08 (limited quantity) 5b (free format)	129 (response) 130 (unsol. resp.)	5b (free format)
	6	File transfer status	1 (read) 22 (assign class)	06 (no range, or all) 07, 08 (limited quantity)	129 (response) 130 (unsol. resp.)	5b (free format)
	7	File descriptor	1 (read) 22 (assign class) 28 (get file info.)	06 (no range, or all) 07, 08 (limited quantity) 5b (free format)	129 (response) 130 (unsol. resp.)	5b (free format)
80	1	Internal Indications	2 (write)	00 (start-stop) (index must =7)		
---		No Object (function code only) see Note 3	13 (cold restart)			
---		No Object (function code only)	14 (warm restart)			
---		No Object (function code only)	23 (delay meas.)			

Note 1: A default variation refers to the variation responded when variation 0 is requested and/or in class 0, 1, 2, or 3 scans. The default variations for object types 1, 2, 20, 21, 22, 23, 30, and 32 are selected via relay settings. Refer to the *Communications* section in Chapter 5 for details. This optimizes the class 0 poll data size.

Note 2: For static (non-change-event) objects, qualifiers 17 or 28 are only responded when a request is sent with qualifiers 17 or 28, respectively. Otherwise, static object requests sent with qualifiers 00, 01, 06, 07, or 08, will be responded with qualifiers 00 or 01 (for change-event objects, qualifiers 17 or 28 are always responded.)

Note 3: Cold restarts are implemented the same as warm restarts – the N60 is not restarted, but the DNP process is restarted.

E.2.1 BINARY INPUT POINTS

The DNP binary input data points are configured through the **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **DNP / IEC104 POINT LISTS** ⇒ **BINARY INPUT / MSP POINTS** menu. Refer to the *Communications* section of Chapter 5 for additional details. When a freeze function is performed on a binary counter point, the frozen value is available in the corresponding frozen counter point.

BINARY INPUT POINTS

Static (Steady-State) Object Number: **1**

Change Event Object Number: **2**

Request Function Codes supported: **1 (read), 22 (assign class)**

Static Variation reported when variation 0 requested: **2 (Binary Input with status)**

Change Event Variation reported when variation 0 requested: **2 (Binary Input Change with Time)**

Change Event Scan Rate: **8 times per power system cycle**

Change Event Buffer Size: **1000**

E.2.2 BINARY AND CONTROL RELAY OUTPUT

Supported Control Relay Output Block fields: Pulse On, Pulse Off, Latch On, Latch Off, Paired Trip, Paired Close.

BINARY OUTPUT STATUS POINTS

Object Number: **10**

Request Function Codes supported: **1 (read)**

Default Variation reported when Variation 0 requested: **2 (Binary Output Status)**

CONTROL RELAY OUTPUT BLOCKS

Object Number: **12**

Request Function Codes supported: **3 (select), 4 (operate), 5 (direct operate), 6 (direct operate, noack)**

Table E-3: BINARY/CONTROL OUTPUTS

POINT	NAME/DESCRIPTION
0	Virtual Input 1
1	Virtual Input 2
2	Virtual Input 3
3	Virtual Input 4
4	Virtual Input 5
5	Virtual Input 6
6	Virtual Input 7
7	Virtual Input 8
8	Virtual Input 9
9	Virtual Input 10
10	Virtual Input 11
11	Virtual Input 12
12	Virtual Input 13
13	Virtual Input 14
14	Virtual Input 15
15	Virtual Input 16
16	Virtual Input 17
17	Virtual Input 18
18	Virtual Input 19
19	Virtual Input 20
20	Virtual Input 21
21	Virtual Input 22
22	Virtual Input 23
23	Virtual Input 24
24	Virtual Input 25
25	Virtual Input 26
26	Virtual Input 27
27	Virtual Input 28
28	Virtual Input 29
29	Virtual Input 30
30	Virtual Input 31
31	Virtual Input 32

Table E-3: BINARY/CONTROL OUTPUTS

POINT	NAME/DESCRIPTION
32	Virtual Input 33
33	Virtual Input 34
34	Virtual Input 35
35	Virtual Input 36
36	Virtual Input 37
37	Virtual Input 38
38	Virtual Input 39
39	Virtual Input 40
40	Virtual Input 41
41	Virtual Input 42
42	Virtual Input 43
43	Virtual Input 44
44	Virtual Input 45
45	Virtual Input 46
46	Virtual Input 47
47	Virtual Input 48
48	Virtual Input 49
49	Virtual Input 50
50	Virtual Input 51
51	Virtual Input 52
52	Virtual Input 53
53	Virtual Input 54
54	Virtual Input 55
55	Virtual Input 56
56	Virtual Input 57
57	Virtual Input 58
58	Virtual Input 59
59	Virtual Input 60
60	Virtual Input 61
61	Virtual Input 62
62	Virtual Input 63
63	Virtual Input 64

E.2.3 COUNTERS

The following table lists both Binary Counters (Object 20) and Frozen Counters (Object 21). When a freeze function is performed on a Binary Counter point, the frozen value is available in the corresponding Frozen Counter point.

BINARY COUNTERS

Static (Steady-State) Object Number: **20**

Change Event Object Number: **22**

Request Function Codes supported: **1 (read), 7 (freeze), 8 (freeze noack), 9 (freeze and clear), 10 (freeze and clear, noack), 22 (assign class)**

Static Variation reported when variation 0 requested: **1 (32-Bit Binary Counter with Flag)**

Change Event Variation reported when variation 0 requested: **1 (32-Bit Counter Change Event without time)**

Change Event Buffer Size: **10**

Default Class for all points: **2**

FROZEN COUNTERS

Static (Steady-State) Object Number: **21**

Change Event Object Number: **23**

Request Function Codes supported: **1 (read)**

Static Variation reported when variation 0 requested: **1 (32-Bit Frozen Counter with Flag)**

Change Event Variation reported when variation 0 requested: **1 (32-Bit Frozen Counter Event without time)**

Change Event Buffer Size: **10**

Default Class for all points: **2**

Table E-4: BINARY AND FROZEN COUNTERS

POINT INDEX	NAME/DESCRIPTION
0	Digital Counter 1
1	Digital Counter 2
2	Digital Counter 3
3	Digital Counter 4
4	Digital Counter 5
5	Digital Counter 6
6	Digital Counter 7
7	Digital Counter 8
8	Oscillography Trigger Count
9	Events Since Last Clear

A counter freeze command has no meaning for counters 8 and 9. N60 Digital Counter values are represented as 32-bit integers. The DNP 3.0 protocol defines counters to be unsigned integers. Care should be taken when interpreting negative counter values.

E.2.4 ANALOG INPUTS

The DNP analog input data points are configured through the **PRODUCT SETUP** ⇒ **COMMUNICATIONS** ⇒ **DNP / IEC104 POINT LISTS** ⇒ **ANALOG INPUT / MME POINTS** menu. Refer to the *Communications* section of Chapter 5 for additional details.

It is important to note that 16-bit and 32-bit variations of analog inputs are transmitted through DNP as signed numbers. Even for analog input points that are not valid as negative values, the maximum positive representation is 32767 for 16-bit values and 2147483647 for 32-bit values. This is a DNP requirement.

The deadbands for all Analog Input points are in the same units as the Analog Input quantity. For example, an Analog Input quantity measured in volts has a corresponding deadband in units of volts. This is in conformance with DNP Technical Bulletin 9809-001: Analog Input Reporting Deadband. Relay settings are available to set default deadband values according to data type. Deadbands for individual Analog Input Points can be set using DNP Object 34.

Static (Steady-State) Object Number: **30**

Change Event Object Number: **32**

Request Function Codes supported: **1 (read), 2 (write, deadbands only), 22 (assign class)**

Static Variation reported when variation 0 requested: **1 (32-Bit Analog Input)**

Change Event Variation reported when variation 0 requested: **1 (Analog Change Event without Time)**

Change Event Scan Rate: defaults to **500 ms**

Change Event Buffer Size: **800**

Default Class for all Points: **1**

F.1.1 REVISION HISTORY

MANUAL P/N	REVISION	RELEASE DATE	ECO
1601-0125-F1	3.4x	10 December 2003	URX-111
1601-0125-F2	3.4x	09 February 2004	URX-115
1601-0125-G1	4.0x	23 March 2004	URX-123
1601-0125-G2	4.0x	17 May 2004	URX-136
1601-0125-H1	4.2x	30 June 2004	URX-145
1601-0125-H2	4.2x	23 July 2004	URX-151
1601-0125-J1	4.4x	15 September 2004	URX-156
1601-0125-K1	4.6x	15 February 2005	URX-176
1601-0125-L1	4.8x	05 August 2005	URX-202
1601-0125-M1	4.9x	15 December 2005	URX-208
1601-0125-M2	4.9x	27 February 2006	URX-214

F.1.2 CHANGES TO THE MANUAL

Table F-1: MAJOR UPDATES FOR N60 MANUAL REVISION M2

PAGE (M1)	PAGE (M2)	CHANGE	DESCRIPTION
Title	Title	Update	Manual part number to 1601-0125-M2
3-26	3-26	Update	Updated RS422 INTERFACE section
4-12	4-12	Update	Updated INVALID PASSWORD ENTRY sub-section
5-8	5-8	Update	Updated PASSWORD SECURITY section
5-63	5-63	Update	Updated PMU RECORDING sub-section
5-125	5-125	Update	Updated DIGITIZERS section
5-128	5-128	Update	Updated EIGHT-BIT COMPARATORS section
7-3	7-3	Update	Updated PHASOR MEASUREMENT UNIT ONE-SHOT section
A-1	A-1	Update	Updated FLEXANALOG DATA ITEMS table

Table F-2: MAJOR UPDATES FOR N60 MANUAL REVISION M1 (Sheet 1 of 2)

PAGE (L1)	PAGE (M1)	CHANGE	DESCRIPTION
Title	Title	Update	Manual part number to 1601-0125-M1
Title	Title	Update	Changed name of relay to N60 Network Stability and Synchrophasor Measurement System
2-2	2-3	Update	Updated ORDERING section
3-1	3-1	Update	Updated PANEL CUTOUT section
4-4	4-4	Update	Updated FACEPLATE section
5-17	5-18	Update	Updated IEC 61850 PROTOCOL sub-section

Table F–2: MAJOR UPDATES FOR N60 MANUAL REVISION M1 (Sheet 2 of 2)

PAGE (L1)	PAGE (M1)	CHANGE	DESCRIPTION
---	5-51	Add	Added PHASE MEASUREMENT UNIT section
5-52	5-70	Update	Updated FLEXLOGIC OPERANDS table
---	5-156	Add	Added PHASE MEASUREMENT UNIT TEST VALUES section
---	6-16	Add	Added PHASE MEASUREMENT UNIT section
---	7-3	Add	Added PHASE MEASUREMENT UNIT ONE-SHOT section
B-8	B-8	Update	Updated MODBUS MEMORY MAP for 4.9x release

Table F–3: MAJOR UPDATES FOR N60 MANUAL REVISION L1

PAGE (K1)	PAGE (L1)	CHANGE	DESCRIPTION
Title	Title	Update	Manual part number to 1601-0125-L1
2-3	2-3	Update	Updated N60 ORDER CODES table
2-4	2-4	Update	Updated ORDER CODES FOR REPLACEMENT MODULES table
2-8	2-8	Update	Updated INPUTS specifications section
2-10	2-10	Update	Updated COMMUNICATIONS specifications section
3-7	3-7	Update	Updated CONTROL POWER section
3-10	3-10	Update	Updated CONTACT INPUTS/OUTPUTS section
3-18	3-18	Update	Updated CPU COMMUNICATIONS PORTS section
3-19	3-20	Update	Updated RS485 SERIAL CONNECTION diagram
3-24	3-25	Update	Updated G.703 INTERFACE section
3-29	3-30	Update	Updated RS422 AND FIBER INTERFACE CONNECTION drawing
---	3-33	Add	Added C37.94SM INTERFACE section
---	4-13	Add	Added INVALID PASSWORD ENTRY section
5-14	5-14	Update	Updated DNP PROTOCOL sub-section
---	5-16	Add	Added DNP / IEC 60870-5-104 POINT LISTS sub-section
5-16	5-17	Update	Updated IEC 61850 PROTOCOL sub-section
5-19	5-20	Update	Updated IEC 60870-5-104 PROTOCOL sub-section
5-23	5-24	Update	Updated DATA LOGGER section
5-47	5-49	Update	Updated DUAL BREAKER CONTROL LOGIC diagram to 827061AN
5-50	5-52	Update	Updated FLEXLOGIC™ OPERANDS table
5-84	5-86	Update	Updated SETTING GROUPS section
5-85	5-87	Update	Updated SELECTOR SWITCH section
5-101	5-104	Update	Updated VT FUSE FAILURE sub-section
5-122	5-125	Update	Updated REMOTE INPUTS section
5-124	5-127	Update	Updated DIRECT INPUTS/OUTPUTS section
B-8	B-8	Update	Updated MODBUS MEMORY MAP for release 4.8x
D-9	D-9	Update	Updated IEC 60870-5-104 POINT LIST sub-section
E-8	E-8	Update	Updated BINARY INPUT POINTS section
E-14	E-9	Update	Updated BINARY AND CONTROL RELAY OUTPUT POINTS section
E-16	E-11	Update	Updated ANALOG INPUTS section

Table F–4: MAJOR UPDATES FOR N60 MANUAL REVISION K1

PAGE (J1)	PAGE (K1)	CHANGE	DESCRIPTION
Title	Title	Update	Manual part number to 1601-0125-K1
2-3	2-3	Update	Updated N60 ORDER CODES table
3-6	3-6	Update	Updated TYPICAL WIRING DIAGRAM to 847702A3
3-19	3-19	Update	Updated RS485 SERIAL CONNECTION diagram to 827757A7
5-9	5-9	Update	Updated DISPLAY PROPERTIES section
5-14	5-14	Update	Updated DNP PROTOCOL sub-section
5-16	5-16	Update	Updated IEC 61850 PROTOCOL sub-section
5-95	5-96	Update	Updated DIGITAL ELEMENTS section
5-117	5-118	Update	The LATCHING OUTPUTS section is now a sub-section of the CONTACT OUTPUTS
5-120	5-121	Update	Updated REMOTE DEVICES section
5-122	5-123	Update	Updated REMOTE OUTPUTS section
B-8	B-8	Update	Updated MODBUS MEMORY MAP for revision 4.6x
---	C-1	Add	Added IEC 61850 COMMUNICATIONS appendix
D-4	E-4	Update	Updated DNP IMPLEMENTATION section

Table F–5: MAJOR UPDATES FOR N60 MANUAL REVISION J1

PAGE (H2)	PAGE (J1)	CHANGE	DESCRIPTION
Title	Title	Update	Manual part number to 1601-0125-J1
5-16	---	Remove	Removed UCA/MMS PROTOCOL sub-section
---	5-16	Add	Added IEC 61850 PROTOCOL sub-section
5-43	5-43	Update	Updated BREAKERS section
5-115	5-115	Update	Updated VIRTUAL INPUTS section
B-8	B-8	Update	Updated MODBUS MEMORY MAP for revision 4.4x
C-1	---	Remove	Removed UCA/MMS COMMUNICATIONS appendix

Table F–6: MAJOR UPDATES FOR N60 MANUAL REVISION H2

PAGE (H1)	PAGE (H2)	CHANGE	DESCRIPTION
Title	Title	Update	Manual part number to 1601-0125-H2
3-22	3-22	Update	Updated CHANNEL COMMUNICATION OPTIONS table

F.2.1 STANDARD ABBREVIATIONS

A.....	Ampere	FREQ.....	Frequency
AC.....	Alternating Current	FSK.....	Frequency-Shift Keying
A/D.....	Analog to Digital	FTP.....	File Transfer Protocol
AE.....	Accidental Energization, Application Entity	FxE.....	FlexElement™
AMP.....	Ampere	FWD.....	Forward
ANG.....	Angle		
ANSI.....	American National Standards Institute	G.....	Generator
AR.....	Automatic Reclosure	GE.....	General Electric
ASDU.....	Application-layer Service Data Unit	GND.....	Ground
ASYM.....	Asymmetry	GNTR.....	Generator
AUTO.....	Automatic	GOOSE.....	General Object Oriented Substation Event
AUX.....	Auxiliary	GPS.....	Global Positioning System
AVG.....	Average		
		HARM.....	Harmonic / Harmonics
BER.....	Bit Error Rate	HCT.....	High Current Time
BF.....	Breaker Fail	HGF.....	High-Impedance Ground Fault (CT)
BFI.....	Breaker Failure Initiate	HIZ.....	High-Impedance and Arcing Ground
BKR.....	Breaker	HMI.....	Human-Machine Interface
BLK.....	Block	HTTP.....	Hyper Text Transfer Protocol
BLKG.....	Blocking	HYB.....	Hybrid
BPNT.....	Breakpoint of a characteristic		
BRKR.....	Breaker	I.....	Instantaneous
		I ₀	Zero Sequence current
CAP.....	Capacitor	I ₁	Positive Sequence current
CC.....	Coupling Capacitor	I ₂	Negative Sequence current
CCVT.....	Coupling Capacitor Voltage Transformer	IA.....	Phase A current
CFG.....	Configure / Configurable	IAB.....	Phase A minus B current
.CFG.....	Filename extension for oscillography files	IB.....	Phase B current
CHK.....	Check	IBC.....	Phase B minus C current
CHNL.....	Channel	IC.....	Phase C current
CLS.....	Close	ICA.....	Phase C minus A current
CLSD.....	Closed	ID.....	Identification
CMND.....	Command	IED.....	Intelligent Electronic Device
CMPSN.....	Comparison	IEC.....	International Electrotechnical Commission
CO.....	Contact Output	IEEE.....	Institute of Electrical and Electronic Engineers
COM.....	Communication	IG.....	Ground (not residual) current
COMM.....	Communications	Igd.....	Differential Ground current
COMP.....	Compensated, Comparison	IN.....	CT Residual Current (3Io) or Input
CONN.....	Connection	INC SEQ.....	Incomplete Sequence
CONT.....	Continuous, Contact	INIT.....	Initiate
CO-ORD.....	Coordination	INST.....	Instantaneous
CPU.....	Central Processing Unit	INV.....	Inverse
CRC.....	Cyclic Redundancy Code	I/O.....	Input/Output
CRT, CRNT.....	Current	IOC.....	Instantaneous Overcurrent
CSA.....	Canadian Standards Association	IOV.....	Instantaneous Overvoltage
CT.....	Current Transformer	IRIG.....	Inter-Range Instrumentation Group
CVT.....	Capacitive Voltage Transformer	ISO.....	International Standards Organization
		IUV.....	Instantaneous Undervoltage
D/A.....	Digital to Analog		
DC (dc).....	Direct Current	K0.....	Zero Sequence Current Compensation
DD.....	Disturbance Detector	kA.....	kiloAmpere
DFLT.....	Default	kV.....	kiloVolt
DGNST.....	Diagnostics		
DI.....	Digital Input	LED.....	Light Emitting Diode
DIFF.....	Differential	LEO.....	Line End Open
DIR.....	Directional	LFT BLD.....	Left Blinder
DISCREP.....	Discrepancy	LOOP.....	Loopback
DIST.....	Distance	LPU.....	Line Pickup
DMD.....	Demand	LRA.....	Locked-Rotor Current
DNP.....	Distributed Network Protocol	LTC.....	Load Tap-Changer
DPO.....	Dropout		
DSP.....	Digital Signal Processor	M.....	Machine
dt.....	Rate of Change	mA.....	MilliAmpere
DTT.....	Direct Transfer Trip	MAG.....	Magnitude
DUTT.....	Direct Under-reaching Transfer Trip	MAN.....	Manual / Manually
		MAX.....	Maximum
ENCRMNT.....	Encroachment	MIC.....	Model Implementation Conformance
EPRI.....	Electric Power Research Institute	MIN.....	Minimum, Minutes
.EVT.....	Filename extension for event recorder files	MMI.....	Man Machine Interface
EXT.....	Extension, External	MMS.....	Manufacturing Message Specification
		MRT.....	Minimum Response Time
F.....	Field	MSG.....	Message
FAIL.....	Failure	MTA.....	Maximum Torque Angle
FD.....	Fault Detector	MTR.....	Motor
FDH.....	Fault Detector high-set	MVA.....	MegaVolt-Ampere (total 3-phase)
FDL.....	Fault Detector low-set	MVA_A.....	MegaVolt-Ampere (phase A)
FLA.....	Full Load Current	MVA_B.....	MegaVolt-Ampere (phase B)
FO.....	Fiber Optic	MVA_C.....	MegaVolt-Ampere (phase C)

MVAR	MegaVar (total 3-phase)	SAT	CT Saturation
MVAR_A	MegaVar (phase A)	SBO	Select Before Operate
MVAR_B	MegaVar (phase B)	SCADA	Supervisory Control and Data Acquisition
MVAR_C	MegaVar (phase C)	SEC	Secondary
MVARH	MegaVar-Hour	SEL	Select / Selector / Selection
MW	MegaWatt (total 3-phase)	SENS	Sensitive
MW_A	MegaWatt (phase A)	SEQ	Sequence
MW_B	MegaWatt (phase B)	SIR	Source Impedance Ratio
MW_C	MegaWatt (phase C)	SNTP	Simple Network Time Protocol
MWH	MegaWatt-Hour	SRC	Source
N	Neutral	SSB	Single Side Band
N/A, n/a	Not Applicable	SSEL	Session Selector
NEG	Negative	STATS	Statistics
NMPLT	Nameplate	SUPN	Supervision
NOM	Nominal	SUPV	Supervise / Supervision
NSAP	Network Service Access Protocol	SV	Supervision, Service
NTR	Neutral	SYNC	Synchrocheck
O	Over	SYNCHCHK	Synchrocheck
OC, O/C	Overcurrent	T	Time, transformer
O/P, Op	Output	TC	Thermal Capacity
OP	Operate	TCP	Transmission Control Protocol
OPER	Operate	TCU	Thermal Capacity Used
OPERATG	Operating	TD MULT	Time Dial Multiplier
O/S	Operating System	TEMP	Temperature
OSI	Open Systems Interconnect	TFTP	Trivial File Transfer Protocol
OSB	Out-of-Step Blocking	THD	Total Harmonic Distortion
OUT	Output	TMR	Timer
OV	Overvoltage	TOC	Time Overcurrent
OVERFREQ	Overfrequency	TOV	Time Overvoltage
OVLDT	Overload	TRANS	Transient
P	Phase	TRANSF	Transfer
PC	Phase Comparison, Personal Computer	TSEL	Transport Selector
PCNT	Percent	TUC	Time Undercurrent
PF	Power Factor (total 3-phase)	TUV	Time Undervoltage
PF_A	Power Factor (phase A)	TX (Tx)	Transmit, Transmitter
PF_B	Power Factor (phase B)	U	Under
PF_C	Power Factor (phase C)	UC	Undercurrent
PFL	Phase and Frequency Lock Loop	UCA	Utility Communications Architecture
PHS	Phase	UDP	User Datagram Protocol
PICS	Protocol Implementation & Conformance Statement	UL	Underwriters Laboratories
PKP	Pickup	UNBAL	Unbalance
PLC	Power Line Carrier	UR	Universal Relay
POS	Positive	URC	Universal Recloser Control
POTT	Permissive Over-reaching Transfer Trip	.URS	Filename extension for settings files
PRESS	Pressure	UV	Undervoltage
PRI	Primary	V/Hz	Volts per Hertz
PROT	Protection	V_0	Zero Sequence voltage
PSEL	Presentation Selector	V_1	Positive Sequence voltage
pu	Per Unit	V_2	Negative Sequence voltage
PUIB	Pickup Current Block	VA	Phase A voltage
PUIT	Pickup Current Trip	VAB	Phase A to B voltage
PUSHBTN	Pushbutton	VAG	Phase A to Ground voltage
PUTT	Permissive Under-reaching Transfer Trip	VARH	Var-hour voltage
PWM	Pulse Width Modulated	VB	Phase B voltage
PWR	Power	VBA	Phase B to A voltage
QUAD	Quadrilateral	VBG	Phase B to Ground voltage
R	Rate, Reverse	VC	Phase C voltage
RCA	Reach Characteristic Angle	VCA	Phase C to A voltage
REF	Reference	VCG	Phase C to Ground voltage
REM	Remote	VF	Variable Frequency
REV	Reverse	VIBR	Vibration
RI	Reclose Initiate	VT	Voltage Transformer
RIP	Reclose In Progress	VTFF	Voltage Transformer Fuse Failure
RGT BLD	Right Blinder	VTLOS	Voltage Transformer Loss Of Signal
ROD	Remote Open Detector	WDG	Winding
RST	Reset	WH	Watt-hour
RSTR	Restrained	w/ opt	With Option
RTD	Resistance Temperature Detector	WRT	With Respect To
RTU	Remote Terminal Unit	X	Reactance
RX (Rx)	Receive, Receiver	XDUCER	Transducer
s	second	XFMR	Transformer
S	Sensitive	Z	Impedance, Zone

GE MULTILIN RELAY WARRANTY

General Electric Multilin Inc. (GE Multilin) warrants each relay it manufactures to be free from defects in material and workmanship under normal use and service for a period of 24 months from date of shipment from factory.

In the event of a failure covered by warranty, GE Multilin will undertake to repair or replace the relay providing the warrantor determined that it is defective and it is returned with all transportation charges prepaid to an authorized service centre or the factory. Repairs or replacement under warranty will be made without charge.

Warranty shall not apply to any relay which has been subject to misuse, negligence, accident, incorrect installation or use not in accordance with instructions nor any unit that has been altered outside a GE Multilin authorized factory outlet.

GE Multilin is not liable for special, indirect or consequential damages or for loss of profit or for expenses sustained as a result of a relay malfunction, incorrect application or adjustment.

For complete text of Warranty (including limitations and disclaimers), refer to GE Multilin Standard Conditions of Sale.

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