



F650 ADDENDUM TO 2.20 FIRMWARE VERSION

8. BOOTCODE AND FIRMWARE UPDATE

FIRMWARE AND BOOT CODE VERSIONS COMPATIBILITY	
FIRMWARE VERSION	BOOT CODE VERSION
2.20	4.00

13.7.1. F650 FIRMWARE REVISION HISTORY

FIRMWARE VERSION	BOOT CODE VERSION	CHANGES DESCRIPTION	MODELS	DATE
2.20	4.00	<p>USER INTERFACE</p> <p>ENERVISTA F650 SETUP</p> <ul style="list-style-type: none"> Data Logger visualization changes. File name available in "Config file converter" tool. <p>COMMUNICATIONS</p> <ul style="list-style-type: none"> DNP Scale factor correction. IRIGB synchronization: <ul style="list-style-type: none"> § Date and time synchronization available. § 980 ms delay correction. <p>INPUTS/OUTPUTS</p> <ul style="list-style-type: none"> Inputs Activation/Deactivation correction. Voltage threshold setting range from 20 to 230 Vdc. Debounce time settings: <ul style="list-style-type: none"> § Timestamp before debounce time § Setting range from 1 to 50 ms. <p>METERING</p> <ul style="list-style-type: none"> Power calculations using ARON method. New measurements provided when using Delta connection and VN as auxiliary voltage input: <ul style="list-style-type: none"> § Neutral voltage available. § Phase to ground voltages available. § Single phase power measurements available. <p>PROTECTION</p> <ul style="list-style-type: none"> Neutral, ground and sensitive ground directional units available with delta connection and VN as auxiliary voltage input. Minor changes in Forward Power elements. <p>REGISTER</p> <ul style="list-style-type: none"> Ground current available in Fault report. Corrected 150 ms delay in Snapshot events measurements. 	All models, excepting: Options 4 and 5 for inputs outputs boards. Option 6 for IEC61850 protocol.	November 25 TH 2005