

capacitor switching for power circuit breakers

technical

33-063

data

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isolated bank • paralell banks

general information

With the increase in number and size of capacitor banks used for voltage and power factor correction, there has been a corresponding increase in the demand for apparatus that will handle the switching duty. Considerable research development and testing have been done to determine the ability of standard power circuit breakers to handle this duty, and when necessary, the best modifications of these breakers to meet the requirements. This bulletin summarizes and interprets the results with respect to practical applications of the circuit breakers listed on page 4.

The type CA compressed air breakers are designed to have a maximum interrupting action under all conditions of current. They are very effective on capacitor switching and can operate essentially restrike free for most ratings.

The sulfur-hexafluoride (SF6) breakers from 34.5 kv to 230 kv and higher are essentially restrike free and standard breakers can be applied to capacitor switching. The extremely high di-

electric strength of the SF_6 gas enables short contact gaps to withstand high recovery voltage. If a restrike should occur, the gas absorbs the explosive effect of the energy released by the current surge and practically eliminates the possibility of any damage to the interrupter.

The type DH air breakers use magnetic blowout for arc interruption, and standard breakers can be applied to capacitor switching. They will not operate restrike free, but extensive tests indicate that the resulting transient voltages seldom exceed two times normal.

On the lower voltages and smaller banks the standard oil circuit breakers can be used, but on larger banks, higher voltages, and where parallel banks are being switched some modification of the oil circuit breaker is frequently necessary, or desirable to obtain the most satisfactory operation.

isolated bank

The switching of an isolated bank of capacitors on and off the line, while not particularly hard on the breaker, can produce serious transient overvoltages on the system. This overvoltage can result from the arc restriking during de-energizing operations. A clearer understanding may be obtained by examining the voltage and current conditions existing prior to and during the switching operations.

In figures 1-a and 1-b the top trace represents the system voltage, the second trace capacitor bank voltage, the third trace current through the breaker (bank current) and the fourth trace recovery voltage which appears across the breaker contacts in the situation where a large bank is being switched off the line. In figure 1-a the breaker contacts part at time To. Interruption will usually occur at the first current zero, time T_{1} , since very little recovery voltage (difference between bank and system voltage) appears across the breaker contacts. Since the current was leading the voltage by nearly 90° prior to interruption, the capacitor bank, on interruption of the current flow, is left charged at practically full line voltage. With no restrike, the change in system voltage is limited to a small transient oscillation which decreases to the slightly lower steady state level as dictated by the change in system reactance on removal of the capacitor bank from the system. The duty on a breaker under these conditions is extremely light. However, as shown in figure 1-b at time T_1 the capacitor bank is charged to practically full line voltage while the system voltage moves away from it thus increasing the recovery voltage across the breaker contacts. Since in this case interruption occurred early in the stroke, little contact separation exists, and it is quite likely that the gap will break down under the increasing voltage stress (in figure 1-b, see time T2). When this occurs, the capacitor voltage immediately attempts to equalize with the system voltage, but the circuit is oscillatory and at the first peak of the transient the capacitor voltage has overshot by an amount nearly equal to the difference between the two voltages immediately prior to the time of the restrike. In this case figure 1-b shows the

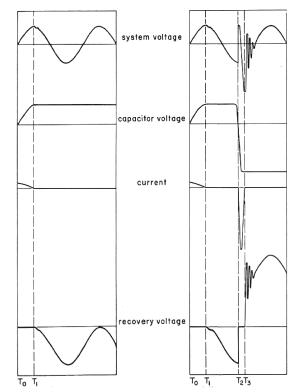


fig. 1-a: no restrike

fig. 1-b: restrike

Opening the circuit to a single phase capacitor in one step. On the left an opening without restrike. On the right, the maximum effect possible with one restrike.



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isolated bank

continued

current interrupted at its first high frequency current zero, time T₃ and the overshoot peak voltage is trapped on the capacitor bank.

The resulting recovery voltage reaches a value greater than that following the first interruption, but in this particular case there is no additional restrike because the contacts have moved further apart and the build up of dielectric strength exceeds the build up of recovery voltage.

If the gap breaks down less than a quarter cycle after a current zero, the amplitude of the voltage oscillation will not exceed the crest voltage, and no overvoltage is caused. This is defined as a re-ignition rather than a restrike.

In figure 1-b the restrike is shown to occur a full half cycle after current interruption. This is the worst possible condition for the first restrike because the recovery voltage has reached its maximum, and the resultant surge voltage can theoretically reach three times normal line-to-ground crest voltage. In actual practice it seldom exceeds two and one-half times normal. This should not be damaging to the system; however, additional restrikes can produce higher crest voltages and the sudden voltage changes and high frequency oscillations may produce other relatively higher voltages elsewhere on the system. Therefore, it is desirable to limit restrikes or the voltage phenomenon resulting from them to protect the entire system.

parallel banks

On breaker switching of a capacitor bank, the magnitude of voltage disturbances on the system is greatly reduced by the presence of one or more additional banks of comparable size close to the breaker and energized by the system. But, during a closing operation, or during a restrike, the transient inrush current through the breaker between the energized banks and the one being switched can be very large. With standard breakers these currents are oscillatory at very high frequencies. The peak current may exceed 100 times the normal peak of the capacitor bank current. Such a high instantaneous current will suddenly release a relatively large amount of energy which, particularly as an arc current in oil circuit breakers, can produce explosive like forces and possibly damage the interrupters.

Since the severity of parallel bank switching is caused by the very sudden high current which initially is limited only by the very low resistance and inductance of the usual circuit between the banks, it can be reduced by the addition of inductance to the circuit. An inductance which will have a 60 cycle reactance as little as $\frac{1}{2}$ to $\frac{1}{6}$ of the 60 cycle capacitive reactance of the banks and placed in series with them will greatly reduce both the rate of rise and the peak value of the inrush current which in turn greatly reduces the severity of the breaker duty.

modifications to oil breakers

. . . for capacitor switching

A circuit breaker which can be applied to either isolated banks or parallel banks must have two particular design features. First it must eliminate the harmful effects of the current surges to the breaker itself and secondly it must limit overvoltages on the system.

On the small outdoor oil breakers, 250 mva and below, and indoor oil breakers below 500 mva, because of the lower voltages involved, restrikes are infrequent and overvoltages are neither great nor much of a problem. By making the grids sufficiently strong, satisfactory results are obtained with these breakers.

In oil breakers for 115 kv and higher, extra strong multi-flow grids with pumps and shock-absorbing gas chambers near the arc spaces are used. The pumps make the grids essentially restrike free. The extra strong grids withstand the physical shock during those rare occasions when restriking does occur.

The higher currents associated with capacitor banks of comparable kvar at voltages below 115 kv make necessary such powerful pumps that the loads on the operating mechanisms are prohibitive. For this reason with breakers rated 69 kv and below, two-step switching where a resistance is inserted in the circuit is preferable to a design using pumps. The use of a resistor switched into a circuit during interruption to reduce switching surge overvoltage on a system has been common for many years; however, unless the interrupter is effective on the second step as well as the first, the resistor current must be very low and have a fairly high power factor. Consequently, the resistor must have an impedance about four times the single phase capacitive reactance. Tests show that such a relatively high value of resistance used with capacitor switching lowers the probability of restriking but does not reduce the overvoltage by much more than 15 to 25%. Lower values of resistance can be used with an interrupter that is effective on the arcs of both steps of the interruption and superior performance is obtained. Figure 2 shows schematically how well the "De-ion" grid is suited to this type of two-step switching.

how resistance switching works

The grid is separated into two sections by a probe which makes physical contact with the moving contact and is connected through a resistor, R, to the stationary contact. The arc drawn initially (at time T_0 in figure 3) in the upper portion of the grid figure 2-A is in parallel with the resistor. Its extinction, at time T_1 in figure 3, inserts the resistance into the circuit. By using a low value of resistance, in the neighborhood of only half the capacitive reactance, the step of inserting the resistance becomes easy. The current is changed very little from its normal magnitude, during time T1 to T2 in figure 3 and the voltage drop across the resistor, and consequently the first gap is small. The voltage across the first gap being small, restriking at this time is rare; however, should restriking occur as shown at time T2, in figure 3, the current and voltage amplitude is low. The arc in the first gap is shown extinguished at time T3 and the current path transferred through the resistor for the period T3 to T4 at which time the current is interrupted.

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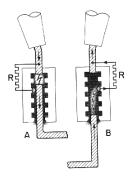


fig. 2:

Schematic drawing through breaker pole unitshowing contacts, interrupters, and resistors (R) at:

A. 1st step of interruption; and B. 2nd step of interruption. restrikes in lower half of grid: Should a restrike occur after the moving contact has withdrawn from the probe contact as shown in figure 2B and at time T₅ in figure 3, the resistor is in series with the bank and the transient current is hypercritically damped as shown from T₅ to T₆. The capacitor voltage equalizes with the system voltage. The transient voltage is not oscillatory and cannot rise above the normal rated frequency peak value.

design of the resistor: Analysis shows that in order to keep overvoltages to a minimum in the event of a restrike during the first step of interruption, the lower the resistance the better, because the bank voltage will then more nearly follow the system voltage, but in the event of a restrike (through the resistor) during the second step of interruption, the greater the resistance the better for minimizing the transient overvoltages.

The optimum condition to limit the transient overvoltage for restrikes in both steps is obtained when the resistance is about half the capacitive reactance, $R/X_{\rm C}\!=\!0.5$. This does not, however, mean that a different resistor must be used for each size of bank. Tests have shown that excellent results are obtained by using one value of resistance for bank sizes from 2,000 to 8,000 kvar, and another for banks of 5,000 to 20,000 kvar. A third resistor size might be used for larger banks. This ratio $R/X_{\rm C}$ is maintained at a value less than one for best results.

When the ratio R/XC is maintained at a value between ½ and l, not only is the problem of system switching solved, but also the stress on the breaker itself is controlled because the impedance of the resistor, being independent of frequency, very effectively limits the surge current on closing the breaker to energize the capacitor bank, or on a restrike during an opening operation.

Thus a breaker designed for two-step switching using low values of resistance has a very low probability of restriking during the first step operation because the voltage on the gap is limited to the voltage across the resistor. If a restrike should occur the transient surge currents and overvoltages will be low. If a restrike occurs during the second step of operation the low value of resistance limits the surge currents and prevents any oscillation and any overvoltage.

resistor switching grid details



grid assembly showing resistor probe connections



two-step switching

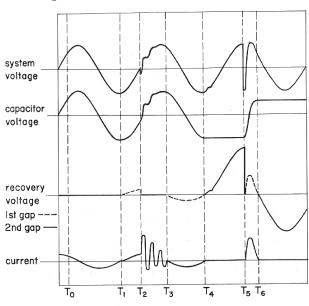


fig. 3: Opening the circuit to a capacitor in two steps. Approximately the maximum effects of one restrike while inserting the resistor and one restrike while opening the circuit through the resistor are shown. No overvoltages appear on the bus.

The resistor is also an aid to the interruption of fault currents which may occur within the capacitor bank. On insertion of the resistor, being in parallel with the first gap, only a very low rate of rise of transient recovery voltage appears across the first gap which in turn results in a low probability of restrike in the first gap. Consequently, insertion of the low value resistor into the circuit is facilitated by the resistor itself. The second gap then has only the resulting reduced fault current to interrupt. The interruption of fault current differs from the interruption of the capacitive circuit principally in the larger current drawn in the first gap and the greater amount of ionized gas which makes the insertion of the resistor more difficult.

The resistance method of capacitor switching has been applied to the three tank outdoor oil breakers between 14.4 and 69 kv, inclusive, from 500 through 2500 mva interrupting capacity.

In all cases, resistor grid designs have been thoroughly tested and verified both as to capacitor switching and power interrupting capabilities in the Westinghouse High Power Laboratory.



detail of finger box (probe)

typical resistor assembly with insulating sleeve removed





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capacitor switching for power circuit breakers

application

circuit breaker				maximum size of				recommended modification	literature reference	
type	ratings			capacitor bank: kvar				to breaker	2020201100	
	kv	amps	mva	isolated		parallel				
type CA comp	ressed a	ir circuit b	reaker					1		
150 CA1000 150 CA1000 150 CA1500 150 CA1500 150 CA2500 345 CA1500 345 CA2500 345 CA2500	14.4 14.4 14.4 14.4 14.4 34.5 34.5 34.5	1200 3000 2000 4000 5000 1200 3000 3000	1000 1000 1500 1500 2500 1500 1500 2500	22000 45000 37000		22000 45000 37000 refer to Westinghouse refer to Westinghouse refer to Westinghouse refer to Westinghouse refer to Westinghouse		none none none	descriptive bulletin 33-450	
type SF-6 sulf	ur-hexaf	luoride bre	akers						1	
345 SF 500 460 SF 500	34.5 46 69 kv to 3	1200 1200 345 k v	500 500	50000 50000		50000 50000 refer to Westinghouse		none none	descriptive bulletin 33-551	
type DH De-io	n® air c	ircuit brea	kers							
50DH75 50DH150E 50DH250E	4.16 4.16 4.16	1200 1200, 2000 1200, 2000	75 150 250	at 2400 v 3600 3600 3600	at 4160 v 6400 6400 6400	at 2400 v \$3600 \$3600 \$3600	at 4160 v \$6400 \$6400 \$6400	none none none	22.056	
150 DH 150 E 150 DH 250 E 150 DH 500 E 150 DH 750 E 150 DH 1000 E	13.8 13.8 13.8 13.8 13.8	1200, 2000 1200, 2000 1200, 2000 1200, 2000 1200, 3000	150 250 500 750 1000	21	1600 1600 1600 1600 1600	20000 A 20000 A 20000 A 20000 A 20000 A		none none none none	descripti ve bulletin 32-250	
indoor oil cire	cuit brea	kers+								
F-122 F-124A	4.16 7.2	600 600	25 50	3200 5400		not recomm		nended	descriptive bulletin 33-150 descriptive bulletin 33-151	
F-100	7.2	1200 600 1200	50 100 100	6000 □ 5400 6000 □		not recomm 4800\$ 4800\$ △		reinforced grid reinforced grid	descriptive bulletin 33-152	
138F150	13.8	600 1200	150 150	6000 6000 □		4800 ° 4800 ° △		reinforced grid reinforced grid	descriptive bulletin 33-152	
138B250 138B500	13.8 13.8	1200 1200 1200	250 500	6000 ☐ 6000 ☐		1	4800 \(\triangle \) reinf \(4800 \) \(\triangle \) reinf		descriptive bulletin 33-152 descriptive bulletin 33-152	
outdoor oil ci	rcuit bre	akers+	e e e e e e e e e e e e e e e e e e e						1 1 1 1 1 1 00 051	
144 G C100 144 G C250	14.4 14.4	600 600	100 250	6000 6000		2700 2700 2700			descriptive bulletin 33-251 descriptive bulletin 33-251	
144 G C 500	14.4	1200 600 1200	250 500 500	6000 □ 6000 □		27003 27003 27003			descriptive bulletin 33-251	
144 G1000 144 G1500	14.4 14.4	1200 3000, 4000	1000 1500	22000* 30000*			22000* 30000*	resistor grid resistor grid	descriptive bulletin 33-252 descriptive bulletin 33-252	
230 G C 250	23	600	250	6000			2700	reinforced grid	descriptive bulletin 33-251	
230 G500	23	1200	500	30000*		30000*		resistor grid	descriptive bulletin 33-252	
345 G500 345 G1000 345 G1500 345 G2500	34.5 34.5 34.5 34.5	1200 1200 1200 1200	500 1000 1500 2500	3000* 3000* 3000* 3000*		3	80000* 80000* 80000* 80000*	resistor grid resistor grid resistor grid resistor grid	descriptive bulletin 33-252 descriptive bulletin 33-252 descriptive bulletin 33-252 descriptive bulletin 33-252	
460 G500 460 G1000	46 46	1200 1200	500 1000	30000* 30000*		30000* 30000*		resistor grid resistor grid	descriptive bulletin 33-252 descriptive bulletin 33-252	
690 G1000 690 G1500 690 G2500	69 69 69	1200 1200 1200	1000 1500 2500	30000* 30000* 30000*		30000* 30000* 30000*		resistor grid resistor grid resistor grid	descriptive bulletin 33-252 descriptive bulletin 33-252 descriptive bulletin 33-252	
GM GW	69 to 16 230 to 3	61 k v 45 k v			refer to Westinghouse. refer to Westinghouse.					

The continuous current rating of the breaker may further limit the capacitor bank size, particularly in applications at reduced voltage. Breaker current rating must exceed the nominal current of the capacitor load by 35%.

Parallel banks equal in size to the isolated bank value may be employed in multiple provided they are effectively isolated with inductance to lower the amplitude and frequency of the interbank transient current. For such applications refer to Westinghouse.

When employed with an isolated bank at reduced voltage (E <7.2 kv), the maximum bank size shall be determined by the following formula if not limited otherwise by the current rating (see note 1):

KVAR =6000 - 500 (7.2—E)

When employed with parallel banks at reduced voltage (4.8 kv <E <7.2 kv), the maximum size shall be determined by the following formula if not limited otherwise by the current rating (see note 1):

KVAR =4800 - 500 (7.2—E)

For voltages less than 4.8 kv the maximum bank size is 3600 kvar if not limited otherwise by the current rating.

The total capacity of the load bank and all parallel banks on the source side is not to exceed two times the value listed.

The total capacity of parallel banks not to exceed 40,000 kvar and the inductance between switched banks must be at least 25 micro-henries per phase.

further information: Refer to Westinghouse.