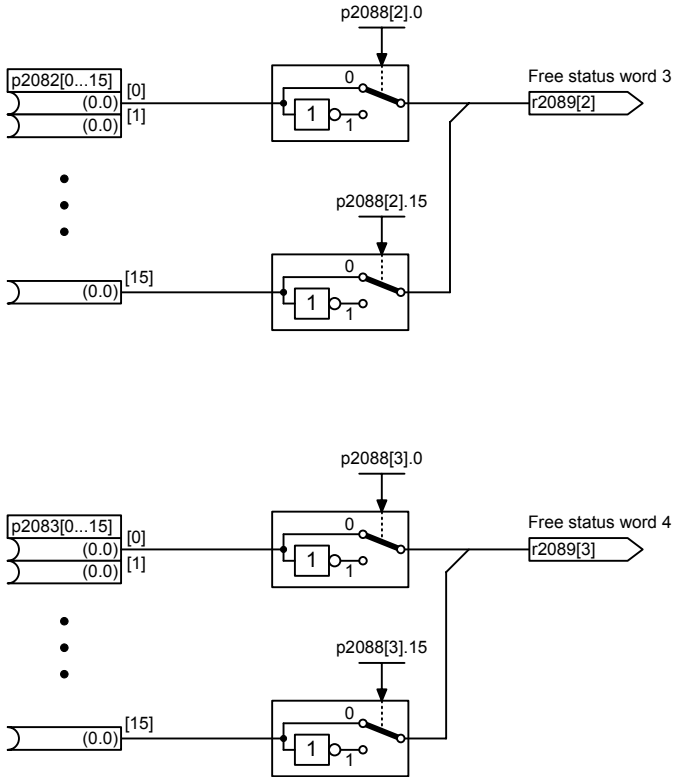
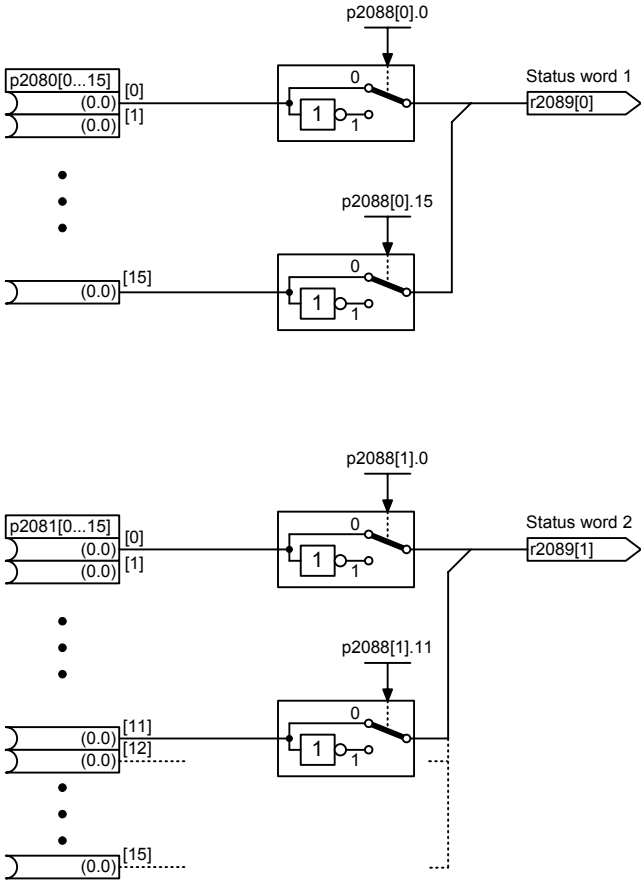


4 binector-connector converter



<1> For clock-cycle synchronous VECTORGL/VECTORMV, these signals may not be interconnected (slave sign-of-life).

1	2	3	4	5	6	7	8
DO: All objects (not B_INFMV)					fp_2472_53_eng.vsd	Function diagram	- 2472 -
PROFIdrive - Status words, free interconnection					04.08.06 V02.05.00	SINAMICS GM/SM/GL	